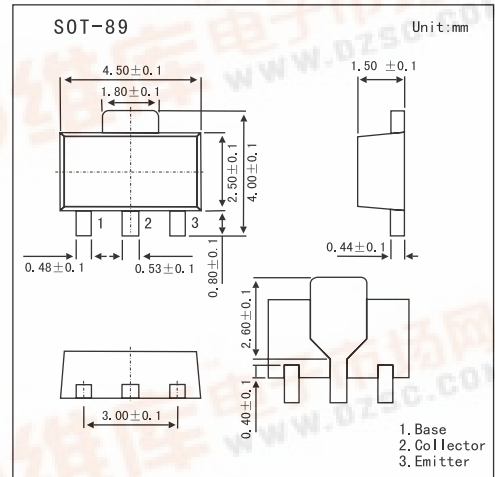


SMD Type Transistors

NPN Epitaxial Planar Silicon Transistor  
2SD1997

Features

- Contains diode between collector and emitter.
- Low saturation voltage.
- Large current capacity.
- Small-sized package making it easy to provide highdensity, small-sized hybrid ICs.



Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V <sub>CB0</sub>	40	V
Collector-emitter voltage	V <sub>CEO</sub>	30	V
Emitter-base voltage	V <sub>EBO</sub>	6	V
Collector current	I <sub>C</sub>	3	A
Collector current (pulse)	I <sub>CP</sub>	5	A
Collector dissipation	P <sub>C</sub>	1.5	W
Jumction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector cutoff current	I <sub>cBO</sub>	V <sub>CB</sub> = 30V, I <sub>E</sub> = 0			1.0	μA
DC current Gain	h <sub>FE</sub>	V <sub>CE</sub> = 2V, I <sub>C</sub> = 0.5A	70			
		V <sub>CE</sub> = 2V, I <sub>C</sub> = 2A	50			
Gain bandwidth product	f <sub>T</sub>	V <sub>CE</sub> = 2V, I <sub>C</sub> = 0.5A		100		MHz
Output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 10V, f = 1MHz		40		pF
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 1A, I <sub>B</sub> = 50mA		0.12	0.3	V
Base-emitter on state voltage	V <sub>BE(ON)</sub>	I <sub>CE</sub> = 2V, I <sub>C</sub> = 1A	1	2	5	V
Collector-to-base breakdown voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> = 10μA, I <sub>E</sub> = 0	40			V
Collector-to-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 10μA, R <sub>BE</sub> = ∞	40			V
		I <sub>C</sub> = 10mA, R <sub>BE</sub> = ∞	3			
Diode forward voltage	V <sub>F</sub>	I <sub>F</sub> = 0.5A			1.5	V
Base-emitter resistance	R <sub>BE</sub>			0.8		kΩ
Base resistance	R <sub>1</sub>		120	160	200	Ω

Marking

Marking	DO
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