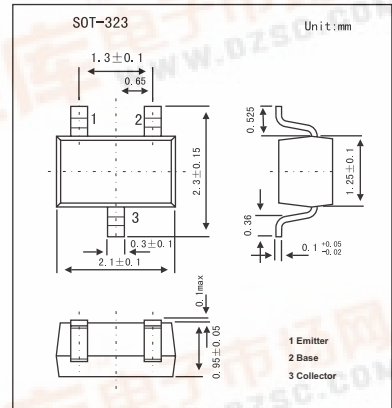
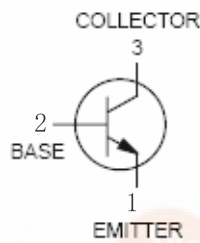


SMD Type Transistors

Switching Transistor
MMBT4403W

■ Features

- Switching transistor.
- PNP Silicon.



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-emitter voltage	V _{CEO}	-40	V
Collector-base voltage	V _{CBO}	-40	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current	I _c	-600	mA
Total Device Dissipation FR-5 Board	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	R _{θJA}	833	°C/W
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

MMBT4403W

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Collector-emitter breakdown voltage *	V _{(BR)CEO}	I _C = -1.0 mA, I _B = 0	-40			V
Collector-base breakdown voltage	V _{(BR)CBO}	I _C = -0.1 mA, I _E = 0	-40			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E = -0.1 mA, I _C = 0	-5			V
Base cutoff current	I _{BEV}	V _{CE} = -35 V, V _{EB} = -0.4 V			-0.1	μA
Collector cutoff current	I _{CEx}	V _{CE} = -35 V, V _{EB} = -0.4 V			-0.1	μA
DC current gain	HFE	I _C = -0.1 mA, V _{CE} = -1.0 V I _C = -1.0 mA, V _{CE} = -1.0 V I _C = -10 mA, V _{CE} = -1.0 V I _C = -150 mA, V _{CE} = -2.0 V * I _C = -500 mA, V _{CE} = -2.0 V *	30 60 100 100 20		300	
Collector-emitter saturation voltage *	V _{CE(sat)}	I _C = -150 mA, I _B = -15 mA I _C = -500 mA, I _B = -50 mA			-0.4 -0.75	V
Base-emitter saturation voltage *	V _{BE(sat)}	I _C = -150 mA, I _B = -15 mA I _C = -500 mA, I _B = -50 mA	-0.75		-0.95 -1.3	
Current-gain-bandwidth product	f _T	I _C = -20 mA, V _{CE} = -10 V, f = 100 MHz	200			MHz
Collector-base capacitance	C _{cb}	V _{CB} = -10 V, I _E = 0, f = 1.0 MHz			8.5	pF
Emitter-base capacitance	C _{eb}	V _{BE} = -0.5 V, I _C = 0, f = 1.0 MHz			30	pF
Input impedance	h _{ie}	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	1.5		15	kΩ
Voltage feedback ratio	h _{re}	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	0.1		8.0	× 10 ⁻⁴
Small-signal current gain	h _{fe}	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	60		500	
Output admittance	h _{oe}	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	1.0		100	μmhos
Delay time	t _d	V _{CC} = -30 V, V _{EB} = -2.0 V,			15	ns
Rise time	t _r	I _C = -150 mA, I _{B1} = -15 mA			20	ns
Storage time	t _s	V _{CC} = -30 V, I _C = -150 mA,			225	ns
Fall time	t _f	I _{B1} = I _{B2} = -15 mA			30	ns

* Pulse test: pulse width ≤ 300 μs, duty cycle ≤ 2.0%.

■ Marking

Marking	2T
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