

SMD Type

Transistors

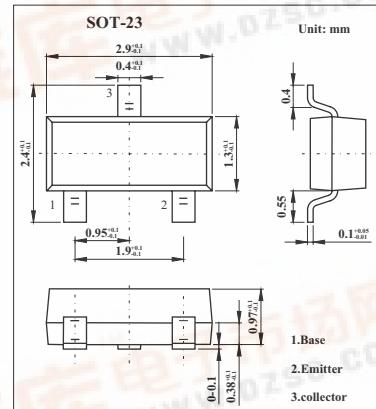
NPN Epitaxial Planar Silicon Transistors

2SC5310



■ Features

- Adoption of FBET, MBIT processes.
- Large current capacitance.
- Low collector-to-emitter saturation voltage.
- High-speed switching.
- Ultrasmall package facilitates miniaturization in end products.



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	30	V
Collector-emitter voltage	V _{CEO}	25	V
Emitter-base voltage	V _{EBO}	6	V
Collector current	I _C	1	A
Collector current (pulse)	I _{CP}	3	A
Base current	I _B	200	mA
Collector dissipation *	P _C	250	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

* Mounted on a glass-epoxy board (20□30□1.6mm)

2SC5310■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 20\text{V}$, $I_E = 0$			0.1	μA
Emitter cutoff current	I_{EBO}	$V_{EB} = 3\text{V}$, $I_C = 0$			0.1	μA
DC current gain	h_{FE}	$V_{CE} = 2\text{V}$, $I_C = 100\text{mA}$	135		400	
Gain bandwidth product	f_T	$V_{CE} = 10\text{V}$, $I_C = 50\text{mA}$		150		MHz
Output capacitance	C_{ob}	$V_{CB} = 10\text{V}$, $f = 1.0\text{MHz}$		19		pF
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$I_C = 500\text{mA}$, $I_B = 25\text{mA}$		100	200	mV
Base-emitter saturation voltage	$V_{BE(\text{sat})}$	$I_C = 500\text{mA}$, $I_B = 25\text{mA}$		0.85	1.2	V
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = 10\mu\text{A}$, $I_E = 0$	30			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 1\text{mA}$, $R_{BE} = \infty$	25			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = 10\mu\text{A}$, $I_C = 0$	6			V
Turn-on time	t_{on}	<p>$PW=20\text{ns}$ D.C.≤1%</p> <p>$V_{BE}=5\text{V}$</p> <p>$20 B_1 -20 B_2 =I_C=500\text{mA}$ (For PNP, the polarity is reversed.)</p>		60		ns
Storage time	t_{stg}			500		ns
Fall time	t_f			25		ns

■ hFE Classification

Marking	NN	
Rank	5	6
hFE	135~270	200~400