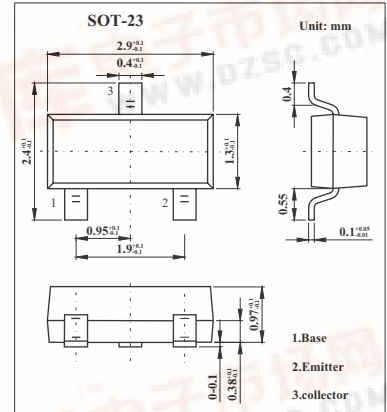


SMD Type Transistors

NPN Transistors  
KMBT3904(MMBT3904)

■ Features

- Epitaxial planar die construction



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector - Base Voltage	V <sub>CB0</sub>	60	V
Collector - Emitter Voltage	V <sub>CEO</sub>	40	V
Emitter - Base Voltage	V <sub>EBO</sub>	6	V
Collector Current - Continuous	I <sub>C</sub>	0.2	A
Collector Power Dissipation	P <sub>C</sub>	0.2	W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to 150	°C

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Collecto- base breakdown voltage	V <sub>CB0</sub>	I <sub>C</sub> = 100 μA, I <sub>E</sub> =0	60			V
Collector- emitter breakdown voltage	V <sub>CEO</sub>	I <sub>C</sub> = 1 mA, I <sub>B</sub> =0	40			V
Emitter - base breakdown voltage	V <sub>EBO</sub>	I <sub>E</sub> = 10 μA, I <sub>C</sub> =0	6			V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 60 V, I <sub>E</sub> =0			0.1	μA
Collector cut-off current	I <sub>CEO</sub>	V <sub>CE</sub> = 30 V, V <sub>BE(off)</sub> =3V			50	nA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 5V, I <sub>C</sub> =0			0.1	μA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 10mA	100		400	
		V <sub>CE</sub> = 1V, I <sub>C</sub> = 50mA	60			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =50 mA, I <sub>B</sub> = 5mA			0.3	V
Base - emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = 50 mA, I <sub>B</sub> = 5mA			0.95	V
Delay time	t <sub>d</sub>	V <sub>CC</sub> =3.0V, V <sub>BE</sub> =-0.5V			35	ns
Rise time	t <sub>r</sub>	I <sub>C</sub> =10mA, I <sub>B1</sub> =-I <sub>B2</sub> =1.0mA			35	
Storage time	t <sub>s</sub>	V <sub>CC</sub> =3.0V, I <sub>C</sub> =10mA			200	ns
Fall time	t <sub>f</sub>	I <sub>B1</sub> =-I <sub>B2</sub> =1.0mA			50	
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 10mA, f=100MHz	250			MHz

■ Marking

Marking	1AM
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### KMBT3904(MMBT3904)

■ Typical Characteristics

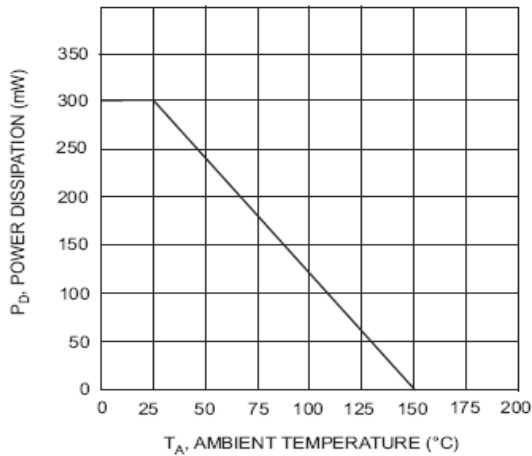


Fig.1 Max Power Dissipation vs Ambient Temperature

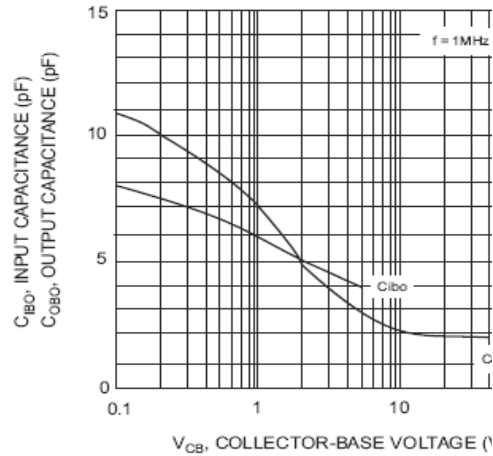


Fig.2 Input and Output Capacitance vs. Collector-Base Voltage

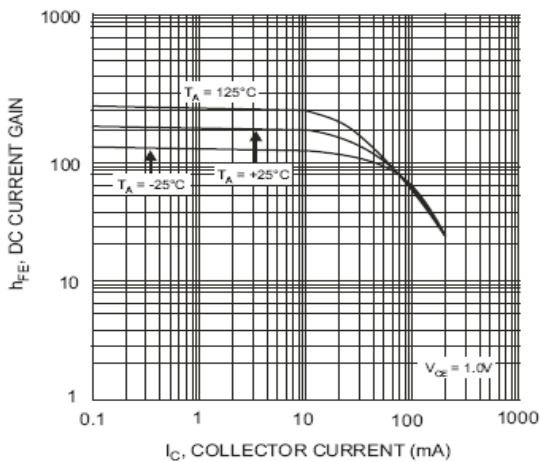


Fig.3 Typical DC Current Gain vs Collector Current

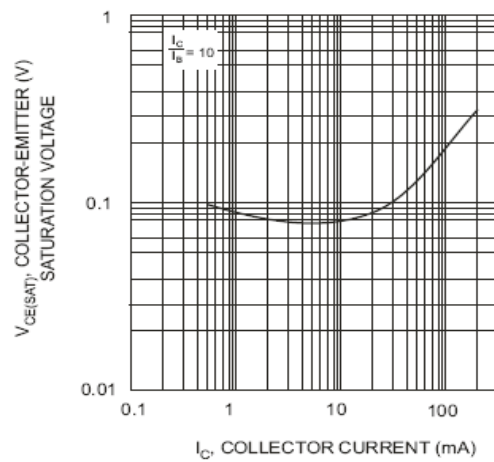


Fig.4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

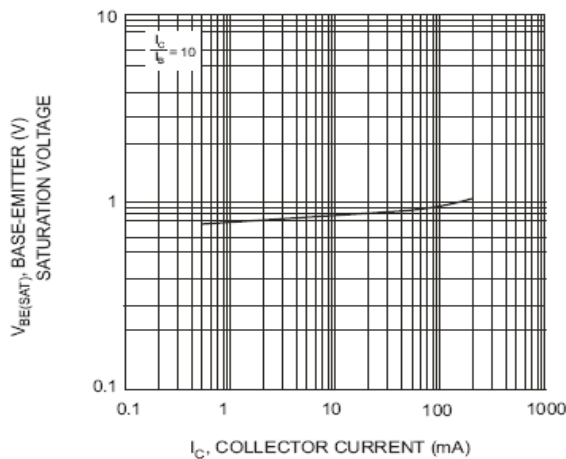


Fig.5 Typical Base-Emitter Saturation Voltage vs. Collector Current