

SMD Type

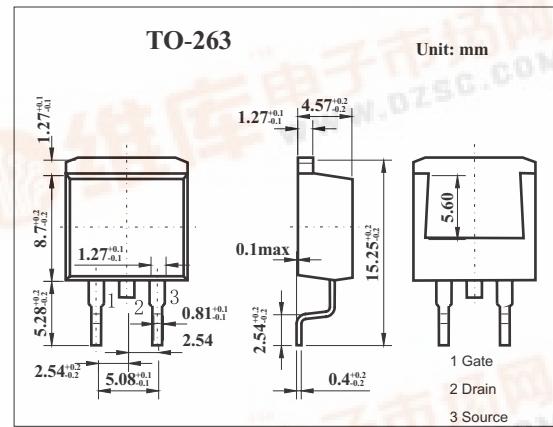
MOSFET

## MOS Field Effect Transistor 2SK3299



### ■ Features

- Low gate charge  
 $Q_G = 34 \text{ nC TYP. } (V_{DD} = 450 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A})$
- Gate voltage rating  $\pm 30 \text{ V}$
- Low on-state resistance  
 $R_{DS(on)} = 0.75 \Omega \text{ MAX. } (V_{GS} = 10 \text{ V}, I_D = 5.0 \text{ A})$
- Avalanche capability ratings
- Surface mount package available



### ■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain to source voltage	$V_{DSS}$	600	V
Gate to source voltage	$V_{GSS}$	$\pm 30$	V
Drain current	$I_D$	$\pm 10$	A
	$I_{Dp}^*$	$\pm 40$	A
Power dissipation $T_A=25^\circ\text{C}$ $T_C=25^\circ\text{C}$	$P_D$	1.5	W
		75	
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\*  $PW \leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$

### ■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain cut-off current	$I_{DSS}$	$V_{DS}=600\text{V}, V_{GS}=0$			100	$\mu\text{A}$
Gate leakage current	$I_{GSS}$	$V_{GS}=\pm 30\text{V}, V_{DS}=0$			$\pm 100$	$\mu\text{A}$
Gat cutoff voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	2.5		3.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=10\text{V}, I_D=5.0\text{A}$	3.2			S
Drain to source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=5.0\text{A}$		0.68	0.75	$\Omega$
Input capacitance	$C_{iss}$	$V_{DS}=10\text{V}, V_{GS}=0, f=1\text{MHz}$		1580		pF
Output capacitance	$C_{oss}$			280		pF
Reverse transfer capacitance	$C_{rss}$			25		pF
Turn-on delay time	$t_{on}$	$I_D=5.0\text{A}, V_{GS(on)}=10\text{V}, R_G=10\Omega, V_{DD}=150\text{V}$		27		ns
Rise time	$t_r$			17		ns
Turn-off delay time	$t_{off}$			66		ns
Fall time	$t_f$			24		ns