

SMD Type

MOSFET

MOS Field Effect Transistor
2SK3482

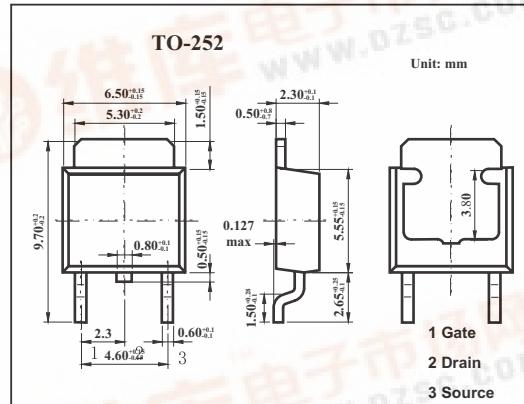
■ Features

- Super low on-state resistance:

$R_{DS(on)1} = 33m\Omega$ MAX. ($V_{GS} = 10V$, $I_D = 18A$)

$R_{DS(on)2} = 39 m\Omega$ MAX. ($V_{GS} = 4.5V$, $I_D = 18A$)

- Low C_{iss} : $C_{iss} = 3600 pF$ TYP.

■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	± 36	A
	I_{Dp}^*	± 100	A
Power dissipation $T_c=25^\circ C$ $T_A=25^\circ C$	P_D	50	W
		1.0	
Channel temperature	T_{ch}	150	$^\circ C$
Storage temperature	T_{stg}	-55 to +150	$^\circ C$

* $PW \leqslant 10 \mu s$, Duty Cycle $\leqslant 1\%$

■ Electrical Characteristics $T_a = 25^\circ C$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	I_{DSS}	$V_{DS}=100V, V_{GS}=0$			10	μA
Gate leakage current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0$			± 10	μA
Gat cutoff voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	1.5	2.0	2.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=10V, I_D=18A$	12	23		S
Drain to source on-state resistance	$R_{DS(on)1}$	$V_{GS}=10V, I_D=18A$			27	$m\Omega$
	$R_{DS(on)2}$	$V_{GS}=4.5V, I_D=18A$			29	$m\Omega$
Input capacitance	C_{iss}	$V_{DS}=10V, V_{GS}=0, f=1MHz$			3600	pF
Output capacitance	C_{oss}				360	pF
Reverse transfer capacitance	C_{rss}				190	pF
Turn-on delay time	t_{on}	$I_D=18A, V_{GS(on)}=10V, R_G=0\Omega, V_{DD}=50V$			15	ns
Rise time	t_r				10	ns
Turn-off delay time	t_{off}				68	ns
Fall time	t_f				6	ns
Total Gate Charge	Q_G	$I_D = 36A, V_{DD} = 80V, V_{GS} = 10V$			72	nC
Gate to Source Charge	Q_{GS}				10	nC
Gate to Drain Charge	Q_{GD}				19	nC