A STATE WWW

Embedded Power for Business-Critical Continuity

PN

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DS1200 1200 Watts

Distributed Power System

Distributed Power Bulk Front-End Total Output Power: 1200 Watts +3.3 Vdc Stand-by Output Wide Range Input Voltage: 90 - 264 Vac

Special Features

- Active power factor correction
- EN61000-3-2 harmonic compliance
- Active AC inrush control
- 1U X 2U form factor
- 21.71 W / in³
- +12 Vdc Output
 +3.3 Vdc stand-by (5 V standby - consult factory)
- No minimum load requiredHot plug operation
- Hot plug operation
 N + 1 redundant
- Internal OR'ing fets
- Active current sharing (10 - 100% load)
- Built-in cooling fan (40 mm x 28 mm)
- I²C communication interface bus
- PMBus compliant
- EERPOM for FRU data
- Red/green bi-color LED status
- Internal fan speed control
- Fan Fail Tach Output SignalINTEL, SSI Std. logic timing
- INTEL, SSI Std. FRU data format
- Full digital control
- One year warranty

Safety

- UL CUL 60950 (UL Recognized)
- CEMark

Electrical Specifications

| Input | | | | | |
|------------------------|--|--|--|--|--|
| Input range: | 180 - 264 (1200 W) 90 - 264 (1000 W) | | | | |
| Frequency: | 47-63 Hz, single phase AC | | | | |
| Inrush current: | 40 Apk maximum inrush current | | | | |
| Efficiency: | 40 Apk maximum inrush current > 91% typical at high line 50% load | | | | |
| Conducted EMI: | FCC Subpart J EN55022 Class B | | | | |
| Radiated EMI: | FCC Subpart J EN55022 Class B | | | | |
| Power factor: | 0.99 typical | | | | |
| Leakage current: | 1.40 mA @ 240 VAC | | | | |
| Hold up time: | 12 ms minimum | | | | |
| Output | | | | | |
| Main DC voltage: | +12 V @ 100 A (high line) +12 V @ 81.6 A (low line) | | | | |
| Stand-By: | +12 V @ 81.6 A (low line) +3.3 Vsb @ 6 A (5 V @ 4 A available) ±5% on +12V only using I ² C | | | | |
| Adjustment range: | ±5% on +12V only using I ² C | | | | |
| Regulation: | +12 Vdc; +5% / -5% | | | | |
| Over current: | +3.3 Vsb; +5% / -5% +12 Vdc; latches off if overcurrent lasts over 1 second, | | | | |
| J TP JOOM | otherwise it is auto recovery (See Table 1 next page) +3.3 Vsb, 9 A max (hiccup mode) | | | | |
| Over voltage: | +12 Vdc; 13.2 - 14.4 Vdc | | | | |
| Under voltage: | +3.3 Vsb; 3.76 - 4.30 Vdc +12 Vdc; 9 - 10.8 V (latch off) | | | | |
| Turn-on delay: | 2 second max, 5 - 50 mS, monotonic rise | | | | |
| Main output rise time: | 5 - 50 mS, monotonic rise | | | | |



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| Logic Control | |
|--------------------------|--|
| PS_SEATED (A4): | TTL logic LOW if power supply is seated into system connector. This is a short pin. A logic HIGH if the PSU is removed |
| PWR GOOD (C3): | Active TTL high when output is within regulation limits. |
| AC OK (B1): | A low logic level if the input voltage is within allowable limits. A TTL logic HIGH level, and a 5mS early warning signal before 12.0 V DC output loss of regulation. |
| PS_INHIBIT/PS_KILL (B4): | This signal is connected to a short pin on the PSU When left open power supply operation will be inhibited. When the power supply is inserted into the system, this pin will be pull low by the system and turn the power supply on only after all other power supply pins have seated. |
| PS ON (A1): | The output will be enabled when this signal is pulled low, below 0.8 V outputs disabled when pin is driven high or left open. |

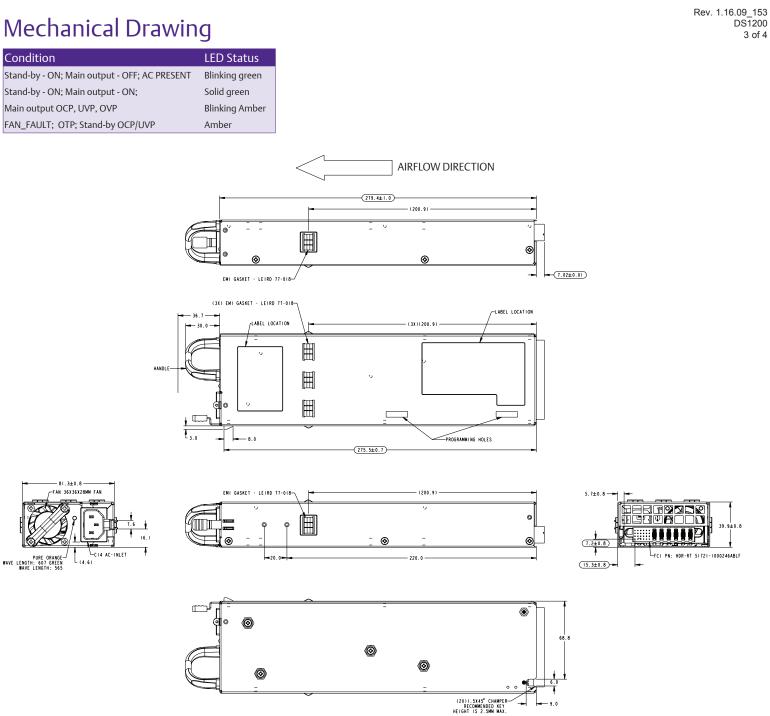
Environmental Specifications

| = | |
|---|--|
| Operating temperature: | -10° to 50 °C ; 50% power derating at 70 °C |
| Storage temperature: | -40 °C to +85 °C |
| Altitude, operating: | 10,000 ft |
| Electromagnetic susceptibility / Input transients: RoHS & lead-free compliant: | -EN61000-3-2, -3-3 -EN61000-4-2, 4.3, 4-4, -4-5, 4-11 Level -EN55024:1998 No tantalum caps. |
| Humidity: Shock and vibration specificatons: | 20 to 90% RH, non-condensing Complies with Astec Std. Specifications, Q3205 |
| MTBF (Demonstrated): | 500K Hrs at full load, 40 °C |

| Ordering Information | | | | | | | | |
|----------------------|---|-------|---------------------|------------------------------------|-------|-------------------------|------------------|-------------|
| Model Number | el Number Nominal Output Set Point Voltage Set Point Tolerance | | Total Regulation | Minimum Maximum Current Current | | Output Ripple P/P | Over Current | Stand-by |
| DS1200-3 | 12.0 Vdc | ±0.2% | ±5% | 0 A | 100 A | 120 mV | 118 A - 147.6 A* | 3.3 V @ 6 A |
| DS1200-3-002 | 12.0 Vdc | ±0.2% | ±5% | 0 A | 100 A | 120 mV | 118 A - 147.6 A* | 5.0 V @ 4 A |

 * Over current latches off if overcurrent lasts over 1 seconds, otherwise it is auto recovery.

Embedded Power for **Business-Critical Continuity**



Stand-by - ON; Main output - OFF; AC PRESENT Stand-by - ON; Main output - ON; Main output OCP, UVP, OVP

DC Output Connector Pinout Assignment

Male connector as viewed from the rear of the supply:

| D1 | D2 | D3 | D4 | D5 | D6 | | | | | | |
|----|----|----|----|----|----|-----|-----|-----|-----|-----|-----|
| C1 | C2 | C3 | C4 | C5 | C6 | PB1 | רסס | כסס | | DDE | |
| B1 | B2 | B3 | B4 | B5 | B6 | РВТ | PBZ | PB3 | PB4 | РБЭ | РВО |
| A1 | A2 | A3 | A4 | A5 | A6 | | | | | | |

P1 - Power Supply Side FCI Power Blade 51721 series 1

51721-10002406AA 2. Molex Power Connector SD-87667 series 87667-7002

Mating Connector (System Side)

| | 5 | × 3 | , |
|----|----------|-------------|---|
| 1. | FCI Powe | er Blade | |
| | 51741-1 | 0002406CC | |
| | Straight | Pins | |
| 2. | FCI Powe | er Blade | |
| | 51761-1 | 0002406AALF | |
| | Right An | gle | |

Pin Assignments

| Pin | Signal Name | | | | |
|----------|--|--|--|--|--|
| PB 1 | Main output return | | | | |
| PB 2 | Main output return | | | | |
| PB 3 | Main output return | | | | |
| PB 4 | + Main output | | | | |
| PB 5 | + Main output | | | | |
| PB 6 | + Main output | | | | |
| A1 | PS_ON | | | | |
| A2 | Main output remote sense return | | | | |
| A3 | Spare | | | | |
| A4 | PS_SEATED (Power Supply Seated) | | | | |
| A5 | STAND-BY | | | | |
| A6 | STAND-BY RETURN | | | | |
| B1 | AC_OK (AC Input Present) | | | | |
| B2 | Main output remote sense | | | | |
| B3 | Main output current share | | | | |
| B4 | PS_INHIBIT / PS_Kill | | | | |
| B5 B6 | STAND-BY STAND-BY RETURN | | | | |
| | | | | | |
| C1 | SDA (I ² C Data Signal) | | | | |
| C2 | SDA (I²C Clock Signal) | | | | |
| C3 | POWER GOOD | | | | |
| C4 | Spare | | | | |
| C5 | STAND-BY | | | | |
| C6 | STAND-BY RETURN | | | | |
| D1 | A0 (I ² C Address BIT 0 Signal) | | | | |
| D2 | A1 (I ² C Address BIT 1 Signal) | | | | |
| D3 | S_INT (Alarm) | | | | |
| D4 | STAND-BY RMT SENSE | | | | |
| D5 | STAND-BY | | | | |
| D6 | STAND-BY RETURN | | | | |
| | | | | | |

Americas

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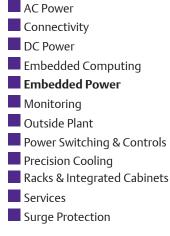
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