





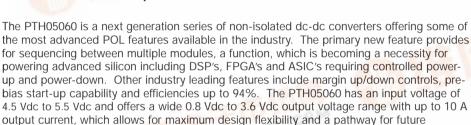
DC-DC CONVERTERS

POLA Non-isolated

NEW Product



- 5 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 3.6 Vdc)
- Auto-track[™] sequencing*
- · Margin up/down controls
- · Pre-bias start-up capability
- Efficiencies up to 94%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant









2 YEAR WARRANTY

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated $C_{\rm in}$ = 330 μ F, $C_{\rm out}$ = 0 μ F

SPECIFICATIONS

OUTPUT SPECIFICATIONS

upgrades.

Voltage adjustability	y (See Note 4)	0.8-3.6 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typ.
Load regulation		±12 mV typ.
Total regulation	am da d	±3.0% Vo
Minimum load	Y WWY	0 A
Ripple and noise	20 MHz bandwidth	25 mV pk-pk
Temperature co-effi	icient -40 °C to +85 °C	±0.5% Vo
Transient response (See Note 5)	Overshoot	70 µs recovery time /undershoot 100 mV
Margin adjustment		±5.0% Vo

INPUT SPECIFICATIONS

Input voltage range

input voltage range	(See Note 3)	4.5-5.5 Vac		
Input current	No load	10 mA typ.		
Remote ON/OFF	(See Note 1)	Positive logic		
Start-up time	M. As	1 V/ms		
Undervoltage lockout		3.7-4.3 V typ.		
Track input voltage	Pin 8 (See Note 6, 7)	±0.3 Vin		

(See Note 3)

EMC CHARACTERISTICS

Electrostatic discharge Conducted immunity EN61000-4-2, IEC801-2 EN61000-4-6 EN61000-4-3

GENERAL SPECIFICATIONS

Efficiency	(See Efficiency Table) 94%		
Insulation voltage		Non-isolated	
Switching frequency	300 kHz typ. ±25 kHz		
Approvals and standards	EN60950 UL/cUL60950		
Material flammability	UL94V-0		
Dimensions	(,	15.75 x 9.00 mm 0.620 x 0.354 in	
Weight		3.7 g (0.13 oz)	
MTBF	Telcordia SR-332	7,092,000 hours	

ENVIRONMENTAL SPECIFICATIONS

Thermal performance (See Note 2)	Operating ambient, temperature Non-operating	-40 °C to +85 °C -40 °C to +125 °C	
MSL	JEDEC J-STD-020C	Level 3	

PROTECTION

15-55 Vdc

Short-circuit Auto reset 20 A typ.



*Auto-track™ is a trade mark of Texas Instruments





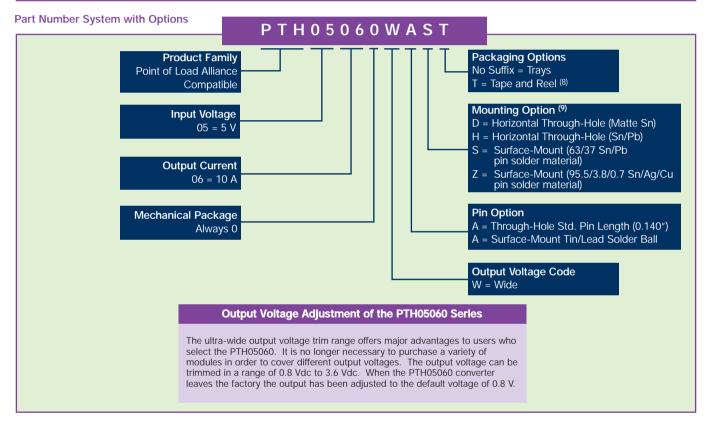


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NEW Product

OUTPUT POWER	INPUT	OUTPUT	OUTPUT CURRENT	OUTPUT CURRENT	EFFICIENCY	REGU	ILATION	MODEL
(MAX.)	VOLTAGE	VOLTAGE	(MIN.)	(MAX.)	(MAX.)	LINE	LOAD	NUMBER ^(9,10)
36 W	4.5-5.5 Vdc	0.8-3.6 Vdc	0 A	10 A	94%	±10 mV	±12 mV	PTH05060



Notes

Remote ON/OFF. Positive Logic Pin 3 open; or V > Vin - 0.5 V Pin 3 GND; or V < 0.8 V (min - 0.2 V). OFF:

See Figures 1 and 2 for safe operating curves.

- A 330 µF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 500 mA rms of ripple current.
- An external output capacitor is not required for basic operation. Adding 330 µF of distributed capacitance at the load will improve the transient
- 1 A/ μ s load step, 50 to 100% I $_{omax}$. C_{out} = 330 μ F. If utilized Vout will track applied voltage by \pm 0.3 V (μ p to Vo set point).
 - The pre-bias start-up feature is not compatible with Auto-Track because when the module is under Auto-Track control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track the function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 159 for more details.
- Tape and reel packaging only available on the surface-mount versions.
- To order Pb-free (RoHS compatible) surface-mount parts replace the mounting option 'S' with 'Z', e.g. PTH05060WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH05060WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable

EFFICIENCY TABLE (I _O = 7 A)				
EFFICIENCY				
85%				
86%				
89%				
90%				
91%				
92%				
94%				







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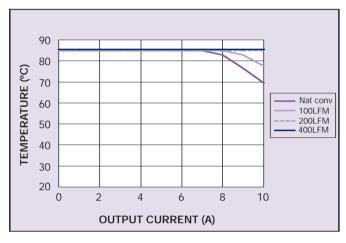
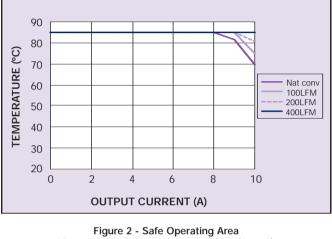


Figure 1 - Safe Operating Area Vin = 5 V, Output Voltage = 3.3 V (See Note A)



Vin = 5 V, Output Voltage = 1.0 V (See Note A)

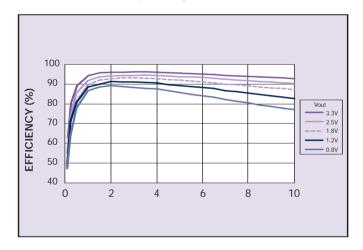


Figure 3 - Efficiency vs Load Current Vin = 5 V (See Note B)

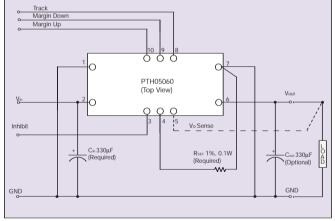


Figure 4 - Standard Application

Notes

- SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.







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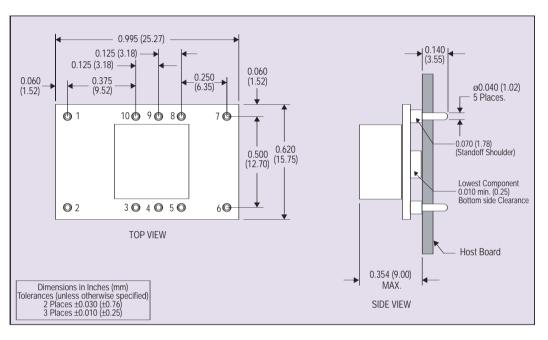
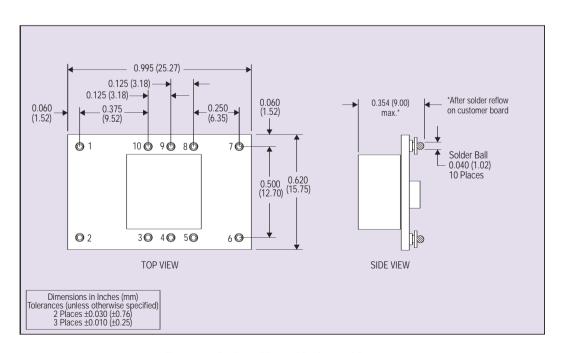


Figure 5 - Plated Through-Hole Mechanical Drawing



PIN CONNECTIONS PIN NO. **FUNCTION** 1 Ground Vin 3 Inhibit* Vo adjust 5 Vo sense Vout 6 7 Ground 8 Track 9 Margin down* 10 Margin up*

*Denotes negative logic: Open = Normal operation Ground = Function active

Figure 6 - Surface-Mount Mechanical Drawing

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