查询AOZ8013供应商



4-Line EMI Filter with Integrated ESD Protection

,24小时加急出货

General Description

The AOZ8013 is an 4-line device integrating EMI filtering with ESD protection for each line. It is designed to suppress unwanted EMI/RFI signals and provide electrostatic discharge (ESD) protection in portable electronic equipment. This state-of-the-art device utilizes AOS leading edge Trench Vertical Structure [TVS]² ™ technology for superior clamping performance and filter attenuation over the full operating display range. The AOZ8013 has been optimized for protection of color LCD displays and CCD camera lines in cellular phones and other portable consumer electronic devices.

The AOZ8013 consists of four identical circuits comprised of TVS diodes for ESD protection, and a resistor–capacitor network for EMI/RFI filtering. A series resistor value of 100³/₄ and a capacitance value of 28pF are used to achieve -35dB minimum attenuation from 800MHz to 2.2GHz. The TVS diodes provide effective suppression of ESD voltages in excess of ±18kV (air discharge) and ±18kV (contact discharge). This exceeds IEC 61000-4-2, level 4 ESD immunity test.

The AOZ8013 comes in an RoHS compliant, 2.0mm x 2.0mm DFN package and is rated over a -40°C to +85°C ambient temperature range.

Features

- 4 lines for EMI filtering and ESD protection:
 - Exceeds IEC 61000-4-2, level 4 (ESD) immunity test
 - $-\pm$ 18kV (air discharge) and \pm 18kV (contact discharge)
- Trench Vertical Structure [TVS]² ™ based technology used to achieve excellent ESD clamping & filter performance over the full operating display range
- Filter performance: -35db attenuation from 800MHz to 2.2GHz
- Low operating voltage: 5.0V

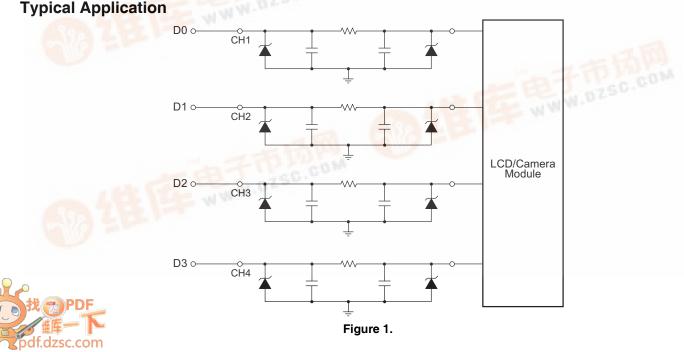
捷多邦,专业PCB打样工厂

- Capacitance stability over wide range of voltages and temperatures
- DFN package, 2.0 x 2.0mm
- Pb-free device
- · Green product

Applications

- EMI filtering and ESD protection for data lines
- LCD displays, camera interface, I/O interface
- Portable handheld devices, cell phones, PDA phones





Poy 1.9 August 2009



Ordering Information

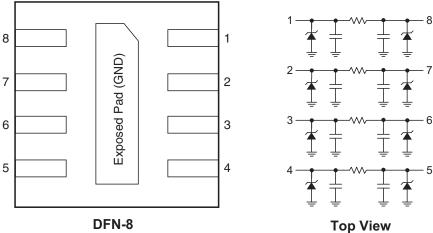
Part Number	Ambient Temperature Range	Package	Environmental
AOZ8013DIL	-40°C to +85°C	DFN-8	RoHS Compliant Green Product



All AOS Products are offering in packaging with Pb-free plating and compliant to RoHS standards.

Parts marked as Green Products (with "L" suffix) used reduced levels of Halogens, and are also RoHS compliant. Please visit www.aosmd.com/web/quality/rohs_compliant.jsp for additional information.

Pin Configuration



(Bottom View)

Pin Description

Pin Number	Pin Name	Pin Function
1, 8	CH 1	Channel 1 Connections
2, 7	CH 2	Channel 2 Connections
3, 6	CH 3	Channel 3 Connections
4, 5	CH 4	Channel 4 Connections
Exposed Pad	GND	Common Ground Connection



Absolute Maximum Ratings

Exceeding the Absolute Maximum ratings may damage the device.

Parameter	Rating				
Storage Temperature (T _S)	-65°C to +150°C				
ESD Rating per IEC61000-4-2, contact ⁽¹⁾	±18kV				
ESD Rating per IEC61000-4-2, air ⁽¹⁾	±18kV				
ESD Rating per Human Body Model ⁽²⁾	±30kV				

Notes:

1. IEC 61000-4-2 discharge with $C_{Discharge} = 150 pF$, $R_{Discharge} = 330$ ³/₄.

2. Human Body Discharge per MIL-STD-883, Method 3015 $C_{\text{Discharge}} = 100 \text{pF}, R_{\text{Discharge}} = 1.5 \text{k}^{34}$.

Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V _{RWM}	Reverse Working Voltage	(3)(8)			5.0	V
V _{BR}	Reverse Breakdown Volt- age	$I_{\rm T} = 1 \rm{m} A^{(4)}$	6	7	8	V
I _R	Reverse Leakage Current	V _{RWM} = 3.3V			0.1	μA
V _{CL}	Signal Clamp Voltage	$I_{LOAD} = 12A$, positive clamp ⁽⁵⁾⁽⁶⁾ $I_{LOAD} = 12A$, negative clamp ⁽⁵⁾⁽⁶⁾		9.5 -8.5		V
R _{CH}	Total Series Resistance	I _R = 20mA	85	100	115	3⁄4
f _C	Cut-off Frequency	Measured with 50¾ source and 50¾ load termination		90		MHz
	Attenuation from 800MHz to 2.2GHz	V _R = 0V Measured with 50¾ source and 50¾ load termination		-35		dB
	Attenuation from 800MHz to 2.2GHz	$V_R = 5V$ Measured with 50 ³ / ₄ source and 50 ³ / ₄ load termination		-35		dB

Notes:

3. The working peak reverse voltage, V_{RWM}, should be equal to or greater than the DC or continuous peak operating voltage level.

4. V_{BR} is measured at the pulse test current $I_{\text{T}}.$

5. Measurements performed using a 100ns Transmission Line Pulse (TLP) system.

6. Total capacitance is equal to 2 x C_{CH} .

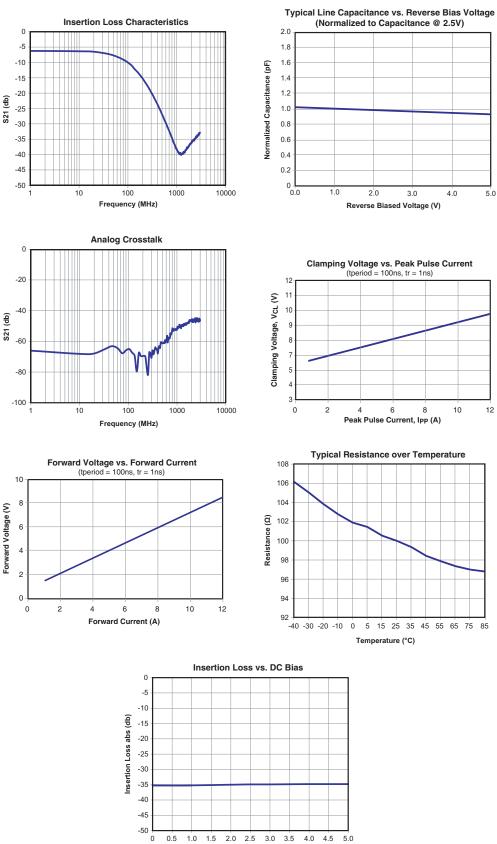
7. Measured at 25°C, V_R = 2.5V, f = 1.0MHz.

8. Guaranteed by design.



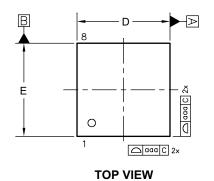
AOZ8013

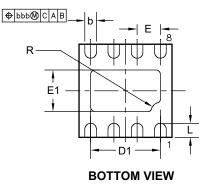
Typical Performance Characteristics

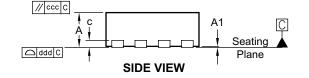




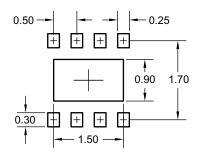
Package Dimensions, DFN 2.0 x 2.0 8L







RECOMMENDED LAND PATTERN



UNIT: mm

Dimensions in millimeters

0.10

0.10

0.08

A

A1

b

С

D

D1

Е

E1

е

L

R

aaa

bbb

ccc

ddd

Dimensions in inches Symbols Symbols Min. Nom. Max. Min. Nom. Max. 0.70 0.75 0.80 0.028 0.030 0.031 A 0.02 0.05 0.000 0.001 0.002 0.00 A1 0.18 0.25 0.30 b 0.007 0.010 0.012 0.15 0.20 0.25 0.006 0.008 0.010 С 2.00 BSC 0.079 BSC D 1.35 1.50 1.60 D1 0.053 0.059 0.063 2.00 BSC Е 0.079 BSC 0.75 0.030 0.035 0.039 0.90 1.00 E1 0.50 BSC 0.020 BSC е 0.008 0.012 0.016 0.20 0.30 0.40 L 0.20 R 0.008 0.006 0.15 aaa

bbb

ccc

ddd

Notes:

- 1. Dimensions and tolerances conform to ASME Y14.5M-1994.
- 2. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.
- 3. Dimension b applied to metallized terminal and is measured between 0.10mm and 0.30mm from the terminal tip. If the terminal has the optional radius on the other end of the terminal, dimension b should not be measured in that radius area.
- 4. Coplanarity ddd applies to the terminals and all other bottom surface metallization.

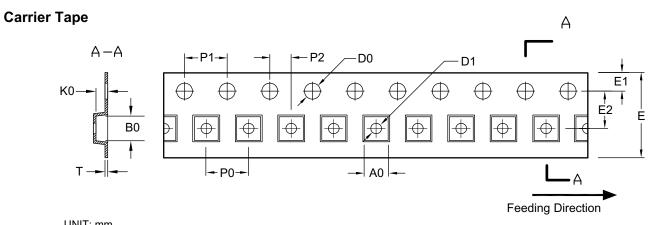
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0.004

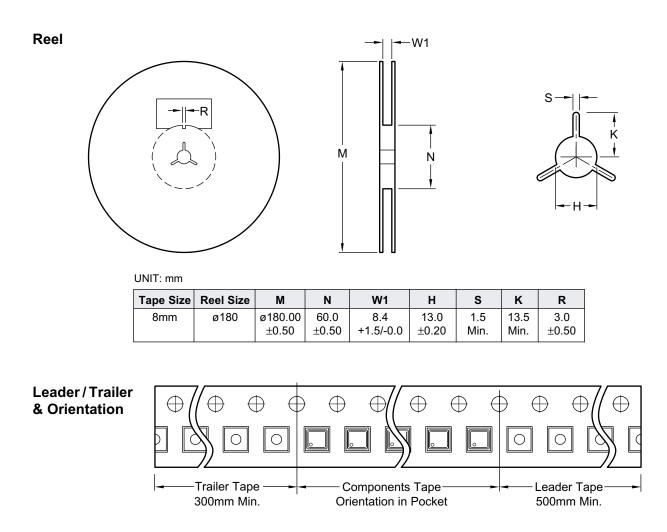
0.003



Tape and Reel Dimensions, DFN 2.0 x 2.0 8L

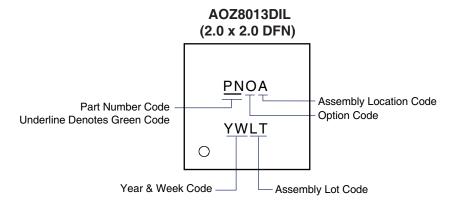


Package	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	Т
DFN 2x2	2.25	2.25	1.00	1.50	1.00	8.00	1.75	3.50	4.00	4.00	2.00	0.254
	±0.05	±0.05	±0.05	+0.1/-0	±0.25/-0	+0.30/-0.1	±0.10	±0.05	±0.10	±0.10	±0.10	±0.02





Package Marking



This data sheet contains preliminary data; supplementary data may be published at a later date. Alpha & Omega Semiconductor reserves the right to make changes at any time without notice.

LIFE SUPPORT POLICY

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As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user. 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.