

Inchange Semiconductor

Product Specification

Silicon NPN Power Transistors

2SC3168

DESCRIPTION

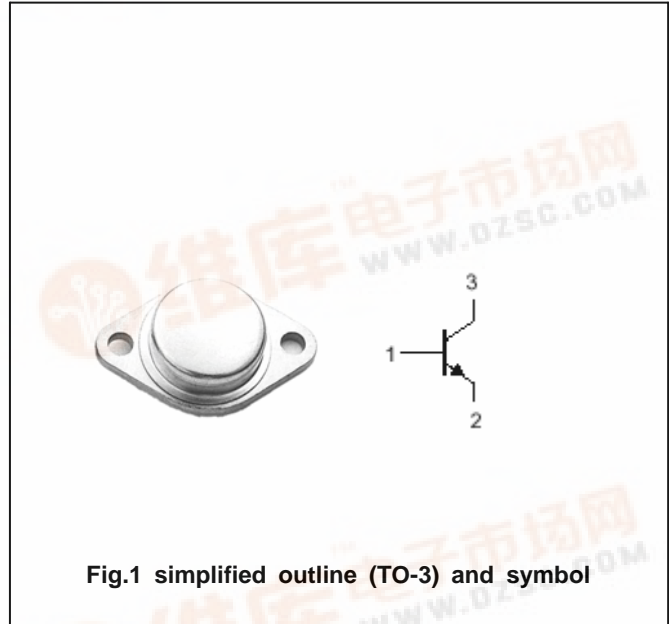
- With TO-3 package
- High voltage ,high speed

APPLICATIONS

- For power switching applications

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
V _{CBO}	Collector-base voltage	Open emitter	500	V
V _{CEO}	Collector-emitter voltage	Open base	400	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		20	A
I _{CM}	Collector current-Peak		40	A
I _B	Base current		7	A
I _{BM}	Base current-Peak		14	A
P _T	Total power dissipation	T _{mb} =25°C	200	W
T _j	Junction temperature		200	°C
T _{stg}	Storage temperature		-65~200	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-mb}	Thermal resistance from junction to mounting base	0.63	°C/W

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.2A ; I _B =0	400			V
V _{CE(sat)}	Collector-emitter saturation voltage	I _C =10A; I _B =2A			1.0	V
V _{BE(sat)}	Base-emitter saturation voltage	I _C =10A; I _B =2A			1.5	V
I _{CBO}	Collector cut-off current	At rated voltage			0.1	mA
I _{CEO}	Collector cut-off current	At rated voltage			0.1	mA
I _{EBO}	Emitter cut-off current	At rated voltage			0.1	mA
h _{FE}	DC current gain	I _C =10A ; V _{CE} =2V	10		40	
f _T	Transition frequency	I _C =2A ; V _{CE} =10V		20		MHz

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PACKAGE OUTLINE

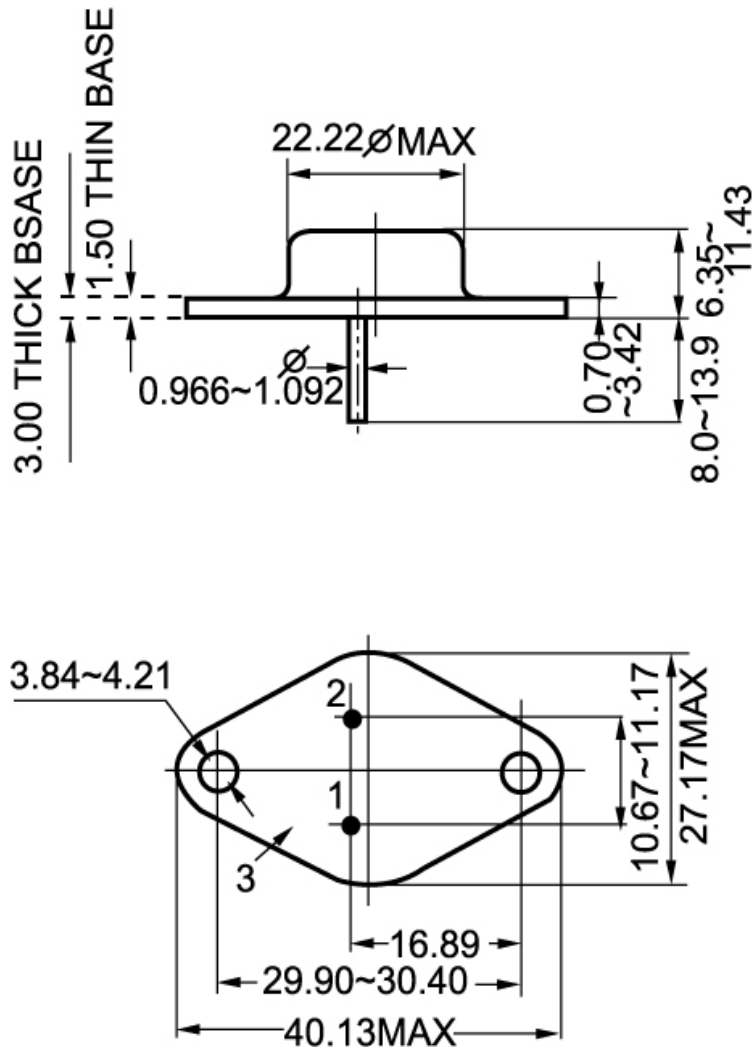


Fig.2 Outline dimensions