

SavantIC Semiconductor

Product Specification

Silicon NPN Power Transistors

2N6546 2N6547

DESCRIPTION

- With TO-3 package
- High voltage ,high speed

APPLICATIONS

Suited for 115 and 220 volt line operated switch-mode applications such as :

- Switching regulators
- PWM inverters and motor controls
- Solenoid and relay drivers
- Deflection circuits

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

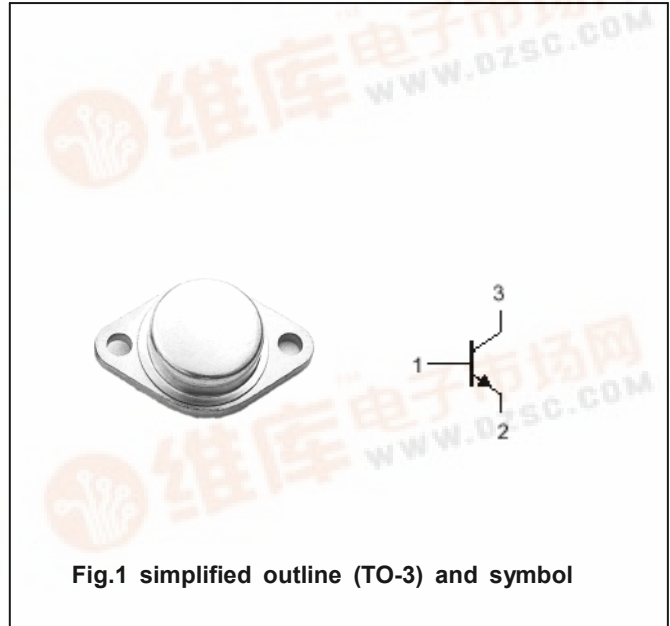


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	2N6546	650	V
		2N6547	850	
V _{CEO}	Collector-emitter voltage	2N6546	300	V
		2N6547	400	
V _{EBO}	Emitter-base voltage	Open collector	9	V
I _C	Collector current		15	A
I _{CM}	Collector current-peak		30	A
I _B	Base current		10	A
I _E	Emitter current		25	A
I _{EM}	Emitter current-peak		50	A
P _T	Total power dissipation	T _c =25□	175	W
T _j	Junction temperature		200	□
T _{stg}	Storage temperature		-65~200	□

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	2N6546	I _C =100mA ; I _B =0			V
		2N6547				
V _{CEsat-1}	Collector-emitter saturation voltage	I _C =10A; I _B =2A			1.5	V
V _{CEsat-2}	Collector-emitter saturation voltage	I _C =15A; I _B =3A			5.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =10A ; I _B =2A			1.6	V
I _{CEV}	Collector cut-off current	2N6546	V _{CE} =650V; V _{BE(off)} =1.5V T _C =100°C			mA
		2N6547				
I _{EBO}	Emitter cut-off current	V _{EB} =9V; I _C =0			1.0	mA
h _{FE-1}	DC current gain	I _C =5A ; V _{CE} =2V	12		60	
h _{FE-2}	DC current gain	I _C =10A ; V _{CE} =2V	6		30	
f _T	Transition frequency	I _C =0.5A ; V _{CE} =10V; f=1MHz	6		35	MHz

Switching times

t _d	Delay time	I _C =10A; I _{B1} =-I _{B2} =2.0A V _{CC} =250V; t _p =0.1ms; Duty Cycle≤2.0%			0.05	μs
t _r	Rise time				1.0	μs
t _{stg}	Storage time				4.0	μs
t _f	Fall time				0.8	μs

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal resistance from junction to case	1.0	°C/W

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PACKAGE OUTLINE

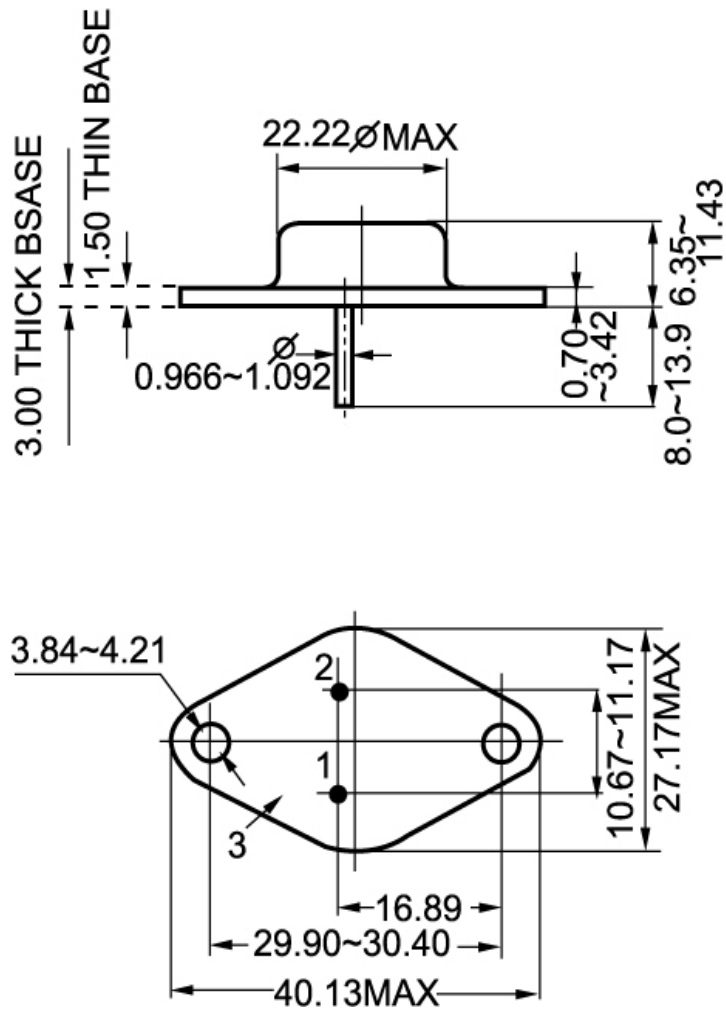


Fig.2 Outline dimensions

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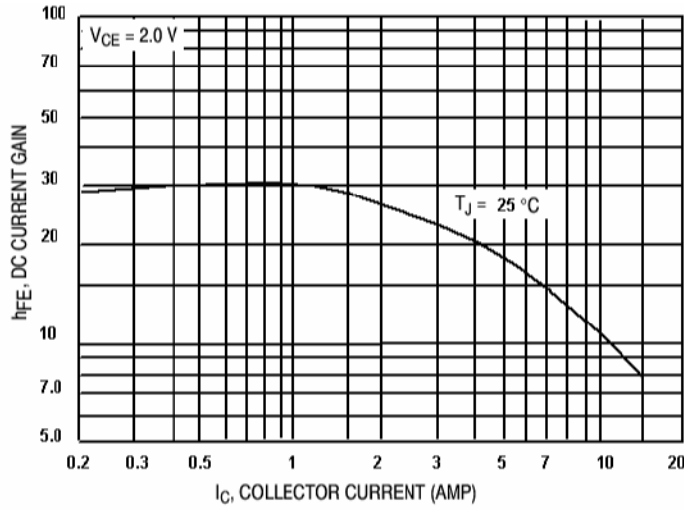


Fig.3 DC current Gain

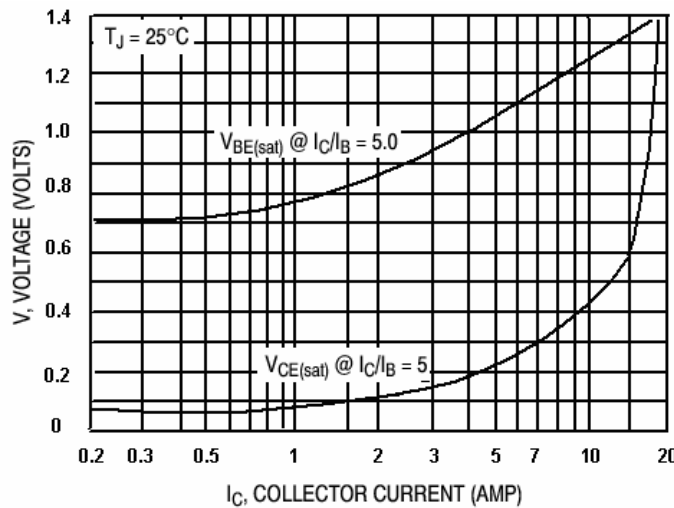


Fig.4 Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

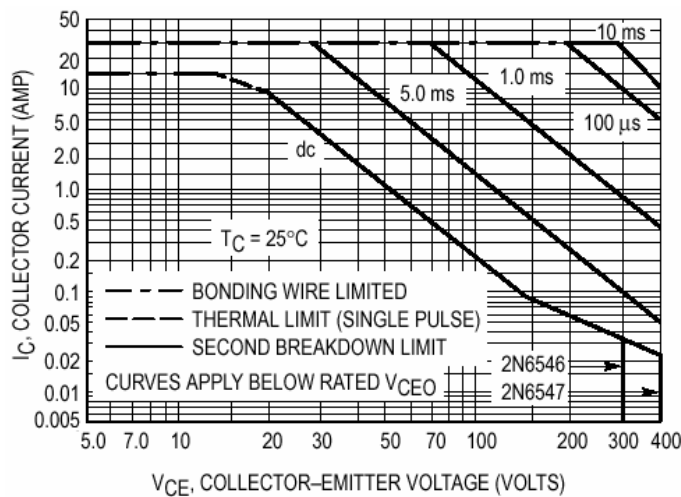


Fig.5 Safe Operating Area