

SavantIC Semiconductor

Product Specification

Silicon PNP Power Transistors

2SA1726

DESCRIPTION

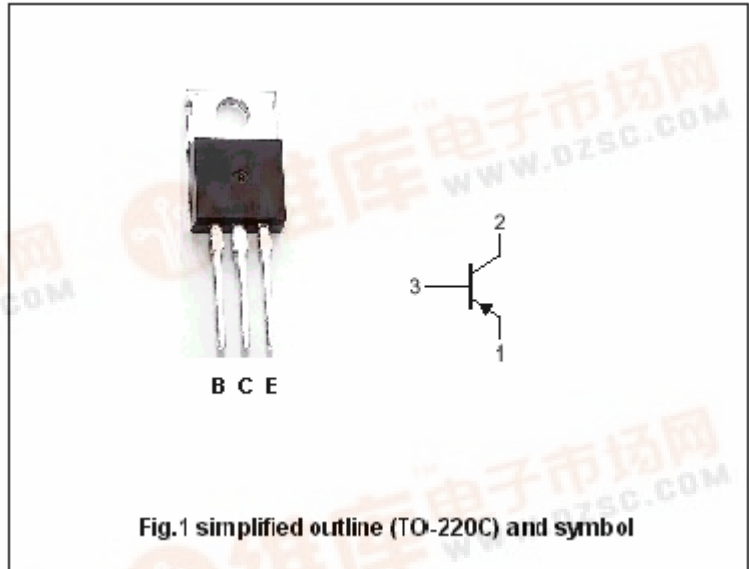
- With TO-220C package
- Complement to type 2SC4512

APPLICATIONS

- Audio and General Purpose

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	-80	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	-80	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-6	V
I <sub>C</sub>	Collector current		-6	A
I <sub>B</sub>	Base current		-3	A
P <sub>C</sub>	Collector dissipation	T <sub>C</sub> =25°C	50	W
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-25mA; R <sub>BE</sub> =∞	-80			V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-2A; I <sub>B</sub> =-0.2A			-0.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-80V; I <sub>E</sub> =0			-10	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-6V; I <sub>C</sub> =0			-10	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-2A; V <sub>CE</sub> =-4V	50		180	
C <sub>OB</sub>	Collector output capacitance	f=1MHz; V <sub>CB</sub> =-10V		150		pF
f <sub>T</sub>	Transition frequency	I <sub>E</sub> =0.5A; V <sub>CE</sub> =-12V		20		MHz

## Switching times

t <sub>on</sub>	Turn-on time	V <sub>CC</sub> =-30V; I <sub>C</sub> =-3A I <sub>B1</sub> =-I <sub>B2</sub> =-0.3A R <sub>L</sub> =10Ω		0.18		μs
t <sub>stg</sub>	Storage time			1.10		μs
t <sub>f</sub>	Fall time			0.21		μs

◆ h<sub>FE</sub> Classifications

O	P	Y
50-100	70-140	90-180

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance:±0.10 mm)

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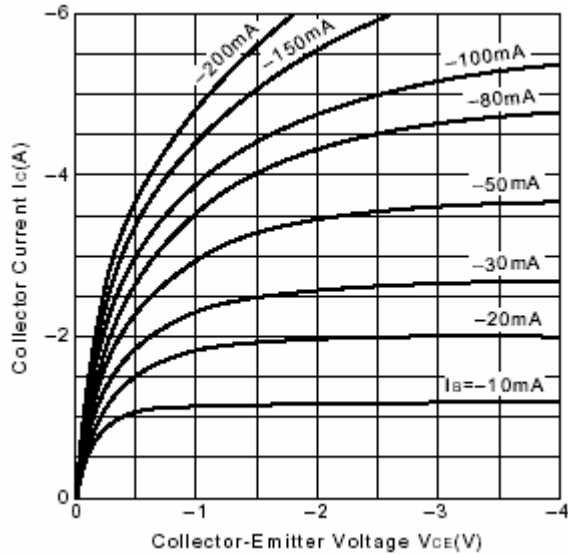


Fig.3 Static Characteristic

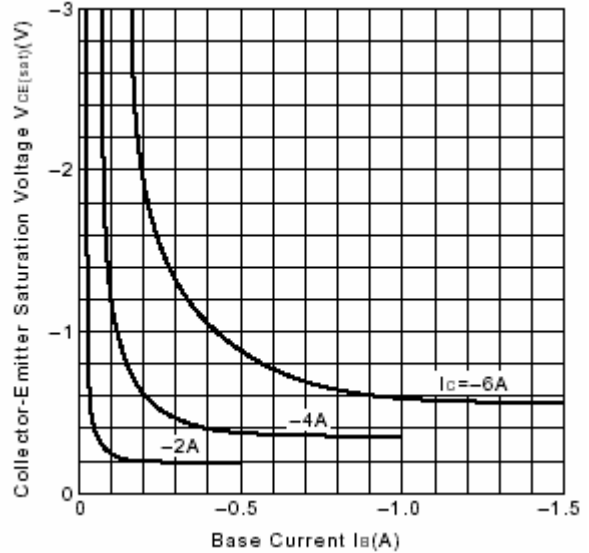


Fig.4  $V_{CE(sat)}-I_B$  Characteristics

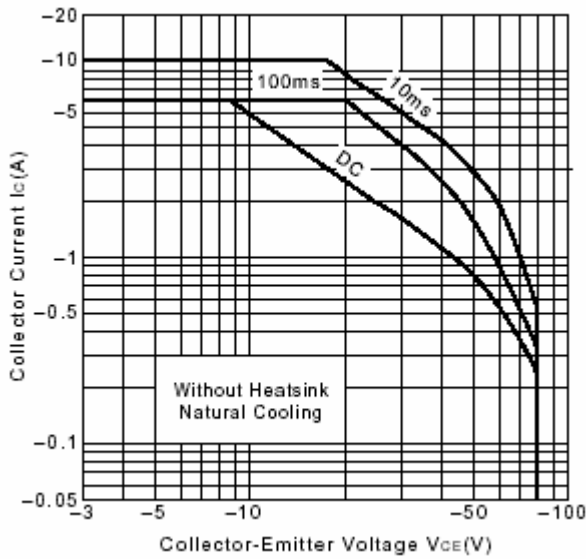


Fig.5 Safe Operating Area

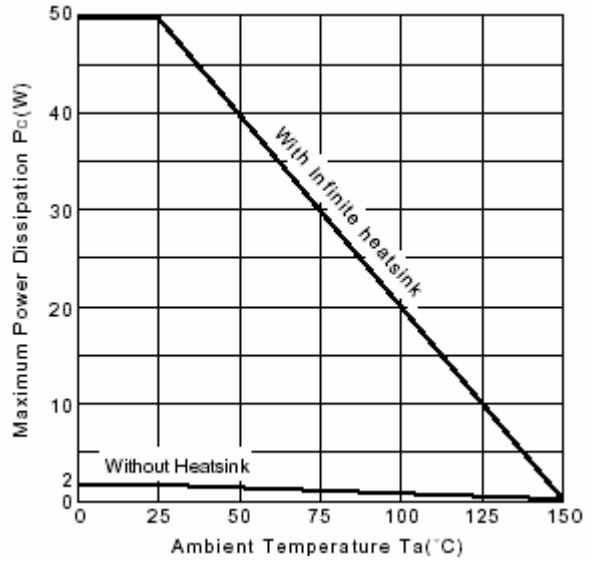


Fig.6  $P_c-T_a$  Derating

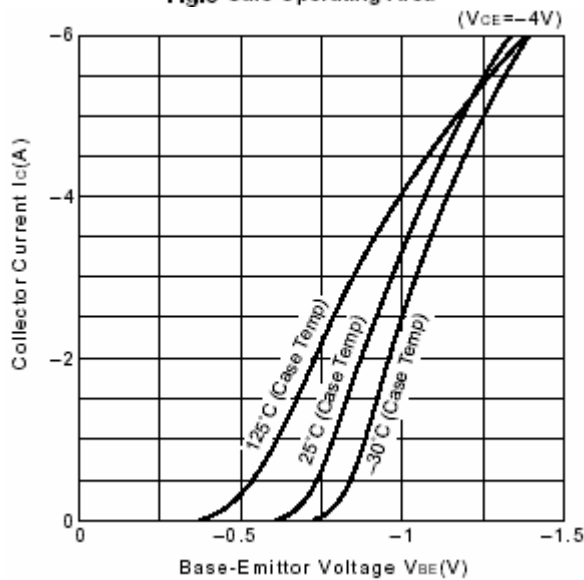


Fig.7  $I_C-V_{BE}$

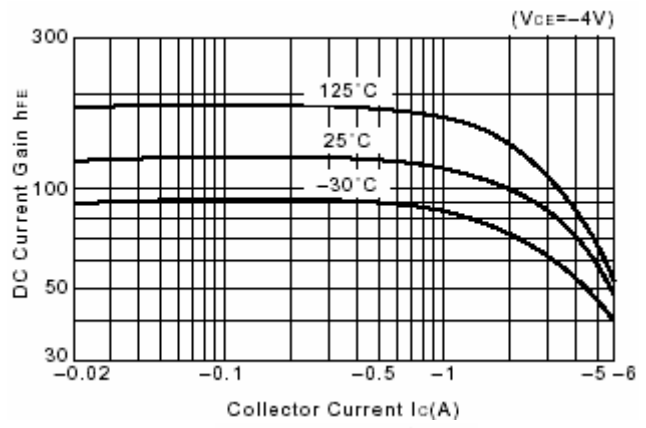


Fig.8 DC current Gain