

SavantIC Semiconductor

Product Specification

Silicon PNP Power Transistors

2SB1373

DESCRIPTION

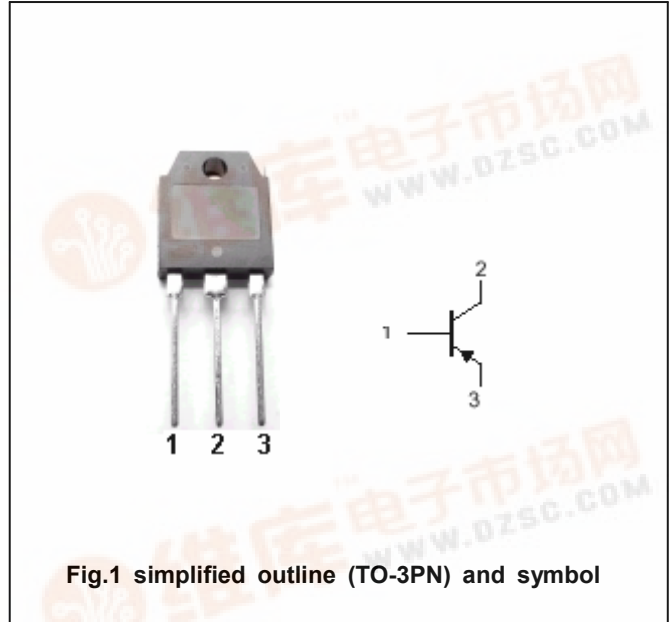
- With TO-3PN package
- Complement to type 2SD2066
- Wide area of safe operation

APPLICATIONS

- For high power amplification

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter



Absolute maximum ratings(Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-160	V
V _{CEO}	Collector-emitter voltage	Open base	-160	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-12	A
P _C	Collector power dissipation	T _C =25℃	120	W
		T _a =25℃	2.5	
T _j	Junction temperature		150	℃
T _{stg}	Storage temperature		-55~150	℃

Silicon PNP Power Transistors

2SB1373

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA ; I _B =0	-160			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-8A; I _B =-0.8A			-2.0	V
V _{BE}	Base-emitter on voltage	I _C =-8A; V _{CE} =-5V			-1.8	V
I _{CBO}	Collector cut-off current	V _{CB} =-160V; I _E =0			-50	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-50	μA
h _{FE-1}	DC current gain	I _C =-20mA ; V _{CE} =-5V	20			
h _{FE-2}	DC current gain	I _C =-1A ; V _{CE} =-5V	60		200	
h _{FE-3}	DC current gain	I _C =-8A ; V _{CE} =-5V	20			
f _T	Transition frequency	I _C =-0.5A ; V _{CE} =-5V		15		MHz
C _{OB}	Collector output capacitance	I _E =0; f=1MHz; V _{CB} =-10V		400		pF

◆ h_{FE-1} Classifications

Q	S	P
60-120	80-160	100-200

Silicon PNP Power Transistors

2SB1373

PACKAGE OUTLINE

