





Logic Diagram (Positive Logic)

FEATURES:

- 3.3V ABT octal bus transceiver with 3-state outputs
- RAD-PAK® radiation hardened against natural space radiation
- Total dose hardness:
- >100 krad (Si), depending upon space misssion
- · Package:
 - 20 pin RAD-PAK® flat package
 - 20 pin Rad-Pak® DIP
- Operating temperature range: -55 to 125°C
- Supports mixed-mode signal operation
 5V input and output voltages with 3.3V V_{CC}
- Supports unregulated battery operation down to 2.7V.
- Typical VOLP (output ground bounce) < 0.8V at V_{CC}=3.3V, T_A= 25°C
- Latch-up performance exceeds 500mA per JEDEC standard
- Supports live insertion
- Bus-hold data inputs eliminate the need for external pullup resistors

Description:

Maxwell Technologies' 54LVTH245A 8-bit octal transceiver micro-circuit features a greater than 100 krad (Si) total dose tolerance, depending upon space mission. The 54LVTH245A is designed specifically for low voltage (3.3V) V_{CC} operation, but with the capability to provide a TTL interface to a 5V system environment. It is designed for asynchronous communication between data buses. The device transmits data from the A bus to the B bus or from the B bus to the A bus depending upon the logic level at the direction control (DIR) input. The output enable (\overline{OE}) input can be used to disable the device so the buses are effectively isolated. The 54LVTH245A active bus-hold circuitry is provided to prevent floating data inputs at a valid logic level and to eliminate the need for external pullup resistors.

Maxwell Technologies' patented RAD-PAK® packaging technology incorporates radiation shielding in the microcircuit package. It eliminates the need for box shielding while providing the required radiation shielding for a lifetime in orbit or space mission. In a GEO orbit, RAD-PAK provides greater than 100 krad (Si) radiation dose tolerance. This product is available with screening up to Class S.

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| TABLE 1. PINOUT DESCRIF |
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| Pin | Symbol | DESCRIPTION | |
|-------|-----------------|------------------|--|
| 1 | DIR | DirectionControl | |
| 2-9 | A1-A8 | Inputs | |
| 10 | GND | Ground | |
| 11-18 | B8-B1 | Outputs | |
| 19 | OE | Output Enable | |
| 20 | V _{CC} | Supply Voltage | |

TABLE 2. 54LVT245 ABSOLUTE MAXIMUM RATINGS ¹

| Parameter | Symbol | Min | Max | Unit |
|--|-----------------|------|-----|------|
| Supply Voltage Range | V _{CC} | -0.5 | 4.6 | V |
| Input Voltage Range ² | VI | -0.5 | 7.0 | V |
| Voltage Range Applied to Any Output in the High State or Power-Off State | V _o | -0.5 | 7.0 | V |
| Current Into Any Output in the High State ³ | Ι _ο | | 48 | mA |
| Input Clamp Current (V _I < 0) | I _{IK} | | -50 | mA |
| Output Clamp Current (V _O < 0) | Ι _{οκ} | | -50 | mA |
| Maximum Power Dissipation at $T_A = 55^{\circ}C$ | P _D | | 1.6 | mW |
| Operating Temperature | T _A | -55 | 125 | °C |
| Storage Temperature Range | Τ _S | -65 | 150 | °C |

 Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- 2. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 3. This current flows only when the output is in the high state and $V_0 > V_{CC}$.

| Parameter | Variation |
|---------------------|--|
| I _{CC(OL)} | ±10% of specified value in Table 5 |
| I _{CC(OH)} | ±10% of specified value in Table 5 |
| I _{CC(OD)} | $\pm 10\%$ of specified value in Table 5 |

TABLE 3. DELTA LIMITS



TABLE 4. 54LVTH245A RECOMMENDED OPERATING CONDITIONS ¹

| Parameter | Symbol | Min | Мах | Unit | |
|------------------------------------|--------------------|-----------------------|-----|------|------|
| Supply Voltage | V _{CC} | 2.7 | 3.6 | V | |
| High-level Input Voltage | V _{IH} | 2.0 | | V | |
| Low-level Input Voltage | V _{IL} | | 0.8 | V | |
| Input Voltage | | VI | | 5.5 | V |
| High-level Output Current | | I _{OH} | | -24 | mA |
| Low-level Output Current | | I _{OL} | | 48 | mA |
| Input Transition Rise or Fall Rate | Outputs enabled | $\Delta t / \Delta v$ | | 10 | ns/V |
| Operating Temperature | • | T _A | -55 | 125 | ٦° |

1. Unused control inputs must be held high or low to prevent them from floating.

TABLE 5. 54LVTH245A ELECTRICAL CHARACTERISTICS $(V_{++} = 3.3V_{+}+10\%_{-}T_{+} = -55 \text{ to } +125^{\circ}\text{C}$

| | (v _{CC} - 3 | $1.3V \pm 10\%$, $I_A = -55$ | 10 ± 125 C, UNLESS | OTHERWISE SPECIFIED | | | |
|---------------------------|--------------------------------|--|---|---------------------|----------------------|------|------|
| Parameter | Symbol | | Test Conditions | | Min | Мах | Unit |
| Input Clamp Voltage | V _{IK} | V _{CC} = 2.7 | = -' | 18mA | | -1.2 | V |
| High-Level Output Voltage | V _{OH} | V _{CC} = 2.7V to 3.6V | I _{ОН} = - | 100µA | V _{CC} -0.2 | | V |
| | | V _{CC} = 2.7V | I _{OH} = | -8mA | 2.4 | | |
| | | V _{CC} = 3V, | I _{OH} = | -24mA | 2.0 | | |
| Low-Level Output Voltage | V _{OL} | V _{CC} = 2.7V | I _{OL} = | 100µA | | 0.2 | V |
| | | | I _{OL} = | 24mA | | 0.5 | |
| | | V _{CC} = 3V | I _{OL} = | 16mA | | 0.4 | |
| | | | I _{OL} = | 32mA | | 0.5 | |
| | | | I _{OL} = 48mA | | | 0.55 | |
| Input Current | I _I | V _{CC} = 3.6V | $V_{I} = V_{CC}$ or GND | Control inputs | | ±1 | μA |
| | | V _{CC} = 0 or 3.6V | V _I = 5.5V | | | 10 | |
| | | V _{CC} = 3.6V | V _I = 5.5V | A or B Ports | | 20 | |
| | | | $V_{I} = V_{CC}$ | | | 1 | |
| | | | V ₁ = 0 | | | -5 | |
| Hold Current | I _{I(HOLD)} | V _{CC} = 3V | V ₁ = 0.8V | A or B Ports | 75 | | μA |
| | | | V ₁ = 2V | | -75 | | |
| Power Up Current | I _{OZPU} ² | V_{CC} = 0 to 1.5V, V_{O} = 0.5V to 3V, \overline{OE} = don't care | | | | ±100 | μA |
| Power Down Current | I _{OZPD} ² | V _{CC} = 1.5V to 0, | $V_0 = 0.5V \text{ to } 3V, \overline{C}$ | DE = don't care | | ±100 | μA |

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TABLE 5. 54LVTH245A ELECTRICAL CHARACTERISTICS

(V_{CC} = $3.3V \pm 10\%$, T_A = -55 to +125°C, UNLESS OTHERWISE SPECIFIED)

| Parameter | Symbol | TEST CONDITIONS | | Min | Max | Unit |
|--------------------------|-----------------------------|--|------------------|-----|------|------|
| Supply Current | I _{CC} | V _{CC} = 3.6V Outputs high | | | 0.39 | mA |
| | | I ₀ = 0 | Outputs low | | 14 | |
| | | V _I = V _{CC} or GND | Outputs disabled | | 0.39 | |
| Delta Supply Current | ΔI_{CC}^{1} | V_{CC} = 3V to 3.6V, One input at V_{CC} -0.0 V_{CC} or GND | | 0.2 | mA | |
| Input Capacitance | Cl ² | V ₁ = 3V or 0 | | 8 | pF | |
| Input Output Capacitance | C _O ² | V _O = 3V or 0 | | 15 | pF | |

1. This is the increase in supply current for each input that is at the specified TTL voltage level rather than V_{CC} or GND.

2. Guaranteed by design.

TABLE 6. 54LVTH245A AC ELECTRICAL CHARACTERISTICS

| | 00 | ~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~ | | | - | | |
|------------------------|------------------|---|-----|--|-----|------|------|
| Parameter | Symbol | $V_{CC} = 3.3V \pm 0.3V$ | | $V_{CC} = 3.3V \pm 0.3V$ $V_{CC} = 2.7V$ | | 2.7V | Unit |
| | | Min | Мах | Min | Max | | |
| Propagation Delay Time | t _{PLH} | 0.7 | 3.7 | | 4.2 | ns | |
| A orB to B or A | t _{PHL} | 0.7 | 3.7 | | 4.2 | | |
| Output Enable Time | t _{PZH} | 1.2 | 5.7 | | 7.4 | ns | |
| OE to A or B | t _{PZL} | 1.6 | 5.7 | | 6.8 | | |
| Output Disable Time | t _{PHZ} | 1.8 | 6.2 | | 6.8 | ns | |
| OE to A or B | t _{PLZ} | 1.8 | 5.3 | | 5.5 | | |

(V_{CC} = $3.3V \pm 10\%$, T_A = -55 ro 125°C, unless otherwise noted)

TABLE 7. FUNCTION TABLE

| INPL | OPERATION | | | | | |
|------|-----------|-----------------|--|--|--|--|
| OE | DIR | | | | | |
| L | L | B data to A bus | | | | |
| I | Н | A data to B bus | | | | |
| Н | Х | Isolation | | | | |



FIGURE 1. LOAD CIRCUIT



Figure Note:

1. C₁ includes probe and jig capacitance.

PARAMETER MEASUREMENT INFORMATION

| Test | S1 |
|------------------------------------|------|
| t _{PLH} /t _{PHL} | Open |
| t _{PLZ} /t _{PZL} | 6V |
| t _{PHZ} /t _{PZH} | GND |





FIGURE 4. PROPAGATION DELAY TIMES INVERTING AND NONINVERTING OUTPUTS



FIGURE 5. ENABLE AND DISABLE TIMES LOW- AND HIGH-LEVEL ENABLING



Figure Note:

2. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by The output control.

Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.



20 PIN RAD-PAK® FLAT PACKAGE

| Symbol | | DIMENSION | | | |
|--------|-------|-----------|-------|--|--|
| | Min | Nом | Мах | | |
| A | 0.128 | 0.141 | 0.154 | | |
| b | 0.015 | 0.017 | 0.022 | | |
| С | 0.003 | 0.005 | 0.009 | | |
| D | 0.470 | 0.480 | 0.490 | | |
| E | 0.287 | 0.295 | 0.303 | | |
| E1 | | | 0.333 | | |
| E2 | 0.155 | 0.160 | | | |
| E3 | 0.030 | 0.068 | | | |
| е | | 0.050 BSC | L | | |
| L | 0.370 | 0.380 | 0.390 | | |
| Q | 0.035 | 0.039 | 0.042 | | |
| S1 | 0.005 | 0.007 | | | |
| Ν | 20 | | | | |

F20-01 Note: All dimensions in inches

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Important Notice:

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3.3V ABT Octal Bus Transceiver with 3-State Output Product Ordering Options

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