

APT4030CN	400V	15.0A	0.30Ω
APT3530CN	350V	15.0A	0.30Ω
APT4040CN	400V	13.0A	0.40Ω
APT3540CN	350V	13.0A	0.40Ω

POWER MOS IV™

N - CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT 3530CN	APT 4030CN	APT 3540CN	APT 4040CN	UNIT
V _{DSS}	Drain-Source Voltage	350	400	350	400	Volts
I _D	Continuous Drain Current @ T _C = 25°C	15.0		13.0		Amps
I _{DM}	Pulsed Drain Current ①	60		52		
V _{GS}	Gate-Source Voltage	±30				Volts
P _D	Total Power Dissipation @ T _C = 25°C	150				Watts
	Linear Derating Factor	1.2				W/°C
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150				°C
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300				

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0V, I_D = 250 \mu\text{A}$)	APT4030CN / APT4040CN	400		Volts
		APT3530CN / APT3540CN	350		
$I_{D(ON)}$	On State Drain Current ② ($V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max, $V_{GS} = 10V$)	APT4030CN / APT3530CN	15.0		Amps
		APT4040CN / APT3540CN	13.0		
$R_{DS(ON)}$	Drain-Source On-State Resistance ② ($V_{GS} = 10V, 0.5 I_D$ [Cont.])	APT4030CN / APT3530CN		0.30	Ohms
		APT4040CN / APT3540CN		0.40	
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}, V_{GS} = 0V$)			250	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125^\circ\text{C}$)			1000	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30V, V_{DS} = 0V$)			± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 1.0\text{mA}$)	2		4	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.80	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction to Ambient			50	

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DYNAMIC CHARACTERISTICS

APT4030/3530/4040/3540CN

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{DC}	Drain-to-Case Capacitance	$f = 1 \text{ MHz}$		15	22	pF
C_{iss}	Input Capacitance	$V_{GS} = 0V$		1500	1800	
C_{oss}	Output Capacitance	$V_{DS} = 25V$		385	540	
C_{rss}	Reverse Transfer Capacitance	$f = 1 \text{ MHz}$		160	240	
Q_g	Total Gate Charge ③	$V_{GS} = 10V$		71	105	nC
Q_{gs}	Gate-Source Charge	$V_{DD} = 0.5 V_{DSS}$		8	12	
Q_{gd}	Gate-Drain ("Miller") Charge	$I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$		36	54	
$t_d(\text{on})$	Turn-on Delay Time	$V_{GS} = 15V$		14	28	ns
t_r	Rise Time	$V_{DD} = 0.5 V_{DSS}$		23	46	
$t_d(\text{off})$	Turn-off Delay Time	$I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$		43	64	
t_f	Fall Time	$R_G = 1.8\Omega$		15	30	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions		MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)	APT4030CN / APT3530CN			15	Amps
		APT4040CN / APT3540CN			13	
I_{SM}	Pulsed Source Current ① (Body Diode)	APT4030CN / APT3530CN			60	
		APT4040CN / APT3540CN			52	
V_{SD}	Diode Forward Voltage ② ($V_{GS} = 0V$, $I_S = -I_D [\text{Cont.}]$)				1.3	Volts
t_{rr}	Reverse Recovery Time ($I_S = -I_D [\text{Cont.}]$, $di_S/dt = 100A/\mu s$)		142	284	568	ns
Q_{rr}	Reverse Recovery Charge ($I_S = -I_D [\text{Cont.}]$, $di_S/dt = 100A/\mu s$)		2.2	4.5	9.0	μC

SAFE OPERATING AREA CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
SOA1	Safe Operating Area	$V_{DS} = 0.4 V_{DSS}$, $I_{DS} = P_D / 0.4 V_{DSS}$, $t = 1 \text{ Sec.}$	150			Watts
SOA2	Safe Operating Area	$I_{DS} = I_D [\text{Cont.}]$, $V_{DS} = P_D / I_D [\text{Cont.}]$, $t = 1 \text{ Sec.}$	150			
I_{LM}	Inductive Current Clamped	APT4030CN / APT3530CN	60			Amps
		APT4040CN / APT3540CN	52			

① Repetitive Rating: Pulse width limited by maximum junction temperature. See Transient Thermal Impedance Curve. (Fig. 1)

② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%

③ See MIL-STD-750 Method 3471

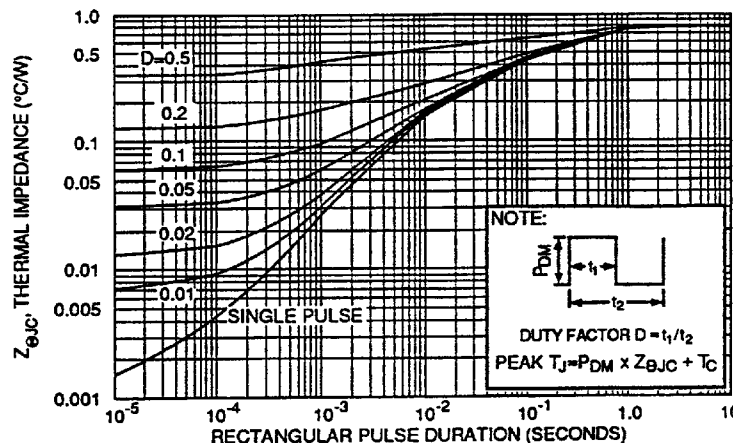


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

APT4030/3530/4040/3540CN

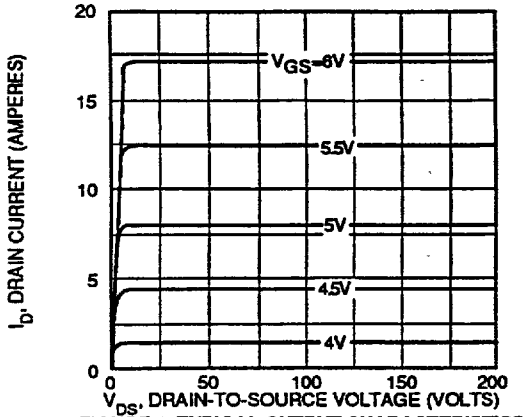


FIGURE 2, TYPICAL OUTPUT CHARACTERISTICS

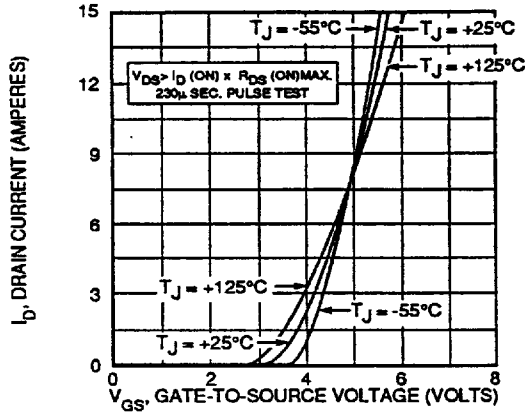


FIGURE 4, TYPICAL TRANSFER CHARACTERISTICS

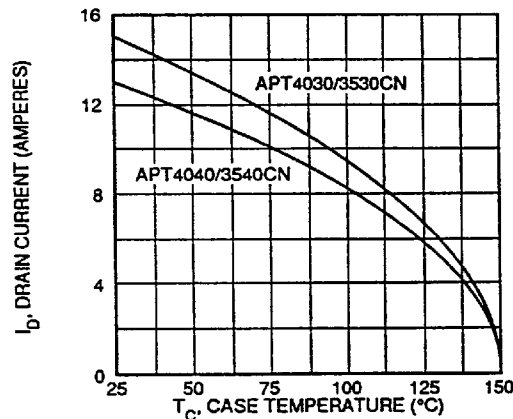


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

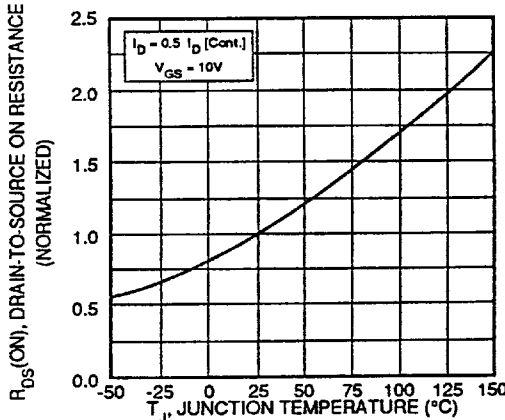


FIGURE 8, ON-RESISTANCE vs. TEMPERATURE

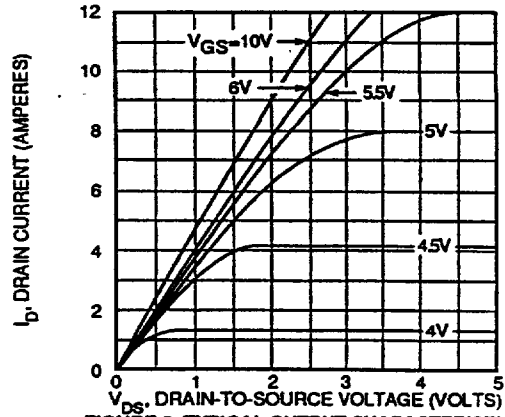


FIGURE 3, TYPICAL OUTPUT CHARACTERISTICS

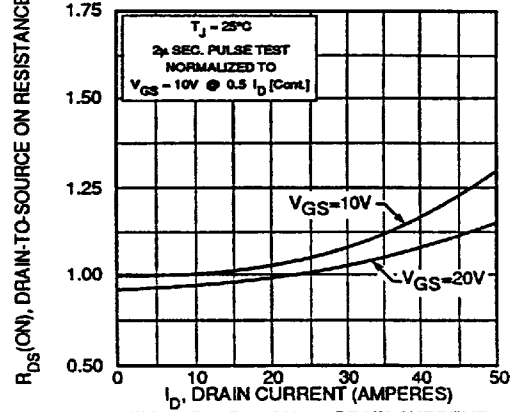


FIGURE 5, $R_{DS(ON)}$ vs DRAIN CURRENT

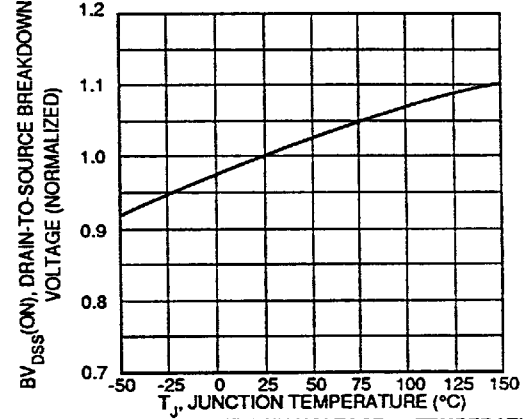


FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE

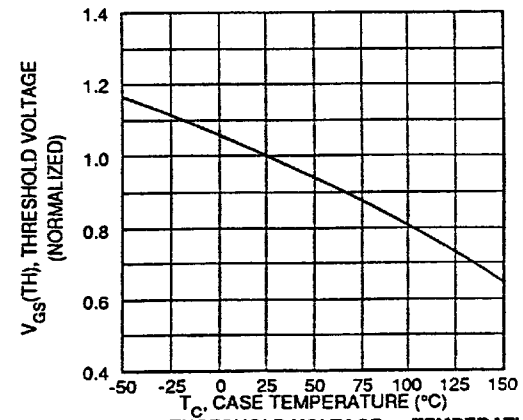


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

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APT4030/3530/4040/3540CN

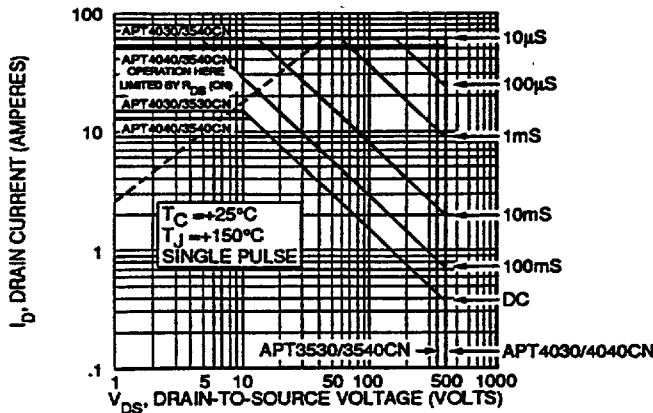


FIGURE 10, MAXIMUM SAFE OPERATING AREA

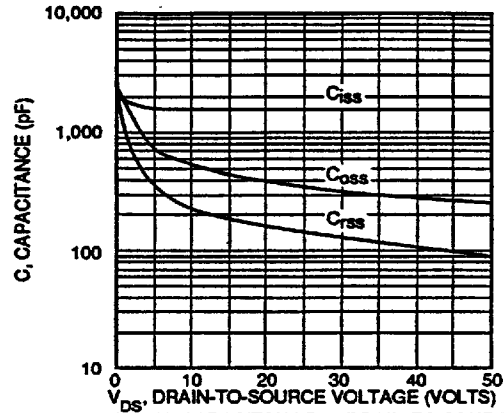


FIGURE 11, TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

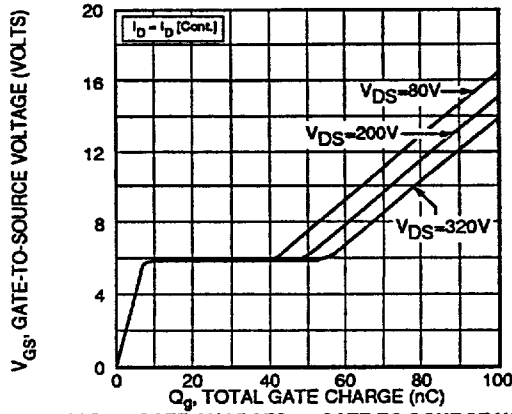


FIGURE 12, GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

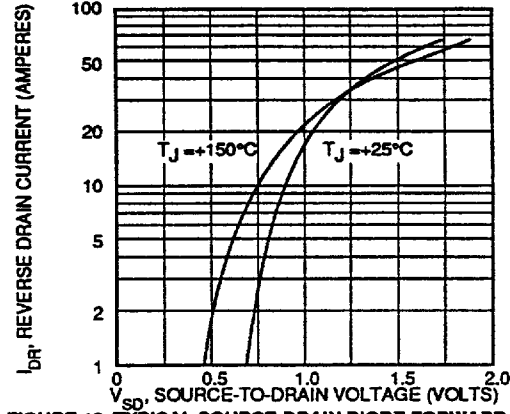
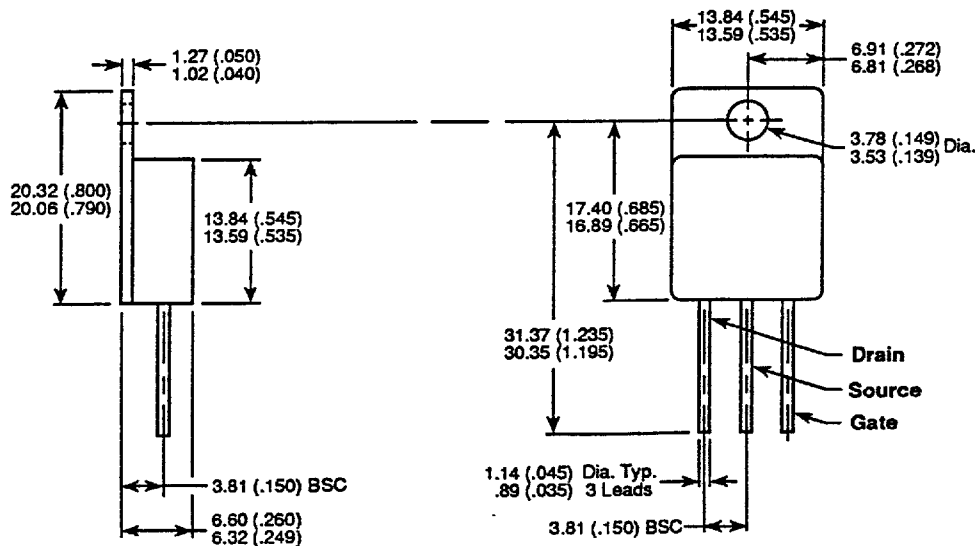


FIGURE 13, TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

TO-254AA Package Outline



Dimensions in Millimeters and (Inches)

050-4015
Rev A

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