

**APT6060BN 600V 13.0A 0.60Ω**  
**APT5560BN 550V 13.0A 0.60Ω**  
**APT6070BN 600V 12.0A 0.70Ω**  
**APT5570BN 550V 12.0A 0.70Ω**

## POWER MOS IV®

### N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

#### MAXIMUM RATINGS

All Ratings:  $T_C = 25^\circ\text{C}$  unless otherwise specified.

Symbol	Parameter	APT				UNIT
		5560BN	6060BN	5570BN	6070BN	
$V_{DSS}$	Drain-Source Voltage	550	600	550	600	Volts
$I_D$	Continuous Drain Current	13		12		Amps
$I_{DM}$	Pulsed Drain Current ①	52		48		Amps
$V_{GS}$	Gate-Source Voltage	±30				Volts
$P_D$	Total Power Dissipation @ $T_C = 25^\circ\text{C}$ , Derate Above $25^\circ\text{C}$	240				Watts
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	- 55 to 150				$^\circ\text{C}$

#### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage ( $V_{GS} = 0V, I_D = 250 \mu\text{A}$ )	APT6060BN / APT6070BN		600	Volts
		APT5560BN / APT5570BN		550	Volts
$I_{DSS}$	Zero Gate Voltage Drain Current ( $V_{DS} = V_{DSS}, V_{GS} = 0V$ )			250	$\mu\text{A}$
	( $V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125^\circ\text{C}$ )			1000	
$I_{GSS}$	Gate-Source Leakage Current ( $V_{GS} = \pm 30V, V_{DS} = 0V$ )			±100	nA
$I_D(ON)$	On State Drain Current ②	APT6060BN / APT5560BN		13	Amps
	( $V_{DS} > I_D(ON) \times R_{DS(ON)}$ Max, $V_{GS} = 10V$ )	APT6070BN / APT5570BN		12	Amps
$V_{GS}(TH)$	Gate Threshold Voltage ( $V_{DS} = V_{GS}, I_D = 1\text{mA}$ )	2		4	Volts
$R_{DS(ON)}$	Static Drain-Source On-State Resistance ②	APT6060BN / APT5560BN		0.60	Ohms
	( $V_{GS} = 10V, I_D = 0.5 I_D(\text{Cont.})$ )	APT6070BN / APT5570BN		0.70	Ohms

#### THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.51	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction to Ambient			40	$^\circ\text{C/W}$
$T_L$	Max. Lead Temp. for Soldering Conditions: 0.063" from Case for 10 Sec.			300	$^\circ\text{C}$

**CAUTION:** These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

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**DYNAMIC CHARACTERISTICS**

APT6060/5560/6070/5570BN

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1\text{ MHz}$		1510	1800	pF
$C_{oss}$	Output Capacitance			280	390	pF
$C_{rss}$	Reverse Transfer Capacitance			105	157	pF
$Q_g$	Total Gate Charge <sup>③</sup>	$V_{GS} = 10V, I_D = I_D [\text{Cont.}]$ $V_{DD} = 0.5 V_{DSS}$		66	105	nC
$Q_{gs}$	Gate-Source Charge			8	12	nC
$Q_{gd}$	Gate-Drain ("Miller") Charge			35	53	nC
$t_d(\text{on})$	Turn-on Delay Time	$V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}], V_{GS} = 15V$ $R_G = 1.8\Omega$		13	25	ns
$t_r$	Rise Time			18	35	ns
$t_d(\text{off})$	Turn-off Delay Time			51	77	ns
$t_f$	Fall Time			21	41	ns

**SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS**

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
$I_S$	Continuous Source Current (Body Diode)	APT6060BN / APT5560BN		13	Amps
		APT6070BN / APT5570BN		12	Amps
$I_{SM}$	Pulsed Source Current <sup>①</sup> (Body Diode)	APT6060BN / APT5560BN		52	Amps
		APT6070BN / APT5570BN		48	Amps
$V_{SD}$	Diode Forward Voltage <sup>②</sup> ( $V_{GS} = 0V, I_S = -I_D [\text{Cont.}]$ )			1.3	Volts
$t_{rr}$	Reverse Recovery Time ( $I_S = -I_D [\text{Cont.}], di_S/dt = 100A/\mu s$ )	148	297	594	ns
$Q_{rr}$	Reverse Recovery Charge	2	4	8	$\mu C$

**SAFE OPERATING AREA CHARACTERISTICS**

Symbol	Characteristic	Test Conditions / Part Number	MIN	TYP	MAX	UNIT
SOA1	Safe Operating Area	$V_{DS} = 0.4 V_{DSS}, I_{DS} = P_D / 0.4 V_{DSS}, t = 1\text{ Sec.}$	240			Watts
SOA2	Safe Operating Area	$I_{DS} = I_D [\text{Cont.}], V_{DS} = P_D / I_D [\text{Cont.}], t = 1\text{ Sec.}$	240			Watts
$I_{LM}$	Inductive Current Clamped	APT6060BN / APT5560BN	52			Amps
		APT6070BN / APT5570BN	48			Amps

- ① Repetitive Rating: Pulse width limited by maximum junction temperature. See Transient Thermal Impedance Curve. (Fig.1)
- ② Pulse Test: Pulse width < 380  $\mu s$ , Duty Cycle < 2%
- ③ See MIL-STD-750 Method 3471

APT Reserves the right to change, without notice, the specifications and information contained herein.

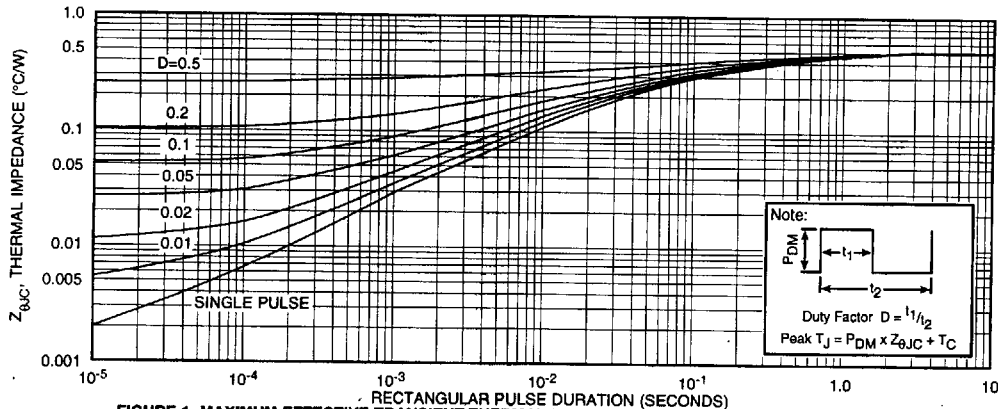
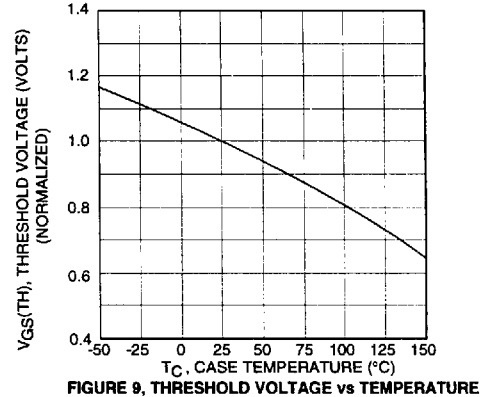
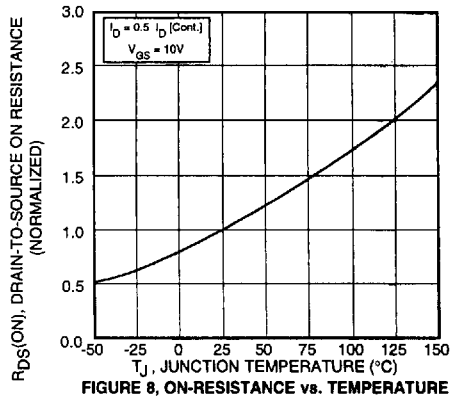
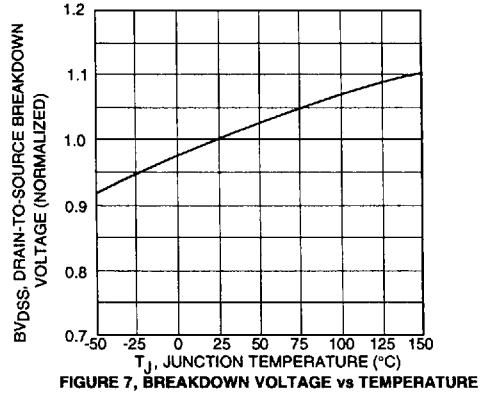
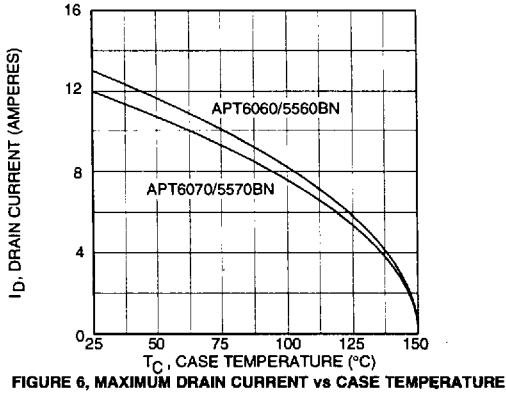
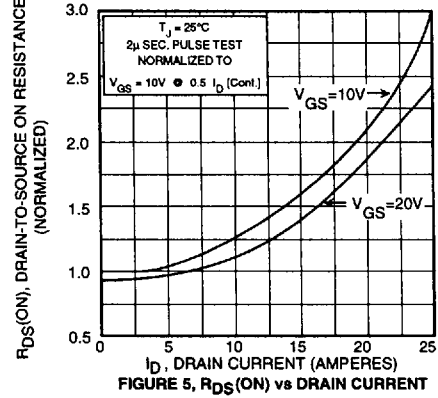
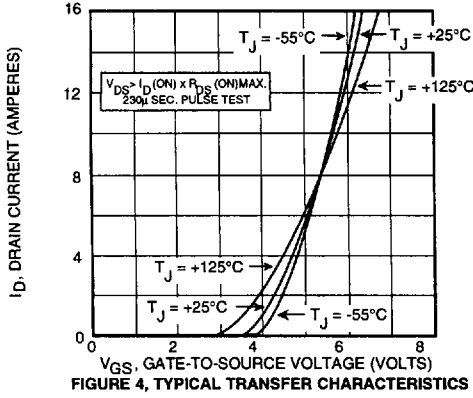
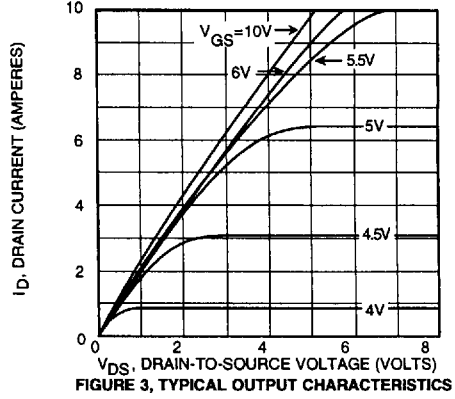
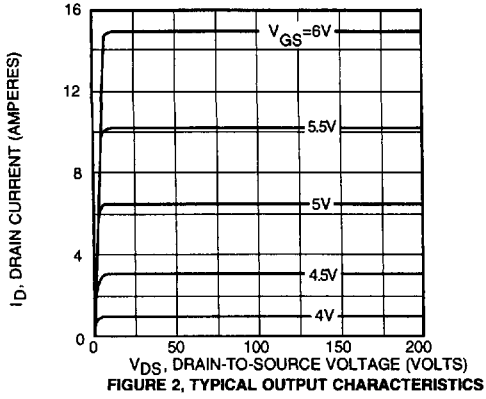


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION  
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**APT6060/5560/6070/5570BN**



APT6060/5560/6070/5570BN

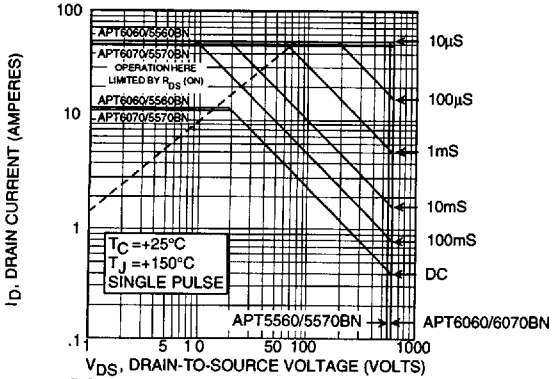


FIGURE 10, MAXIMUM SAFE OPERATING AREA

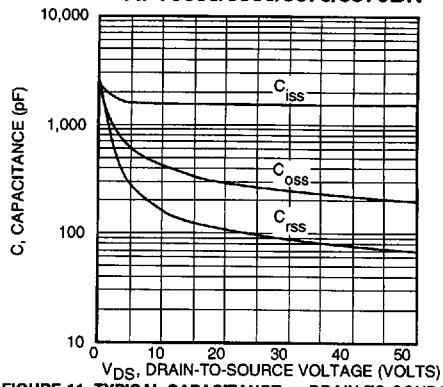


FIGURE 11, TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

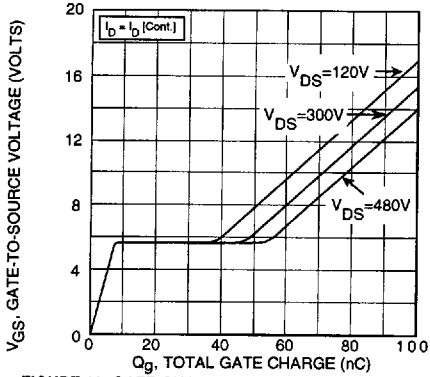


FIGURE 12, GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

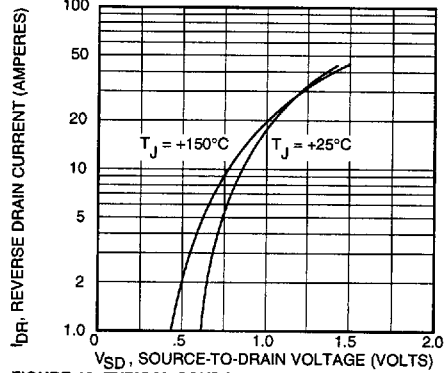
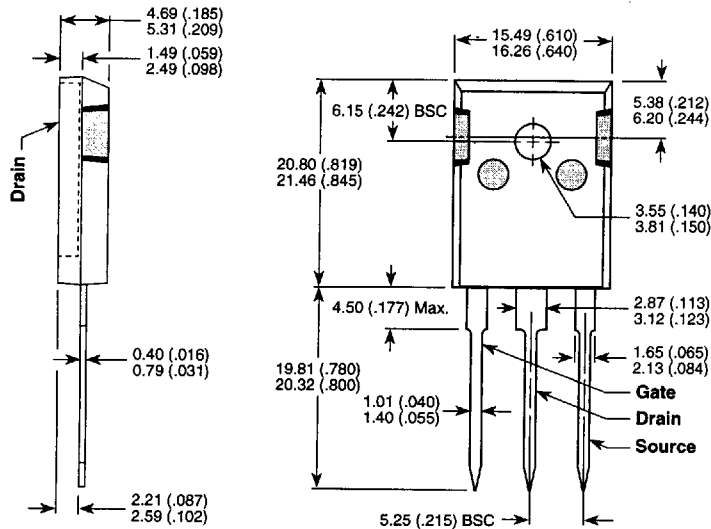


FIGURE 13, TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

TO-247AD Package Outline



Dimensions in Millimeters and (Inches)  
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