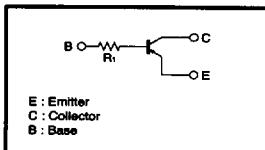


Digital transistor (built-in resistors)

DTA113TKA

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on/off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

Circuit schematic**Electrical characteristics (Ta=25°C)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{ceo}	-50	—	—	V	I _c =-50 μA
Collector-emitter breakdown voltage	BV _{CEO}	-50	—	—	V	I _c =-1mA
Emitter-base breakdown voltage	BV _{CEO}	5	—	—	V	I _c =-50 μA
Collector cutoff current	I _{CEO}	—	—	-0.5	μA	V _{ce} =-50V
Emitter cutoff current	I _{EO}	—	—	-0.5	μA	V _{ce} =-4V
Collector-emitter saturation voltage	V _{CE(sat)}	—	—	0.3	V	I _c /I _e =-10mA/-1mA
DC current transfer ratio	h _{FE}	100	250	600	—	I _c =-1mA, V _{ce} =-5V
Input resistance	R ₁	0.7	1	1.8	kΩ	—
Transition frequency	f _T	—	250	—	MHz	V _{ce} =-10V, I _e =5mA, f=100MHz *

* Transition frequency of mounted transistor.

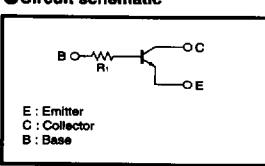
(SPEC-A113T)

Digital transistor (built-in resistors)

DTC123TKA

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on/off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

Circuit schematic**Electrical characteristics (Ta=25°C)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{ceo}	50	—	—	V	I _c =50 μA
Collector-emitter breakdown voltage	BV _{CEO}	50	—	—	V	I _c =1mA
Emitter-base breakdown voltage	BV _{CEO}	5	—	—	V	I _c =50 μA
Collector cutoff current	I _{CEO}	—	—	0.5	μA	V _{ce} =50V
Emitter cutoff current	I _{EO}	—	—	0.5	μA	V _{ce} =4V
Collector-emitter saturation voltage	V _{CE(sat)}	—	—	0.3	V	I _c /I _e =5mA/0.25mA
DC current transfer ratio	h _{FE}	100	250	600	—	I _c =1mA, V _{ce} =5V
Input resistance	R ₁	1.54	2.2	2.86	kΩ	—
Transition frequency	f _T	—	250	—	MHz	V _{ce} =10V, I _e =-5mA, f=100MHz *

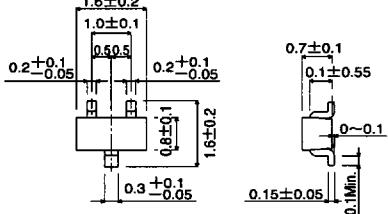
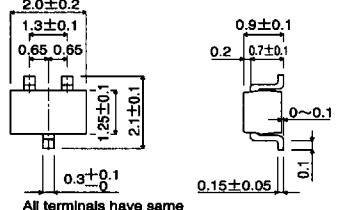
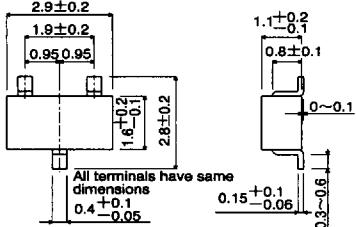
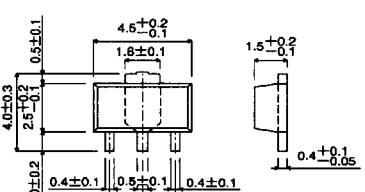
* Transition frequency of mounted transistor.

(SPEC-C123T)

Packages

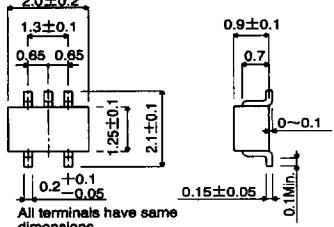
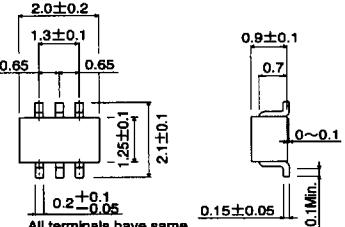
ROHM has been manufacturing transistors since 1975. In the development of products, we constantly strive to anticipate the needs of our customers. Regarding packages, the demands of the market for compactness, low power consumption, low power dissipation and automatic mounting support are becoming ever greater, and we are strengthening our product development system to meet these needs.

●Types and features of surface-mount packages

Type	External dimensions (Units : mm)	Features
EMT3 SC-75A type		A more compact version of the UMT3 (SC-70), the EMT3 is the world's smallest transistor with a mold size of 1.6 × 0.8 mm. The mounting area is approximately 60% of the UMT3 and 30% of the SMT3, making it ideal for ultra-high density mounting. Mounting is possible with the same type of automatic mounting machine as the UMT3.
UMT3 SC-70 type		The UMT3 is a smaller version of the SMT3 (SC-59). The mounting area is approximately 60% of the SMT3, making it optimum for high density mounting. The taping size is the same as the SMT3, allowing use of conventional automatic mounting machines. Electrical characteristics and reliability are the same as the SMT3.
SMT3 SC-59 type		The SMT3 is a compact package suitable for small electronic devices and hybrid IC applications. With proven performance, this is one of the most basic small packages. With the exception of P_c (collector power dissipation), electrical characteristics are similar to leaded packages. Reliability is on the same level as the TO-92.
MPT3 SC-62 type		By itself the MPT3 has a P_c of 0.5 W ($T_a = 25^\circ\text{C}$), but when used on a 40 × 40 × 0.7 mm ceramic board, $P_c = 2$ W ($T_c = 25^\circ\text{C}$), allowing high power to be obtained with a small package. The flat package makes it suitable for applications requiring compactness such as hybrid ICs. Available on tape for automatic mounting.

■ 7828999 0016709 276 ■

Type	External dimensions (Units : mm)	Features
CPT 3 SC-63 type		By itself the CPT3 has a P_c of 1 W ($T_a = 25^\circ\text{C}$), but a large P_c of several watts can be obtained with an appropriate mounting surface. At the same time the CPT3 is compact, making it suitable for high density mounting and hybrid ICs. Available on tape for automatic mounting. For vertical high density mounting, the leaded CPT (SC-64) type with the same mold size is also available.
PSD3		The PSD3 is a TO-220 class surface-mount package. A high P_c can be obtained with an appropriate mounting surface. Surface mounting allows a high vertical density, enabling the design of slim and compact devices. The PSD3 is available on tape for automatic mounting, and it helps improve mounting efficiency and reduce mounting cost.
SMT5 SC-74A type		The SMT5 consists of two connected transistors or digital transistors in an SMT3 (SC-59) package. The mounting area can be reduced by 50% compared to the SMT3 and the internal circuitry is complete, making this package ideal for high density mounting at half the assembly cost.
SMT6 SC-74 type		The SMT6 consists of two independent transistors or two independent digital transistors in an SMT3 (SC-59) package. The mounting area and mounting cost can be reduced by 50% compared to the SMT3, and the two transistors are independent to allow free configuration of a high density circuit.

Type	External dimensions (Units : mm)	Features
UMT5 SC-88A type	 <p>2.0±0.2 1.3±0.1 0.85 0.85 0.65 ± 0.05 1.25±0.1 0.2 ± 0.05 2.1±0.1 0.15±0.05 0.1 Min. 1 All terminals have same dimensions</p>	The UMT5 consists of two connected transistors or digital transistors in a UMT3 (SC-70) package. The mounting area can be reduced by 50% compared to the UMT3 and the internal circuitry is completed, making this package ideal for high density mounting at half the assembly cost.
UMT6 SC-88 type	 <p>2.0±0.2 1.3±0.1 0.85 0.85 0.65 ± 0.05 1.25±0.1 0.2 ± 0.05 2.1±0.1 0.15±0.05 0.1 Min. 1 All terminals have same dimensions</p>	The UMT6 consists of two independent transistors or two independent digital transistors in a UMT (SC-70) package. The mounting area and mounting cost can be reduced by 50% compared to the UMT3, and the two transistors are independent to allow free configuration of a high density circuit.

●Types and features of leaded packages

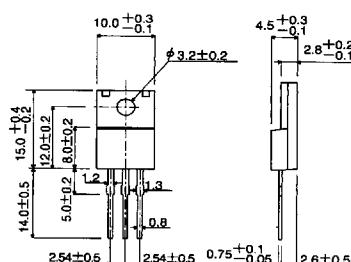
Type	External dimensions (Units : mm)	Features
SPT (SC-72 type)		The SPT is a smaller version of the conventional TO-92 type. The body size ($3 \times 4 \times 2$ mm 3) has been reduced to 1/4 that of the TO-92 ($5 \times 5 \times 4$ mm 3). The SPT is available on tape for automatic insertion, and less space is occupied on the printed circuit board than the TO-92. Reliability is the same as the TO-92.
FTR		SIL type with a height of 3.4 mm and a lead pitch of 2.54 mm.
FTL		The FTL is a radial taping version of the highly popular FTR. This enables automatic high-density mounting with a radial insertion machine.
ATR (SC-71 type)		SC-71type with a height of 4.4 mm and a $P_c=1W$ type.

EXPLANATION

116

■ 7828999 0016712 860 ■

■ 7828999 0016713 777 ■

Type	External dimensions (Units : mm)	Features
TO-220FN		<p>The TO-220FN features the same performance as the TO-220FP with approximately 2 mm less height, allowing the design of slimmer devices. Furthermore, the elimination of support pins in the fin (collector electrode) solves short-circuiting problems with neighboring components and the chassis.</p> <p>To make the height to the installation hole the same as the TO-220FP, it can be replaced as is from the TO-220FP.</p>