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# **HD74HC279**

# Octal D-type Flip-Flops (with Clear)

REJ03D0605-0200 (Previous ADE-205-483) Rev.2.00 Jan 31, 2006

### **Description**

The latch is ideally suited for use as temporary stage for binary information processing and input/output units. When either  $\overline{S}$  or  $\overline{R}$  is low, output is dependent on  $\overline{R}$  input. When both inputs are high, Output is stored before the indicated steady-state input conditions were established. And when both inputs are low, output is high, but this high level are uncontinuance, if either of input goes high.

### **Features**

• High Speed Operation:  $t_{pd}$  ( $\overline{S}$  to Q) = 10 ns typ ( $C_L = 50 \text{ pF}$ )

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 2  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74HC279FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)	
HD74HC279RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)	

Note: Please consult the sales office for the above package availability.

### **Function Table**

In	Output	
<u>S</u> *²	R	Q
Н	Н	$Q_0$
L	Н	Н
Н	L	L
L	L	H* <sup>1</sup>

H: High level L: Low level

Q<sub>0</sub>: The level of Q respectively, before the indicated steady-state input conditions were established.

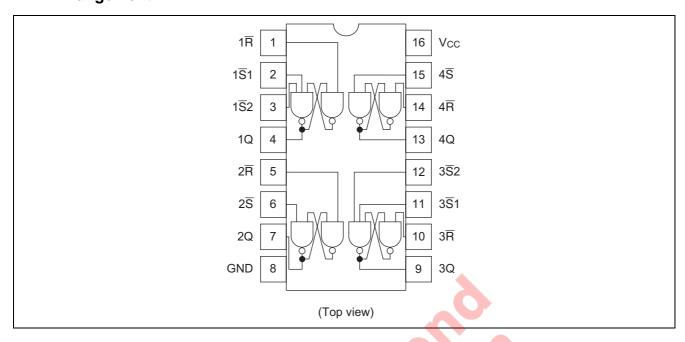
Notes: 1. It is unpredictable, if  $\overline{S}$  or  $\overline{R}$  goes High.

2. As to latches which has two  $\overline{S}$  inputs.

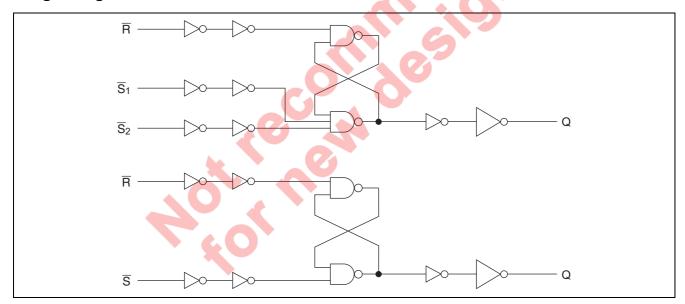
H: Both of  $\overline{S}$  inputs are high.

L: Either or both of  $\overline{S}$  inputs are low.

# **Pin Arrangement**



# **Logic Diagram**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	I <sub>0</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V <sub>CC</sub>	2 to 6	V	
Input / Output voltage	$V_{IN}, V_{OUT}$	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time <sup>*1</sup>	t <sub>r</sub> , t <sub>f</sub>	0 to 1000	ns	V <sub>CC</sub> = 2.0 V
		0 to 500		V <sub>CC</sub> = 4.5 V
		0 to 400		V <sub>CC</sub> = 6.0 V

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

### **Electrical Characteristics**

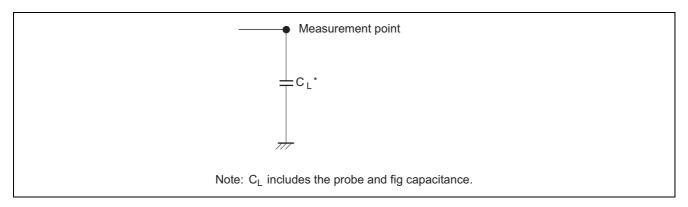
ltom	Cymbol	v ^^	Т	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit		
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15				
		6.0	4.2	_	_	4.2	_			
	V <sub>IL</sub>	2.0	_	_	0.5	_	0.5	V		
		4.5	1		1.35		1.35			
		6.0	I	1	1.8		1.8	•		
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9		V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	  -			
		6.0	5.9	6.0	_	5.9	1			
		4.5	4.18	1	1	4.13	}			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	l	1	5.63				$I_{OH} = -5.2 \text{ mA}$
	$V_{OL}$	2.0	I	0.0	0.1		0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	I	0.0	0.1		0.1			
		6.0		0.0	0.1	_	0.1			
		4.5	4		0.26	<u> </u>	0.33			$I_{OL} = 4 \text{ mA}$
		6.0	1		0.26	_	0.33			$I_{OL} = 5.2 \text{ mA}$
Input current	lin	6.0	١	4	±0.1		±1.0	μΑ	$Vin = V_{CC}$ or $GN$	D
Quiescent supply	Icc	6.0	_	-	2.0	_	20	μΑ	$Vin = V_{CC}$ or $GN$	D, lout = $0 \mu A$
current			4							

# Switching Characteristics

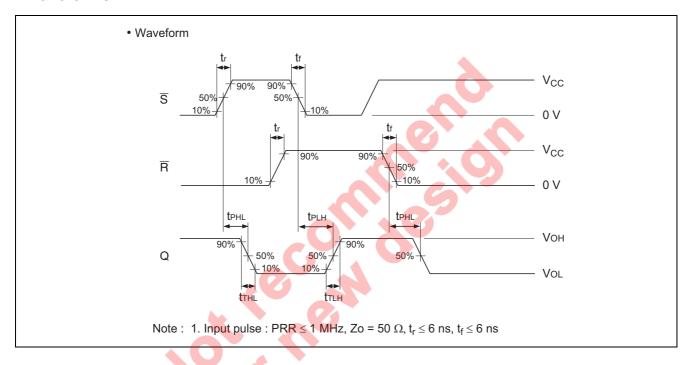
 $(C_L = 50 \text{ pF, Input } t_r = t_f = 6 \text{ ns})$ 

Item	Symbol	Symbol	Symbol	V (\( \)	T	a = 25°	С	Ta = -40	to +85°C	Unit	Test Conditions
item	Syllibol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Oilit	rest Conditions		
Propagation delay	t <sub>PLH</sub>	2.0	_	_	130	_	165	ns	S to Q		
time	t <sub>PHL</sub>	4.5	_	10	26	_	33				
		6.0	_	_	22	_	28				
	t <sub>PHL</sub>	2.0	_	_	120	_	150	ns	R to Q		
		4.5	_	12	24	_	30				
		6.0	_	_	20	_	26				
Output rise/fall	t <sub>TLH</sub>	2.0	_	_	75	_	95	ns			
time	t <sub>THL</sub>	4.5	_	5	15	_	19				
		6.0	_	_	13	_	16				
Input capacitance	Cin	_	_	5	10	_	10	pF			

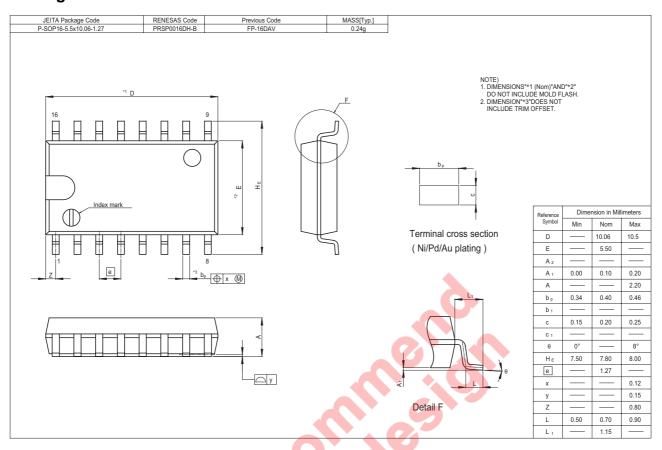
### **Test Circuit**

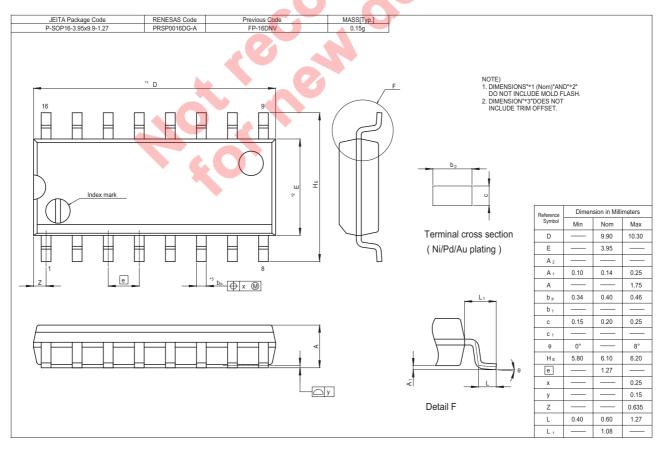


### **Waveforms**



### **Package Dimensions**





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Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510