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# **HD74HC51**

## 2-wide 2-input, 2-wide 3-input AND-OR-INVERT Gate

REJ03D0547-0200 (Previous ADE-205-419) Rev.2.00 Oct 06, 2005

#### **Features**

High Speed Operation: t<sub>pd</sub> = 10.5 ns typ (C<sub>L</sub> = 50 pF)
 High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 μA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 1  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC51P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74HC51FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74HC51RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

#### **Function Table**

 $1Y = \overline{(1A \cdot 1B \cdot 1C)} + \overline{(1D \cdot 1E \cdot 1F)}$ 

	Inputs								
1A	1B	1C	1D	1E	1F	1Y			
Н	Н	Н	Х	Х	Х	L			
Х	Х	Х	Н	Н	Н	L			
L	Х	Х	L	Х	Х	Н			
L	Х	Х	Х	L	Х	Н			
L	Х	Х	Х	Х	L	Н			
X	L	X	L	X	X	L			
Х	L	Х	Х	L	Х	Н			
Х	L	Х	Х	Х	L	Н			
Х	Х	L	L	Х	X	Н			
Х	Х	L	Х	L	X	Н			
Х	Х	L	Х	Х	L	Н			

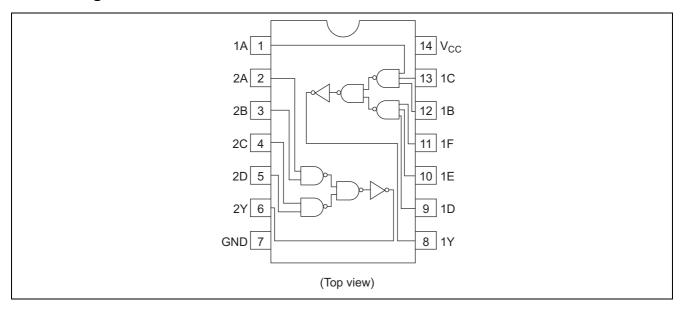
H: High levelL: Low levelX: Irrelevant

 $2Y = \overline{(2A \cdot 2B)} + \overline{(2C \cdot 2D)}$ 

	Output			
2A	2B	2C	2D	2Y
Н	Н	X	X	L
X	X	Н	Н	L
L	X	L	X	Н
L	Х	Х	L	Н
X	L	L	X	Н
X	L	Х	L	Н

H: High level
L: Low level
X: Irrelevant

### **Pin Arrangement**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	l <sub>0</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

## **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V <sub>CC</sub>	2 to 6	V	
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V <sub>CC</sub> = 2.0 V
Input rise / fall time*1	$t_r$ , $t_f$	0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

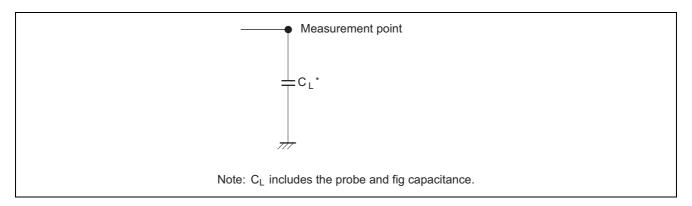
### **Electrical Characteristics**

			Ta = 25°C		Ta = -40 to+85°C					
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	ns
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	$V_{IL}$	2.0	1		0.5		0.5	V		
		4.5	I		1.35		1.35			
		6.0	_	_	1.8	_	1.8			
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}  _{OH} =$	= –20 μA
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	_	_	4.13	_		I <sub>OH</sub> =	-4 mA
		6.0	5.68	_	_	5.63	_		I <sub>OH</sub> =	-5.2 mA
	V <sub>OL</sub>	2.0	1	0.0	0.1		0.1	V	$Vin = V_{IH} \text{ or } V_{IL}   I_{OL} =$	20 μΑ
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	_	0.33		I <sub>OL</sub> =	4 mA
		6.0	_	_	0.26	_	0.33		I <sub>OL</sub> =	5.2 mA
Input current	lin	6.0		_	±0.1		±1.0	μΑ	Vin = V <sub>CC</sub> or GND	
Quiescent supply current	I <sub>CC</sub>	6.0	_	_	1.0	_	10	μΑ	$Vin = V_{CC}$ or GND, lo	ut = 0 μA

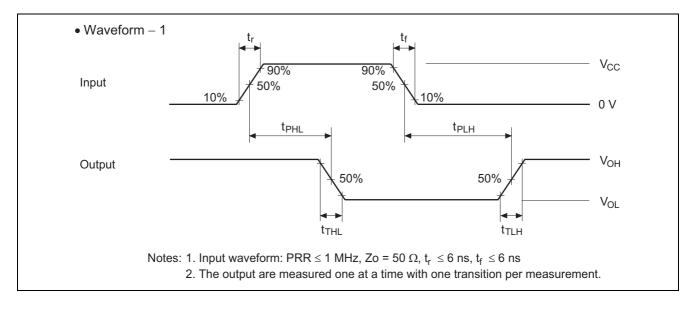
## Switching Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

			Т	a = 25°	С	Ta = -40	to +85°C		
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	<b>Test Conditions</b>
Propagation delay	t <sub>PLH</sub>	2.0	_	_	110	_	140	ns	
time		4.5	_	11	22	_	28		
		6.0	_	_	19	_	24		
	t <sub>PHL</sub>	2.0	_	_	110	_	140	ns	
		4.5	_	10	22	_	28		
		6.0	_	_	19	_	24		
Output rise time	t <sub>TLH</sub>	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19		
		6.0	_	_	13	_	16		
Output fall time	t <sub>THL</sub>	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19		
		6.0	_	_	13	_	16		
Input capacitance	Cin	_	_	5	10	_	10	pF	

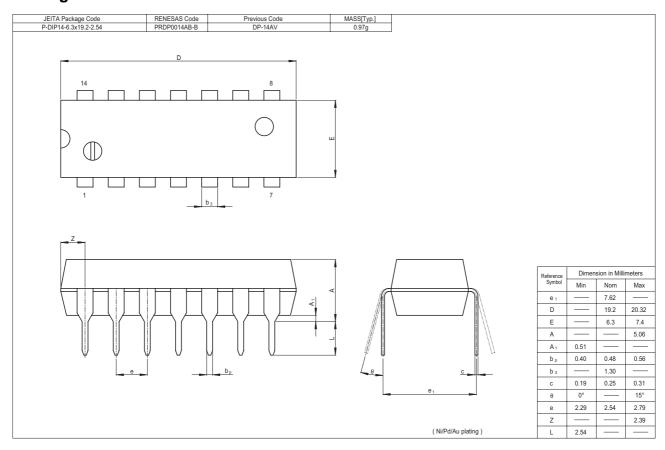
### **Test Circuit**

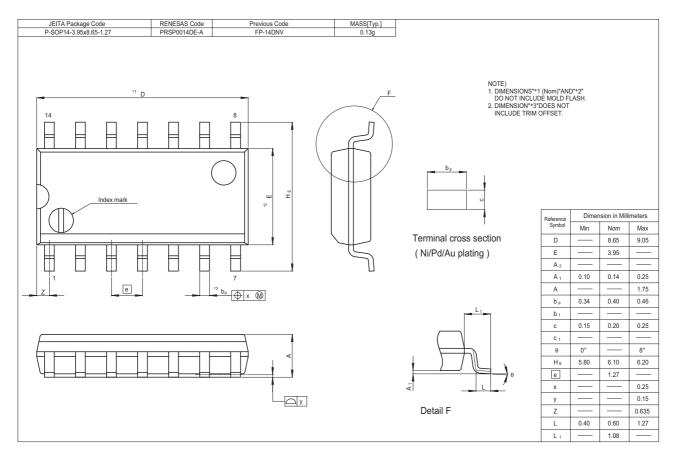


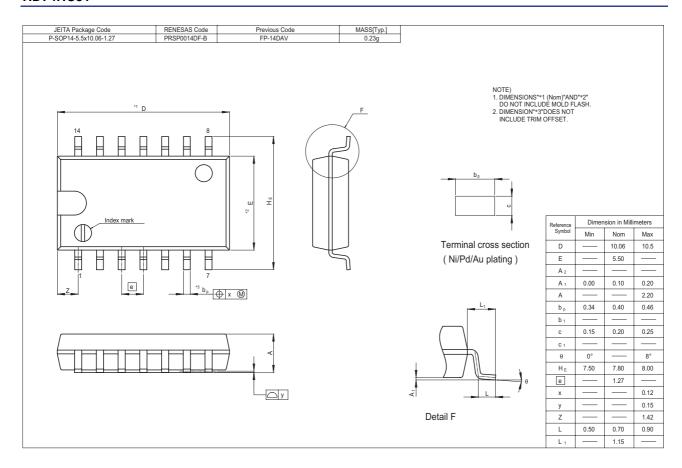
#### **Waveforms**



### **Package Dimensions**







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