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# **HD74LS240**

# Octal Buffers / Line Drivers / Line Receivers (inverted three-state outputs)

REJ03D0459-0200 Rev.2.00 Feb.18.2005

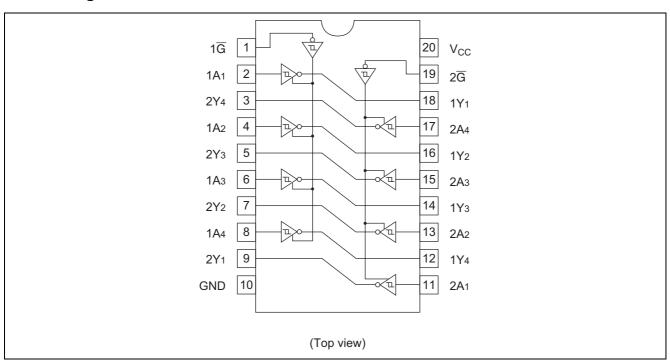
### **Features**

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS240P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	_
HD74LS240FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74LS240RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

# **Pin Arrangement**

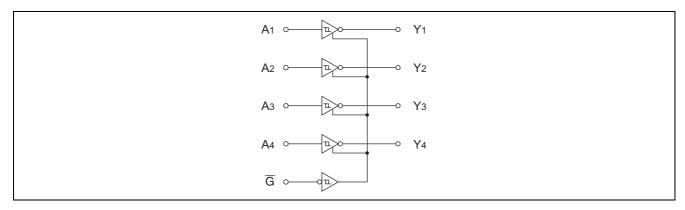


## **Function Table**

Inp	Output	
G	Α	Y
Н	X	Z
L	Н	L
L	L	Н

Note: H; high level, L; low level, X; irrelevant, Z; off (high-impedance) state of a 3-state output

# Block Diagram (1/2)



# **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	7	V
Input voltage	V <sub>IN</sub>	7	V
Power dissipation	P <sub>T</sub>	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

# **Recommended Operating Conditions**

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	$V_{CC}$	4.75	5.00	5.25	V
Output ourrant	I <sub>OH</sub>	_	_	<b>-15</b>	mA
Output current	I <sub>OL</sub>	_	_	24	mA
Operating temperature	Topr	-20	25	75	°C

# **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$ 

It	em	Symbol	min.	typ.*	max.	Unit	Condition		
Input voltage		$V_{IH}$	2.0	_	_	V			
		V <sub>IL</sub>	_	_	0.8	V			
Hysteresis	i	$V_T^+ - V_T^-$	0.2	0.4	_	V	V <sub>CC</sub> = 4.75 V		
		V	2.4	_	_	V	V <sub>IL</sub> = 0.8 V, I <sub>OH</sub> =	= – 3 mA	$V_{CC} = 4.75$
Output val	togo	V <sub>OH</sub>	2.0	_	_	V	V <sub>IL</sub> = 0.5 V, I <sub>OH</sub> =	= – 15 mA	$V$ , $V_{IH} = 2 V$
Output vol	ıaye	V	_	_	0.4	V	I <sub>OL</sub> = 12 mA	V <sub>CC</sub> = 4.75 \	/, V <sub>IH</sub> = 2 V,
		V <sub>OL</sub>	_	_	0.5	V	I <sub>OL</sub> = 24 mA	V <sub>IL</sub> = 0.8 V	
Off state o	utout ourront	l <sub>ozh</sub>	_	_	20	μΑ	V <sub>O</sub> = 2.7 V	V <sub>CC</sub> = 5.25 \	/, V <sub>IH</sub> = 2 V,
OII-State 0	utput current	I <sub>OZL</sub>	_	_	-20	μΑ	V <sub>O</sub> = 0.4 V V <sub>IL</sub> = 0.8 V		
			_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_I = 2.7 \text{ V}$		
Input curre	ent	I <sub>IL</sub>	_	_	-0.2	mA	V <sub>CC</sub> = 5.25 V, V <sub>I</sub> = 0.4 V		
		l <sub>1</sub>	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}$	ı = 7 V	
Short-circu	iit output	los	-40	_	-225	mA	V <sub>CC</sub> = 5.25 V		
	Outputs high		_	13	23				
Supply Outputs current** low		I <sub>CC</sub>	_	26	44	mA	V <sub>CC</sub> = 5.25 V		
	All outputs disabled		_	29	50				
Input clamp voltage		V <sub>IK</sub>	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN}$	$_{N} = -18 \text{ mA}$	

Notes:  $V_{CC} = 5 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ 

# **Switching Characteristics**

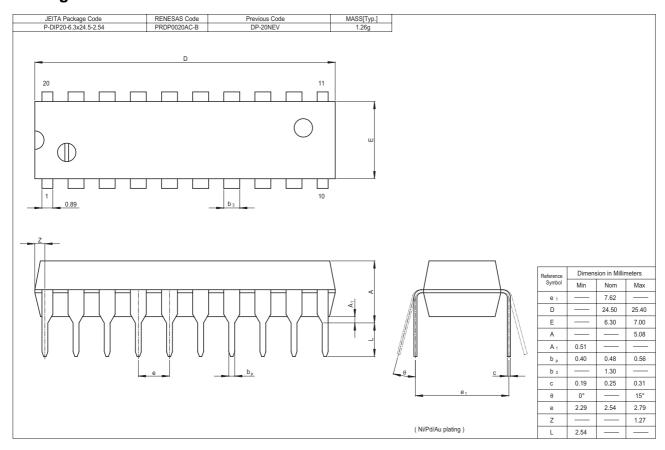
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$ 

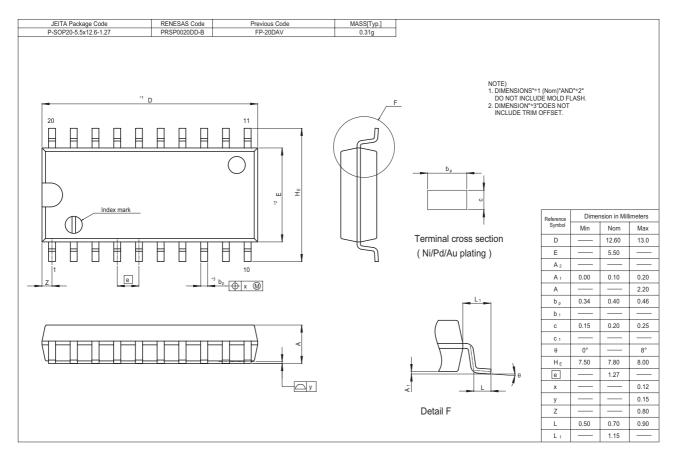
Item	Symbol	min.	typ.	max.	Unit	Condition	
Propagation delay time	t <sub>PLH</sub>	_	9	14	ne		
Fropagation delay time	t <sub>PHL</sub>	_	12	18	ns	$C_L = 45 \text{ pF}, R_L = 667 \Omega$	
Output anable time	t <sub>ZL</sub>	_	20	30	ns	$G_{L} = 45  \text{pr},  \text{RL} = 607  \text{S2}$	
Output enable time	t <sub>ZH</sub>	_	15	23	ns		
Output disable time	$t_{LZ}$	_	15	25	ns	$C_L = 5 \text{ pF}, R_L = 667 \Omega$	
Output disable tillle	t <sub>HZ</sub>	_	10	18	ns	$O_{L} = 5 \text{ pr}, N_{L} = 667 \Omega$	

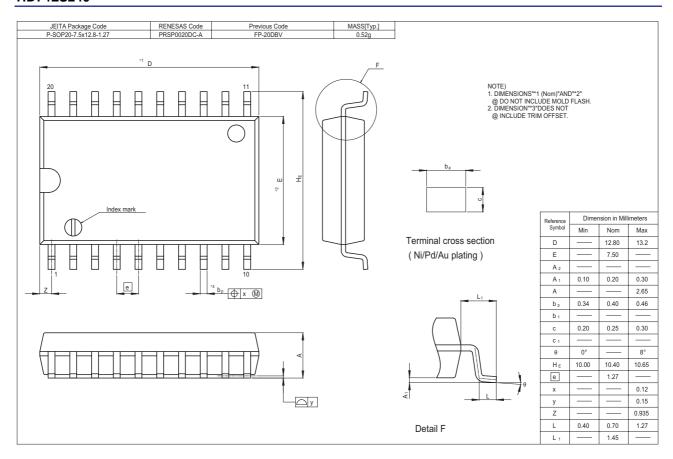
Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

 $<sup>^{\</sup>star\star}$   $\,$   $I_{\text{CC}}$  is measured with all outputs open.

# **Package Dimensions**







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