

ROFS

Description

The HR Series of DC-DC converters represents versatile power supplies ideally suitable for use in transportation and other advanced electronic systems. Features include a very broad input voltage range, very high efficiency, high reliability, low output voltage noise, and excellent dynamic response to load/line changes. They can be connected to all conventional railway batteries.

The converter inputs are protected against surges and transients. An input over- and undervoltage lockout circuitry disables the outputs if the input voltage is outside of the specified range. To avoid high input current at operation with high-voltage batteries, the inhibit input allows for adjusting of the undervoltage lockout to a suitable level, thus allowing the use of an appropriate input fuse.

The converters exhibit an inrush current limiter, preventing circuit breakers and fuses from tripping at switch-on.

The outputs are open- and short-circuit proof.

Full input-to-output, input-to-case, output-to-case, and output

Features

- Extremely wide input voltage range from 12 to 168 VDC in the same converter
- RoHS-compliant
- · Class I equipment
- Compliant with EN 50155, EN 50121-3-2, EN 45545.
- Input over- and programmable undervoltage lockout
- Shutdown function
- Inrush current limitation
- Interruption time 10 ms
- Adjustable output voltages
- 2 independent, isolated outputs: no load, overload, and short-circuit proof
- · Rectangular current limiting characteristic
- · Parallel operation with active current sharing
- Very high efficiency up to 94%
- Immunity according to IEC 61000-4-2, -3, -4, -5, -6
- · ALL PCB boards protected by lacquer
- Very high reliability

Safety-approved according to IEC/EN 60950-1, UL/CSA 60950-1 2nd Ed.





to output isolation is provided. The converters are particularly suitable for railway applications and can be supplied by all common railway batteries of 24 V, 36 V, 48 V, 72 V, 96 V,110 V, and 120 V nominal voltage. All boards are coated with a protective lacquer.

The case design allows operation at nominal load up to 71 $^{\circ}$ C with natural cooling. If forced cooling is provided, the ambient temperature may exceed 71 $^{\circ}$ C, but the case temperature must remain below 95 $^{\circ}$ C.

A temperature sensor generates an inhibit signal, which disables the outputs when the case temperature $T_{\rm C}$ exceeds the limit. The outputs are automatically re-enabled when the temperature drops below the limit.

LED indicators display the status of the converter and allow for visual monitoring of the system at any time.

The converters may either be plugged into a 19" rack system according to IEC 60297-3, or be chassis mounted. Two heat sinks of different size and cooling plates for chassis mounting (option B, B1) are available.

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Model Selection

Table 1: Model Selection

Out	put 1	Out	put 2	Power		Input voltage		η:	24 ¹	η 1	10 2	Model	Opt.
V _{o nom} [V]	I _{onom} [A]	V _{onom} [V]	I _{onom} [A]	P _{o nom} [W]	V _{i min} ³ [V]	V _{i cont} [V]	V _{imax} ³ [V]	min. [%]	typ. [%]	min. [%]	typ. [%]		
12	20			240	12	16.8 to 150	168	91	92.5	92	94.5	HR2320-9RG ⁴	B, B1
12	12			144	12	16.8 to 150	168	91	92.5	92	94.5	HRL2320-9RG ⁴	
12	24			288	12	16.8 to 150	168	91	92.5	92	94.5	HRP2320-9RG4	
15	16			240	12	16.8 to 150	168	91	92.5	92	94.5	HR2540-9RG ⁴	
15	19.2			288	12	16.8 to 150	168	91	92.5	92	94.5	HRP2540-9RG4	
48	4			192	12	16.8 to 150	168	89.5	90.5	92	93	HR2880-9RG ⁴	
48	5			240	12	16.8 to 150	168	89.5	90.5	92	93	HRP2880-9RG ⁴	
12	10	12	10	240	12	16.8 to 150	168	91	92.5	92	94.5	HR2320-9RG	B, B1
12	6	12	6	144	12	16.8 to 150	168	91	92.5	92	94.5	HRL2320-9RG	
12	12	12	12	288	12	16.8 to 150	168	91	92.5	92	94.5	HRP2320-9RG	
15	8	15	8	240	12	16.8 to 150	168	91	92.5	92	94.5	HR2540-9RG	
15	9.6	15	9.6	288	12	16.8 to 150	168	90	92.5	92	94.5	HRP2540-9RG	
48	2	48	2	192	12	16.8 to 150	168	89.5	90.5	92	93	HR2880-9RG	
48	2.5	48	2.5	240	12	16.8 to 150	168	89.5	90.5	92	93	HRP2880-9RG	
24	10			240	12	16.8 to 150	168	91	92.5	92	94.5	HR2320-9RG ⁵	B, B1
24	6			144	12	16.8 to 150	168	91	92.5	92	94.5	HRL2320-9RG 5	
24	12			288	12	16.8 to 150	168	91	92.5	92	94.5	HRP2320-9RG 5	
30	8			240	12	16.8 to 150	168	91	92.5	92	94.5	HR2540-9RG ⁵	
30	9.6			288	12	16.8 to 150	168	91	92.5	92	94.5	HRP2540-9RG ⁵	
96	2			192	12	16.8 to 150	168	89.5	90.5	92	93	HR2880-9RG 5	
96	2.5			240	12	16.8 to 150	168	89.5	90.5	92	93	HRP2880-9RG 5	

¹ Efficiency at $T_A = 25$ °C, $V_i = 24$ V, $I_{o nom}$, $V_{o nom}$ ² Efficiency at $T_A = 25$ °C, $V_i = 110$ V, $I_{o nom}$, $V_{o nom}$

³ Short time; see table 2 for details

⁴ Both outputs connected in parallel

⁵ Both outputs connected in series





Part Number Description

Operating input voltage V _{i cont} (continuously): 16.8 – 150 VDC HR, HRL, HRP Number of outputs	HR 2 5 40 -9 R B1 G
Nominal voltage of main output Vo1 nom	
12 V 3 15 V 5 24 V 6 36 V 7 48 V 8 Other voltages 1 9	
Nominal voltage of tracking output V _{o2} ³ 12 V	
12 V	
24 V	
Operational temperature range: T _A :	
$T_{\rm A}$ = -40 to 71 °C, $T_{\rm C} \le 95$ °C	
Auxiliary functions and options:	
Output voltage control inputR Cooling plate standard caseB, B1 Cooling plate for long case 220 mm ² B2 ² RoHS-compliant for all 6 substancesG ⁴	

¹ Customer-specific models. No safety-relevant changes compared to the respective basic model, e.g. different mechanical details, special markings, mounted front plates, reduced output voltage, etc.

- ² Converters with 220 mm case (customer-specific models). Add 5000 to the model number!
- ³ The nominal voltages of both outputs are always equal.

⁴ G is always placed at the end of the part number.

Note: The sequence of options must follow the order above.

Example: HR2540-9RB1G: DC-DC converter, operating input voltage range 16.8 – 150 VDC, 2 isolated outputs, each providing 15 V, 8 A, control input R to adjust the output voltages, cooling plate B1, and RoHS-compliant for all six substances.

Product Marking

Basic type designation: applicable approval marks, CE mark, warnings, pin designation, patents and company logo, identification of LEDs.

Specific type designation: input voltage range, nominal output voltages and currents, degree of protection, batch no., serial no., and data code including production site, modification status, and date of production.





Functional Description

The input voltage is fed via an efficient filter to the interleaved switching boost converter, which provides the intermediate circuit voltage on the bulk capacitor $C_{\rm b}$. The inrush current is limited by the resistor $R_{\rm inr}$, which is shorted by $V_{\rm inr}$ after the bulk capacitor was charged.

The bulk capacitor sources a single-transistor forward converter with active clamp and provides the power during the interruption time of 10 ms.

The main transformer exhibits two separate secondary windings for the two outputs. The resultant voltages are rectified by synchronous rectifiers (not models with $V_0 = 2 \times 48 \text{ V}$) in order to provide the best efficiency. Their ripple voltages are smoothed by a dual power choke and output filters. The control logic senses the main output voltage V_{01} and generates the control signal for the forward converter, with respect to the max. output current transferred via magnetic feedback to the control circuit of the forward converter, located on the primary side.

The second output voltage is tracking the main output, but has

its own current limiting circuit. If the main output voltage drops due to current limitation, the second output voltage will drop as well and vice versa.

The output voltages can be adjusted by external means. Parallel operation of several converters is possible by interconnecting the T-pins to provide active current sharing. Both outputs can be connected in parallel or in series without any precaution. They exhibit a rectangular current limitation characteristic. Switchable preloads V_{PL} (Version V101 or later) ensure good regulation even with no load at one output.

A control output (D) and two LEDs signal correct operation of the converter. In case of an output overvoltage, the converter is disabled by a latch.

Input over- and undervoltage lockout is provided. The undervoltage trigger level can be adjusted by an external resistor connected to PUL (pin 24) depending on the nominal voltage of the supplying battery.

Temperature sensors on the primary and secondary side prevent the converter from excessive warm-up.

A cooling plate for chassis-mounting is available (opt. B, B1).

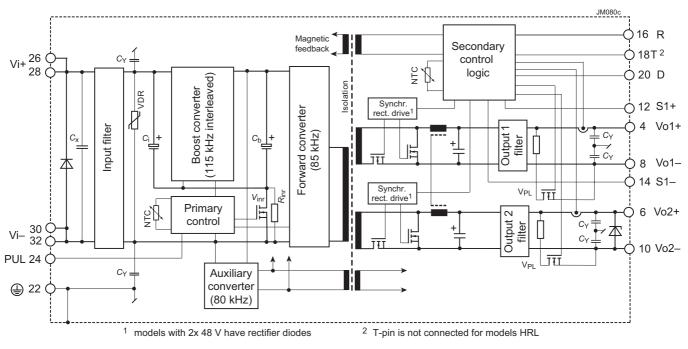


Fig. 1 Block diagram of HR2320, version V101 (or later)





Electrical Input Data

General Conditions:

- $T_A = 25$ °C, unless T_C is specified.
- Pin 24 (PUL) left open-circuit
- Pin 16 (R) and 18 (D) left open-circuit.

Table 2: Input data

Mode	I			HR			HRL			HRP		Unit
Chara	acteristics	Conditions	min	typ	max	min	typ	max	min	typ	max	
Vi	Operating input voltage	$I_{\rm o} = 0 - I_{\rm o max}$	16.8		150	16.8		150	16.8		150	V
	for ≤ 2 s, without shutdown	$T_{\rm C min} - T_{\rm C max}$	12.0		168	12.0		168	12.0		168	
Vinom	Nom. input voltage range		24	(110)	120	24	(110)	120	24	(110)	120	
V _{iabs}	Input voltage limits	3 s, without damage	0		176	0		176	0		176	
/i	Input current: HR2320 HR2540 HR2880	V _{i max} (110 V) V _{i min} , I _{o nom}	1.76 1.76 1.37	(2.36) (2.36) (1.86)	15.65 15.65 12.82	1.03	(1.38)	9.31	2.1 2.1 1.71	(2.83) (2.83) (2.33)	21.4 21.4 16.05	A
P _{i0}	No-load input power	$V_{\rm i min} - V_{\rm i max}, I_{\rm o} = 0$			11			11			11	W
P _{i inh}	Idle input power	$V_{i \min} - V_{i \max}, V_{PUL} = 0 V$			2.5			2.5			2.5	
Cx	Input capacitance 1			8.6			8.6			8.6		μF
Ri	Input resistance	-		10			10			10		mΩ
I _{inr p}	Peak inrush current ²	V _i = 150 V, <i>I</i> _{o nom}		30			30			30		А
t _{inrr}	Time constant of Iinr			10			10			10		ms
t _{d on}	Start-up time	$0 \rightarrow V_{i \min}, I_{o nom}$		400			400			400		
t _r	Rise time after shutdown	$V_{\rm i} \ge 16.8 {\rm V}, I_{\rm o nom}, V_{\rm PUL} = 0 \rightarrow 5 {\rm V}$		40			40			40		

¹ At start-up (not smoothed by the inrush current limiter)

² According to ETS 300132-2

PUL Function and Fuse

This converter is designed for an extremly wide input voltage range, allowing for connection to all common railway batteries. However, the programmable input undervoltage lockout (PUL, pin 24) should be adjusted adequately in order to limit the high input current at start-up; see fig 2.

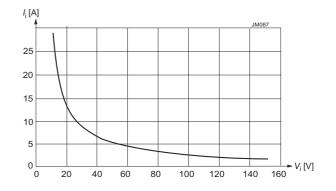
Table 3 specifies the values of the resistor R_{PUL} , connected between PUL and Vi–, versus the resultant minimum input voltage and the recommended fuse.

Fig. 3 shows more values of $R_{\rm PUL}$ versus start-up voltage. For stationary batteries, a higher start-up voltage might be advatageous.

Note: If PUL (pin 24) is connected to Vi– (pin 30/32), the converter is disabled; see *Shutdown Function*.

No fuse is incorporated in the converter. Consequently, an external fuse or a circuit breaker must be installed at system level to protect against severe defects.

Reverse polarity protection is provided by an antiparallel diode across the input, causing the external input fuse or circuit breaker to trip.





Typ. input current versus input voltage at nominal load (HR2320)







Table 3: PUL Specification (typ.) and recommended external fuse for all models. Smaller fuses are possible for HRL models.

Battery	R _{PUL}	V _{i min} (o	on / off)	Fuse rating						
24 V	00	15 V	12 V ³	25 A, fast, Littlefuse 3141						
36 V	16.9 kΩ	20 V	18 V	16 A, fast, Schurter /SP ²						
48 V	13.7 kΩ	26 V	20 V	12.5 A, fast, Schurter /SP ²						
72 V	9.5 kΩ	38 V	32 V	8 A, fast, Schurter /SP ²						
110 V	5.2 kΩ	62 V	57 V	6.3 A, slow, BEL fuse MRT ²						
120 V	2.9 kΩ	90 V	84 V	5 A, slow, BEL fuse MRT ²						
all	< 100 Ω		Converter disabled							

 1 fuse size 6.3 \times 32 mm $^{-2}$ fuse size 5 \times 20 mm $^{-3}$ for \leq 2 s $^{-1}$

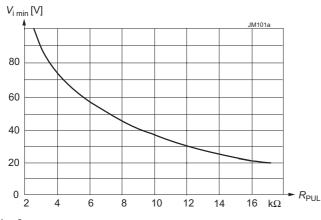


Fig. 3

R_{PUL} versus switch-on voltage

Input Transient Protection

The double stage symmetrical input filter together with a VDR (voltage depending resistor) form an effective protection against high input transient voltages which typically occur in battery-driven mobile applications.

At very high input voltage, the overvoltage lockout disables the converter such protecting it from damage.

Inrush Current Limitation

The converters exhibit an electronic inrush current limiting circuit. This circuit is also functional, when the input voltage is removed and immediately reapplied.

However, several capacitors are directly connected to the input pins. Consequently, a peak curent is still present when applying the input voltage.

The inrush current peak value can be determined by following calculation; see also fig. 4:

$$V_{\text{inr p}} = \frac{V_{\text{i source}}}{(R_{\text{ext}} + R_{\text{i}})}$$

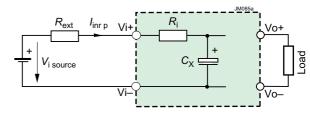


Fig. 4 Equivalent input ciruit

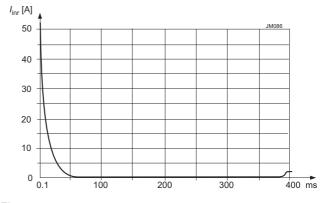


Fig. 5 Inrush current at V_i = 150 V, I_{o nom} (HR2320)

Efficiency

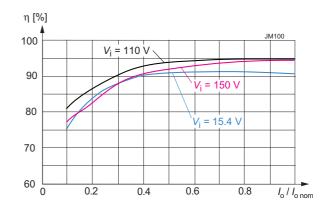


Fig. 6 Efficiency versus V_i and I_o (HR2320, both outputs connected in series)





Electrical Output Data

General Conditions: – T_A = 25 °C, unless T_C is specified; Pin 24 (PUL) \ge 5 V

Table 4a: Output data of HR2320 and HRL2320

Model Nom. d	Nodel Nom. output voltage					HR2320 2 × 12 V							HRL2320 2 × 12 V					
				Output 1 Output				2	Output 1			Output 2			1			
Charac	cteristics		Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max			
Vo	Output vo	oltage	V _{inom} , 0.5 I _{onom}	11.93	12.0	12.07		12.0		1.93	12.0	12.07		12.0		V		
$V_{\rm 0BR}$	Output pi (suppres	rotection sor diode)	Output 2				14.4		15.9				14.4		15.9			
I _{o nom}	Output cu	urrent nom.	V _{i min} – V _{i max}		10			10			6.0			6.0		Α		
I _{01L} , I _{02L}	Output cu	urrent limit ¹	$T_{\rm Cmin} - T_{\rm Cmax}$	10.5			10.5			6.5			6.5					
I _{o12L}	Output cu	Irrent limit ^{1 2}	-	21 ²						13						-		
Vo	Output no spikes	oise incl.	V _{i nom} , I _{o nom} BW = 20 MHz			60			60			60			60	mV _{pp}		
$V_{ m oadj}$	Adjustme	nt by R-input ⁴	V _{i min} – V _{i max}	4.8		13.8 ¹		3		4.8		13.8 ¹		3		V		
ΔV _{ou}		e/load regulation iation of V_{o})	(0.1 – 1) / _{o nom}			±120		3				±120		3		mV		
V _{od}	Dynamic load	Voltage deviation ⁵	$V_{i \text{ nom}}, 0.5 I_{o2 \text{ nom}}$ $I_{o1 \text{ nom}} \leftrightarrow 0.5 I_{o1 \text{ nom}}$		±200			±200			±150			±150				
t_{od}	regulat.	Recovery time ⁵	and after turn on		1			3			1			3		ms		
α^{vo}	Temperat of output	ture coefficient voltage	$\frac{T_{\rm Cmin} - T_{\rm Cmax}}{I_{\rm onom}}$		+0.01	+0.02					+0.01	+0.02				%/K		

Table 4b: Output data of HRP2320 models

Model Nom. d	output vo	ltage					P2320 12 V			Unit
				0	utput	1	0	utput	2	
Charac	teristics		Conditions	min	typ	max	min	typ	max	
Vo	Output vo	oltage	V _{inom} , 0.5 / _{onom}	11.93	12.0	12.07		12.0		V
V _{o BR}	Output pr (suppres	otection sor diode)	Output 2				14.4		15.9	
I _{o nom}	Output cu	irrent nom.	V _{i min} – V _{i max}		12			12		А
I _{01L} , I _{02L}	Output cu	ırrent limit ¹	$T_{\rm Cmin} - T_{\rm Cmax}$		12.3			12.3		
I _{o12L}	Output cu	rrent limit ^{1 2}	-	24.6 ²						
Vo	Output no spikes	bise incl.	V _{i nom} , / _{o nom} BW = 20 MHz			60			60	$\mathrm{mV}_{\mathrm{pp}}$
$V_{ m o adj}$	Adjustme	nt by R-input ⁴	V _{i min} – V _{i max}	4.8		13.8 ¹		3		V
ΔV _{ou}		/load regulation iation of $V_{\rm o}$)	(0.1 – 1) / _{o nom}			±120		3		mV
V _{od}	Dynamic load	Voltage deviation ⁵	V _{i nom} , 0.5 / _{o2 nom} I _{o1 nom} ↔ 0.5 I _{o1 nom}		±250			±250		
t _{od}	regulat.	Recovery time ⁵	and after turn on		1			3		ms
ανο	Temperat of output	ure coefficient voltage	$T_{\rm C min} - T_{\rm C max}$ $I_{\rm o nom}$		+0.01	+0.02				%/K

¹ If V_0 is increased above $V_{0 \text{ nom}}$ through R-, sense, or T-input, the output currents should be reduced so that $P_{0 \text{ nom}}$ is not exceeded.

² Both outputs connected in parallel

³ See Output voltage regulation

⁴ For battery charger application, a defined negative temp. coefficient can be provided by using a temp. sensor (see Accessories)

⁵ See Dynamic load regulation

 $^{6}\,$ Measured with a ceramic cap of 1 μF across each output.



Model Nom. d	Model Nom. output voltage				HR2540 2 × 15 V						HRP2540 2 × 15 V					
				Output 1			Output 2			Output 1			Output 2]
Charac	cteristics		Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max	1
Vo	Output vo	oltage	V _{inom} , 0.5 / _{onom}	14.91	15.0	15.09		15.0		14.91	15.0	15.09		15.0		V
V _{o BR}	Output pi (suppres	otection sor diode)	Output 2				20.9		23.1				20.9		23.1	
I _{o nom}	Output cu	irrent nom.	V _{i min} – V _{i max}		8			8			9.6			9.6		A
I _{01L} , I _{02L}	Output cu	ırrent limit ¹	$T_{\rm Cmin} - T_{\rm Cmax}$	8.4			8.4			10.1			10.1]
I _{o12L}	Output cu	Irrent limit ^{1 2}		16.8 ²	2					19.7 ²						
Vo	Output no spikes	bise incl.	V _{i nom} , I _{o nom} BW = 20 MHz			75			75			75			75	mV _{pp}
$V_{ m o adj}$	Adjustme	nt by R-input ⁴	V _{i min} – V _{i max}	6.0		17.25 ¹		3		6.0		17.25 ¹		3		V
ΔV _{ou}		e/load regulation iation of $V_{\rm o}$	(0.1 – 1) / _{o nom}			±150		3				±150		3		mV
V _{od}	Dynamic load	Voltage deviation ⁵	V _{i nom} , 0.5 / _{o2 nom} I _{o1 nom} ↔ 0.5 I _{o1 nom}		±250			±250			±300			±300		
t _{od}	regulat.	Recovery time ⁵	and after turn on		1			3			1			3		ms
α_{vo}	Temperat of output	ture coefficient voltage	T _{C min} – T _{C max} I _{o nom}		+0.01	+0.02					+0.01	+0.02				%/K

Table 4c: Output data of HR2540 and HRP2540. General conditions as per table 6a

¹ If the output voltages are increased above $V_{o nom}$ through R-input control, remote sensing, or option T, the output currents should be reduced accordingly so that $P_{o nom}$ is not exceeded.

² Both outputs connected in parallel

³ See Output voltage regulation

⁴ For battery charger applications, a defined negative temperature coefficient can be provided by using a temperature sensor (see *Accessories*)

⁵ See Dynamic load regulation

 $^{6}\,$ Measured with a ceramic cap of 1 μF across each output.





Model Nom. c	/lodel lom. output voltage					HR2880 2 × 48 V						HRP2880 2 × 48 V					
				Output 1			Output 2			Output 1			Output 2			1	
Charac	teristics		Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max		
Vo	Output vo	oltage	V _{inom} , 0.5 / _{onom}	47.7	48.0	48.3		48.0		47.7	48.0	48.3		48.0		V	
V _{o BR}	Output pr (suppres	otection sor diode)	Output 2				56.7		62.7				56.7		62.7		
I _{o nom}	Output cu	irrent nom.	V _{i min} – V _{i max}		2.0			2.0			2.5			2.5		А	
I _{01L} , I _{02L}	Output cu	ırrent limit ¹	$T_{\rm Cmin} - T_{\rm Cmax}$	2.1			2.1			2.625			2.625				
I _{o12L}	Output cu	Irrent limit ^{1 2}	-	4.2 ²						5.25 ²							
Vo	Output no spikes	bise incl.	V _{i nom} , I _{o nom} BW = 20 MHz			240			240			240			240	mV _{pp}	
$V_{ m oadj}$	Adjustme	nt by R-input ⁴	V _{i min} – V _{i max}	19.2		55.2 ¹		3		19.2		55.2 ¹		3		V	
ΔV _{ou}		e/load regulation iation of $V_{\rm o}$)	(0.1 – 1) / _{o nom}			±0.5		3				±0.5		3			
V _{od}	Dynamic load	Voltage deviation ⁵	V _{i nom} , 0.5 <i>I</i> _{o2 nom} <i>I</i> _{o1 nom} ↔ 0.5 <i>I</i> _{o1 nom}		±0.8			±0.8			±1.0			±1.0			
t _{od}	regulat.	Recovery time ⁵	and after turn on		1			3			1			3		ms	
ανο	Temperat of output	ture coefficient voltage	$T_{\rm C min} - T_{\rm C max}$ $I_{\rm o nom}$		+0.01	+0.02					+0.01	+0.02				%/K	

Table 4d: Output data of HR2880 and HRP2880. General conditions as per table 6a

¹ If the output voltages are increased above $V_{o nom}$ through R-input control, remote sensing, or option T, the output currents should be reduced accordingly so that $P_{o nom}$ is not exceeded.

² Both outputs connected in parallel

³ See Output voltage regulation

⁴ For battery charger applications, a defined negative temperature coefficient can be provided by using a temperature sensor (see *Accessories*)

⁵ See Dynamic load regulation

⁶ Measured with a ceramic cap of 1 µF across each output.



Thermal Considerations

If a converter is located in free, quasi-stationary air (convection cooling) at the indicated maximum ambient temperature $T_{A max}$ (see table *Temperature specifications*) and is operated within the specified input voltage range and nominal load, the temperature measured at the *Measuring point of case temperature* T_C (see *Mechanical Data*) will approach the indicated value $T_{C max}$ after the warm-up phase. However, the relationship between T_A and T_C depends heavily upon the conditions of operation and integration into a system. The thermal conditions are influenced by input voltage, output current, airflow, and temperature of surrounding components and surfaces. $T_{A max}$ is therefore, contrary to $T_{C max}$, an indicative value only.

Caution: The installer must ensure that under all operating conditions $T_{\rm C}$ remains within the limits stated in the table *Temperature specifications*.

Notes: Sufficient forced cooling or enhanced cooling with the help of cooling plates (options B, B1) allows for T_A to be higher than 71 °C (e.g. 85 °C), as long as $T_{C max}$ is not exceeded.

Thermal Protection

Two temperature sensors generate an internal inhibit signal, which disables the converter in the case of overtemperature. The outputs automatically recover when the temperature drops below the limit.

Interruption Time

The integrated storage capacitor (C_b) is loaded to the boost voltage and ensures full output voltage with nominal load during an interruption time (or ride-through time) of at least 10 ms, provided that V_i was ≥ 20 V before the interruption. This complies with EN 50155 class S2.

Output Protection

The 2nd output of xx2320 models is protected by a suppressor diode against overvoltage, which could occur due to a failure of the internal control circuit. This suppressor diode was not designed to withstand externally applied overvoltages. Overload at any of the outputs will cause both outputs to shutdown.

Note: $V_{o BR}$ of the suppressor diode is specified in *Electrical Output Data*. If this voltage is exceeded, the suppressor diode generates losses and may become a short circuit.

Note: The output voltage of the first output is monitored. If it exceeds typ. 140% of $V_{o nom}$ for 10 ms, the converter is inhibited. To reactivate, V_i must be removed or a shutdown signal applied to PUL (pin 24).

Each output has its own current limiting circuit, providing a rectangular output characterisitc and protecting against short circuit. There is no limitation for the capacitive load, and battery charging is possible as well.

Series and Parallel Connection

Both outputs of the same converter can be series-connected or parallel-connected in order to double the output current or the output voltage respectively.

Outputs of different converters may be series-connected.

In parallel connection of several converters, the T-pins should be interconnected so that all converters share the output current equally. HRL models have no T-pins and should not be operated in parallel connection.

If both outputs of each converter are connected in series, Vo1– of both converters should be connected together and the Tpins as well. See fig. 7 and 8.

Notes:

- Not more than 5 converters should be connected in parallel.
- If several outputs are connected in series, the resulting voltage may exceed the SELV level.
- The PUL-pins (pin 24) should exhibit an individual PUL resistor for each converter. If the shutdown function is used, each PUL-pin must be controlled induvidually.
- The R-pins should be left open-circuit. If not, the output voltages must individually be adjusted prior to paralleling within 1 to 2% or the R-pins should be connected together.
- Series connection of second outputs without involving their main outputs should be avoided, as regulation may be poor.

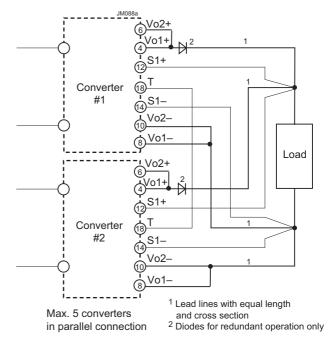


Fig. 7

Parallel connection with OR-ing diodes and sense lines connected at the load





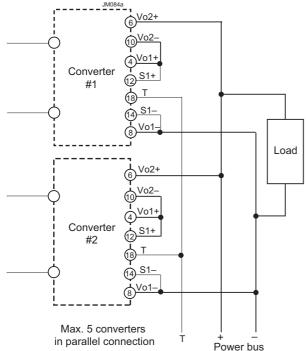
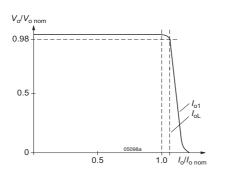


Fig. 8

Parallel connection of double-output models with the outputs of each converter connected in series, using option T. The signal at the T pins is referenced to Vo1–.





Output characteristic V_o versus I_o (both outputs connected in parallel or in series)

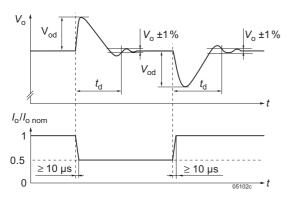


Fig. 10 Typical dynamic load regulation of V_0 .

Output Voltage Regulation

If both outputs are connected in parallel or in series, the converter exhibits a rectangular output characterisitic; see fig. 9.

The typ. dynamic load regulation illustrates fig. 10.

Output 1 is under normal conditions regulated to $V_{o nom}$, irrespective of the output currents.

However, V_{o2} depends upon the load distribution; see fig. 11. Converters with version V101 (or later) have incorporated switchable preloads and do not need a minimum load.

Note: If output 2 is not used, connect it in parallel with output 1! This ensures good regulation and efficiency.

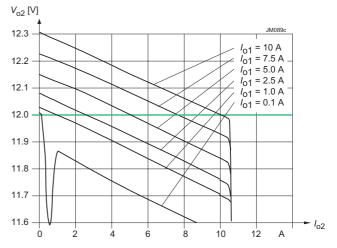


Fig. 11a Models HR2320 (V101): $V_{\rm o2}$ versus $I_{\rm o2}$ with various $I_{\rm o1}$

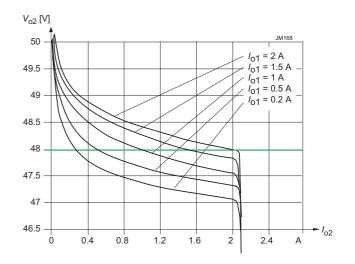


Fig. 11b Models HR2880: V_{o2} versus I_{o2} with various I_{o1}



Auxiliary Functions

Shutdown Function

The PUL input (pin 24) can also be used as shutdown (for the PUL function see table 3). The response time t_r is specified in table 2.

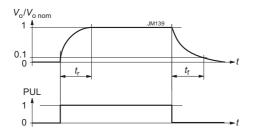
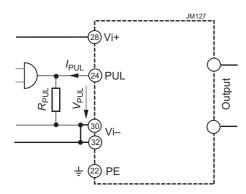
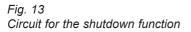


Fig. 12

Typical output response to the PUL-signal (shutdown)

The current coming out from pin 24 (PUL) is typ. 1.5 mA (<2 mA). If pin 24 is left open-circuit, the voltage is typ. 5 V. The converter is disabled when V_{PUL} is \leq 500 mV.





Current Share Function

If the pins 18 (T) of parallel-connected converters are linked together, the converters share the output current evenly. Refer to section *Parallel and Series Connection*. Not for HRL models.

Table 5: Maximum voltage compensation allowed using sense lines

Output voltage	Total voltage difference between sense lines and their respective outputs	Voltage difference between Vo1– and S1–
12 V	<1.0 V	<0.5 V
15 V	<1.25 V	<0.6 V
48 V	<2.0 V	<1.0 V

Sense Lines

This feature allows for compensation of voltage drops across the connector contacts and if necessary, across the load lines. We recommend connecting the sense lines directly at the female connector.

To ensure correct operation, both sense lines (S1+, S1–) should be connected to their respective power outputs (Vo1+ and Vo1–), and the voltage difference between any sense line and its respective power output (as measured on the connector) should not exceed the values specified in table 7.

Important: Sense lines should be connected! Incorrectly connected sense lines may activate the overvoltage protection resulting in a permanent short-circuit of the output. Open sense

lines are allowed, but result in inaccurate output voltages.

Output Voltage Adjust

As a standard feature, the converters offer an adjustable output voltage. The control input R (pin 16) accepts either a control voltage V_{ext} or a resistor R_{ext} to adjust the output voltage. When input R is not connected, the output voltage is set to $V_{\text{o nom}}$.

a) Adjustment by means of an **external control voltage** *V*_{ext} between pin 16 (R) and pin 14 (S1–):

The control voltage range is 1.0 - 2.875 V and allows for an adjustment in the range of approx. 40 - 115% of $V_{o nom}$.

$$V_{\text{ext}} \approx \frac{V_{\text{o}} \cdot 2.5 \text{ V}}{V_{\text{o nom}}}$$

Caution: Applying an external control voltage >2.875 V may damage the converter.

b) Adjustment by means of an external resistor:

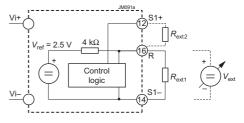
Depending on the value of the required output voltage, the resistor shall be connected

either: between pin 16 (R) and pin 14 (S1–) to adjust the output voltage in the range of approx. 40 - 100% of $V_{o nom}$.

$$R_{\rm ext1} \approx 4 \ {\rm k}\Omega \cdot {V_{\rm o} \over V_{\rm o \ nom} - V_{\rm o}}$$

or: between pin 16 (R) and pin 12 (S1+) to adjust the output voltage in the range of 100 - 115% of $V_{o nom}$.

$$R_{\text{ext2}} \approx 4 \text{ k}\Omega \cdot \frac{(V_{\text{o}} - 2.5 \text{ V})}{2.5 \text{ V} \cdot (V_{\text{o}}/V_{\text{o nom}} - 1)}$$









Caution: To prevent the converter from damage, the value of R'_{ext} shall never be less than the value for increasing V_{o1} to 115% !

Notes

- If the output voltages are increased above $V_{\rm o\,nom}$ via R-input control, sense lines, or option T, the output currents should be reduced, so that $P_{\rm o\,nom}$ is not exceeded.
- The second output of double-output models follows the voltage of the controlled main output.

Output Voltage Monitor

The output voltage monitor generates a logic "low" signal (NPN open-collector output) at the D-output (pin 20), when $V_{o1} \ge 0.96 V_{o nom}$. For converters with version V101 (or later), the voltage at S1+ (corresponding to V_{o1}) must be $\ge 0.96 V_{o nom}$ and $\le 1.04 V_{o nom}$ (typ. values). Then, a green LED (Out OK) at the frontplate is illuminated. If the output voltage is adjusted by the R-input, the trigger levels are corrected accordingly.

At low D-output, the current is limited by a 10 Ω protective resistor; for converters with Version V102 (or later) I_D should be \leq 100 mA. If the D-output is high (open collector), V_D should

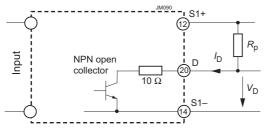


Fig. 15 Output voltage monitor

be \leq 75 V. For previous converters: \leq 50 mA and 50 V.

Note: Output overvoltage activates a latch; see Output Protection.

Indicators

Two green indicators are visible at the front plate:

- Out OK; see Output Voltage Monitor
- In OK. This signal is activated when V_i is below 158 V and greater than V_{i min}, whereas V_{i min} is defined by the adjust resistor connected to the PUL input (pin 24).

Battery Charging / Temperature Sensor

All converters with an R-input are suitable for battery charger application. For optimal battery charging and life expectancy of the battery an external temperature sensor can be connected to the R-input. The sensor is mounted as close as possible to the battery and adjusts the output voltage accoring to the battery temperature.

Depending upon cell voltage and the temperature coefficient of the battery, different sensor types are available, see *Accessories*.

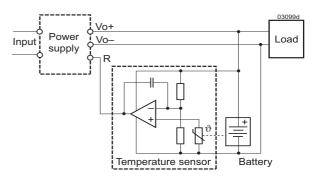


Fig. 16 Connection of a temperature sensor

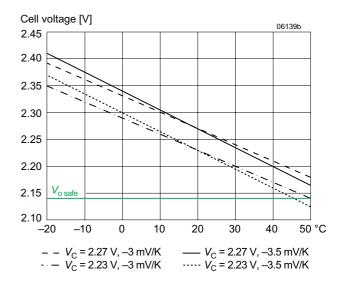


Fig. 17

Trickle charge voltage versus temperature for defined temperature coefficient. $V_{o \text{ nom}}$ is the output voltage with open *R*-input.





which typically occur in most installations. The converters have been successfully tested to the following specifications:

Electromagnetic Compatibility (EMC)

A metal oxide VDR together and an efficient input filter form an effective protection against high input transient voltages,

Electromagnetic Immunity

Table 6: Electromagnetic immunity (type tests)

Phenomenon	Standard	Level	Coupling mode ¹	Value applied	Waveform	Source imped.	Test procedure	In oper.	Perf. crit. ²
Electrostatic	IEC/EN	4 ⁵	contact discharge	8000 V _p	1/50 ns	330 Ω	10 positive and	yes	A
discharge (to case)	61000-4-2		air discharge	15000 V _p	-		10 negative discharges		
Electromagnetic	IEC/EN	X 6	antenna	20 V/m	AM 80% /1 kHz	n.a.	80 – 1000 MHz	yes	A
field	61000-4-3	7	antenna	20 V/m	AM 80% /1 kHz	n.a.	800 – 1000 MHz	yes	A
				10 V/m			1400 – 2100 MHz	1	
				5 V/m	-	-	2100 – 2500 MHz	-	
		3	antenna	10 V/m	50% duty cycle, 200 Hz rep. rate	n.a.	900 ±5 MHz	yes	A
Electrical fast	IEC/EN	38	capacitive, o/c	±2000 V _p	bursts of 5/50 ns	50 Ω	60 s positive	yes	A
transients/burst	61000-4-4	4	i/c, +i/–i direct	±4000 V _p	2.5/5 kHz over 15 ms; burst period: 300 ms		60 s negative transients per coupling mode		
Surges	IEC/EN	3 ⁹	i/c	±2000 Vp	1.2/50 µs	12 Ω	5 pos. and 5 neg.	yes	A
	61000-4-5		+i/—i	±1000 Vp	-	2 Ω	surges per coupling mode		
Conducted disturbances	IEC/EN 61000-4-6	310	i, o, signal wires	10 VAC (140 dBµV)	AM 80% 1 kHz	150 Ω	0.15 – 80 MHz	yes	A
Power frequency magnetic field	IEC/EN 61000-4-8	311		300 A/m			60 s in all 3 axis	yes	A
Pulse magnetic field	IEC/EN 61000-4-9	-		±300 A/m			5 pulses per axis repetit. rate 10 s	yes	A

¹ i = input, o = output, c = case

² A = normal operation, no deviation from specs.; B = normal operation, temporary loss of function or deviation from specs possible

⁵ Exceeds EN 50121-3-2:2006 table 9.3 and EN 50121-4:2006 table 1.4.

⁶ Corresponds to EN 50121-3-2:2006 table 9.1 and exceeds EN 50121-4:2006 table 1.1.

⁷ Corresponds to EN 50121-3-2:2006 table 9.2 and EN 50121-4:2006 table 1.2 (compliance with digital mobile phones).

⁸ Corresponds to EN 50121-3-2:2006 table 7.2 and EN 50121-4:2006 table 2.2.

⁹ Covers or exceeds EN 50121-3-2:2006 table 7.3 and EN 50121-4:2006 table 2.3.

¹⁰ Corresponds to EN 50121-3-2:2006 table 7.1 and EN 50121-4:2006 table 3.1 (radio frequency common mode).

¹¹ Corresponds to EN 50121-4:2006 table 1.3.



Electromagnetic Emissions

All models comply with Class A according to EN 55011/55022 for conducted and radiated emissions.

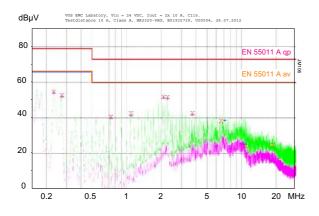


Fig. 18a

Typ. conducted emissions (peak/quasipeak and average) at the input according to EN 55011/22, measured at $V_i = 24 V$ and $I_{o nom}$ (HR2320-9RG).

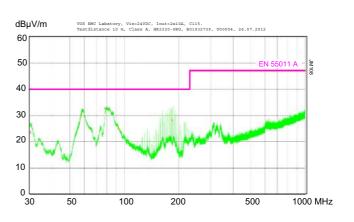
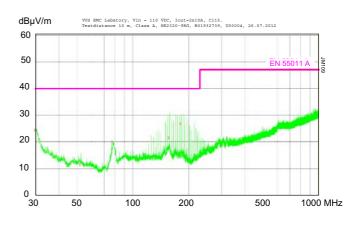


Fig. 19a

Typ. radiated emissions accord. to EN 55011/22, antenna 10 m distance, measured at $V_i = 24 V$ and $I_{o nom}$ (HR2320-9RG).





Typ. radiated emissions accord. to EN 55011/22, antenna 10 m distance, measured at $V_i = 110 V$ and $I_{o nom}$ (HR2320-9RG).

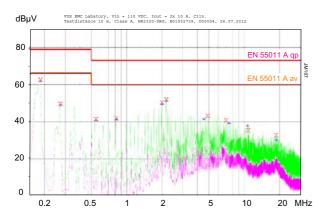


Fig. 18b

Typ. conducted emissions (peak/quasipeak and average) at the input according to EN 55011/22, measured at V_i = 110 V and $I_{o nom}$ (HR2320-9RG).





Immunity to Environmental Conditions

Table 7: Mechanical and climatic stress (type tests)

Test	method	Standard	Test conditions		Status
Cb	Humidity	IEC/EN 60068-2-56	Temperature: Relative humidity: Duration:	25 °C 95 % 8 h	Converter operating
Cab	Damp heat steady state	IEC/EN 60068-2-78 MIL-STD-810D section 507.2	Temperature: Relative humidity: Duration:	40 ^{±2} °C 93 ^{+2/-3} % 56 days	Converter not operating
Db	Damp heat test, cyclic	EN 50155:2007, clause 12.2.5 IEC/EN 60068-2-30	Temperature: Cycles (respiration effect): Duration:	55 °C and 25 °C 2 2× 24 h	Converter not operating
Bd	Dry heat test steady state	EN 50155:2007, clause 12.2.4 IEC/EN 60068-2-2	Temperature: Duration:	70 °C 6 h	Converter operating
Ad	Cooling test steady state	EN 50155:2007, clause 12.2.3 IEC/EN 60068-2-1	Temperature, duration Performance test	–40 °C, 2 h +25 °C	Conv. not operating
	Salt mist test sodium chloride (NaCl) solution	EN 50155:2007, clause 12.2.10 class ST3	Temperature: Duration:	35 ^{±2} °C 48 h	Converter not operating
Fc	Vibration (sinusoidal)	IEC/EN 60068-2-6 MIL-STD-810D section 514.3	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (10 – 60 Hz) 5 g _n = 49 m/s ² (60 - 2000 Hz) 10 – 2000 Hz 7.5 h (2.5 h in each axis)	Converter operating
Fh	Random vibration broad band (digital control) and guidance	IEC/EN 60068-2-64	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.05 g _n ² /Hz 8 – 500 Hz 4.9 g _{n rms} 1.5 h (0.5 h in each axis)	Converter operating
Eb	Bump (half-sinusoidal)	IEC/EN 60068-2-29 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	25 g _n = 245 m/s ² 6 ms 6000 (1000 in each direction)	Converter operating
Ea	Shock (half-sinusoidal)	IEC/EN 60068-2-27 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	50 g _n = 490 m/s ² 11 ms 18 (3 in each direction)	Converter operating
	Shock	EN 50155:2007 clause 12.2.11, EN 61373 sect. 10, class B, body mounted ¹	Acceleration amplitude: Bump duration: Number of bumps:	5.1 g _n 30 ms 18 (3 in each direction)	Converter operating
	Simulated long life testing at increased random vibration levels	EN 50155:2007 clause 12.2.11, EN 61373 sect. 8 and 9, class B, body mounted ¹	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.02 g _n ² / Hz 5 – 150 Hz 0.8 g _{n rms} 15 h (5 h in each axis)	Converter operating

¹ Body mounted = chassis of a railway coach

Temperatures

Table 8: Temperature specifications, valid for an air pressure of 800 - 1200 hPa (800 - 1200 mbar)

Temperature				-9		
Characteristics Conditions			min	typ	max	
TA	Ambient temperature	Converter operating	-40		71 ¹	°C
T _C	Case temperature		-40		95 ¹²	
Ts	Storage temperature	Non operational	-55		100	

¹ See Thermal Considerations.

² Overtemperature lockout at $T_{\rm C}$ >95 °C (An NTC resistor on primary and secondary heatsink).





Reliability

Table 9: MTBF and device hours

Ratings at specified case temperature	Model	Ground benign 40 °C	Ground 40 °C	fixed 70 °C	Ground mobile 50 °C	Life test ¹ 25 °C	Device hours ²	Unit
Bellcore	HR2320	352 000	176 000	49 000	38 000	500 000		h

¹ Life test with 32 converters during 26 days, cycling at 60 °C; confidience level 60%.

² Statistical values, based on an average of 4300 working hours per year and in general field use over 5 years; upgrades and customer-induced errors are excluded.

Mechanical Data

Dimensions in mm. The converters are designed to be inserted into a 19" rack, 160 mm long, according to IEC 60297-3.

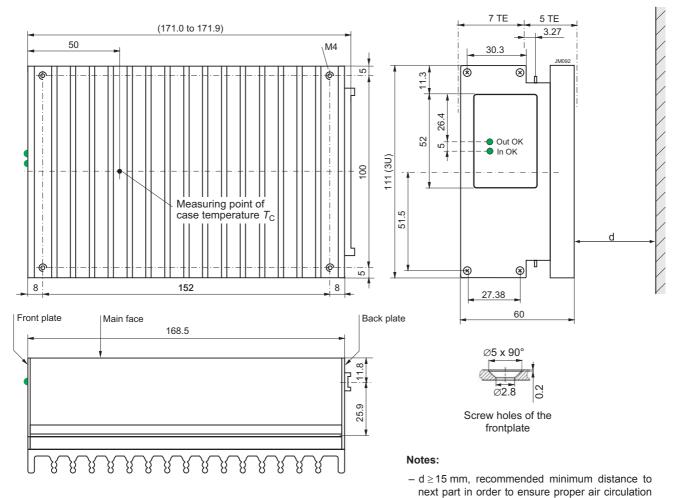
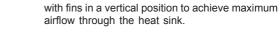


Fig. 20

Aluminum case of HR models with heat sink; black finish (EP powder coated); weight approx. 1.5 kg



- free air location: the converter should be mounted

at full output power.



⊕ 🖯

European Projection



Dimensions in mm. The converters are designed to be inserted into a 19" rack, 160 mm long, according to IEC 60297-3.



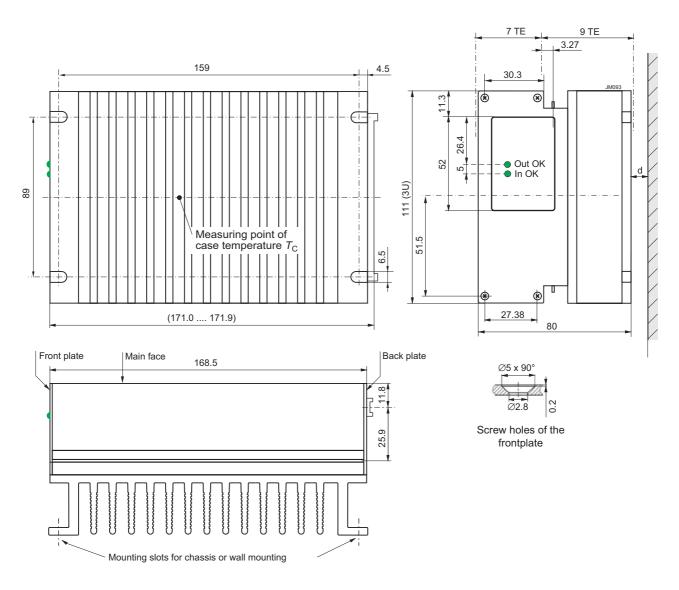


Fig. 21 Aluminum case of HRP models with heat sink; black finish (FP powder coated):

black finish (EP powder coated); weight approx. 1.8 kg

Notes:

- d ≥ 15 mm, recommended minimum distance to next part in order to ensure proper air circulation at full output power.
- free air location: the converter should be mounted with fins in a vertical position to achieve maximum airflow through the heat sink.





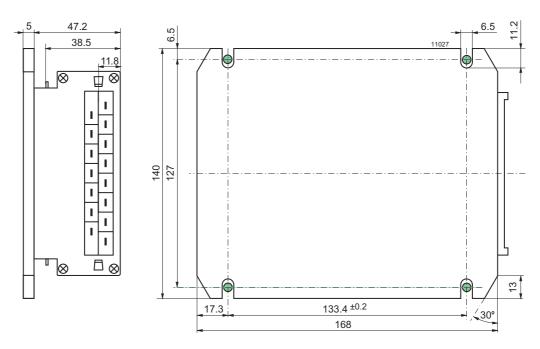


Fig. 22 Option B: Aluminum case S with large cooling plate; black finish (EP powder coated). Suitable for front mounting. Total weight approx. 1.5 kg

Note: Long case with option B2, elongated by 60 mm for 220 mm rack depth, is available on request. (No LEDs)

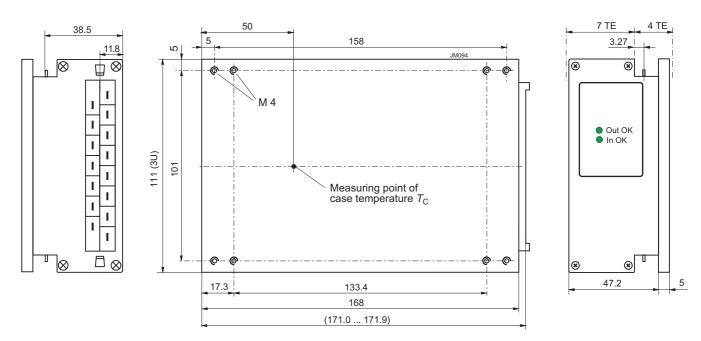


Fig. 23

Option B1: Aluminum case S with small cooling plate; black finish (EP powder coated). Suitable for mounting with access from the backside. Total weight approx. 1.4 kg. \odot

European Projection



Safety and Installation Instructions

Connector Pin Allocation

The connector pin allocation table defines the electrical potentials and the physical pin positions on the H15 connector. The protective earth is connected by a leading pin (no. 24), ensuring that it makes contact with the female connector first.

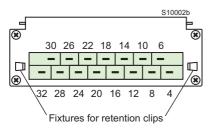


Fig. 24 View of module's male connectors

Table	10:	Pin	allocation
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Pin no.	Name	Function
4	Vo1+	Pos. output 1
6	Vo2+	Pos. output 2
8	Vo1–	Neg. output 1
10	Vo2–	Neg. output 2
12	S1+	Pos. sense line
14	S1–	Neg. sense line
16	R	Output voltage adjust
18 ²	Т	Current share
20	D	Out OK
22 ¹	PE	Protection earth 🚇
24	PUL	Programmable undervoltage lockout
26 + 28	Vi+	Pos. input
30 + 32	Vi–	Neg. input

¹ Leading pin (pre-connecting)

² Not connected for HRL models

Installation Instructions

The converters are components, intended exclusively for inclusion within other equipment by an industrial assembly operation or by professional installers. Installation must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings, and segregation requirements of the end-use application.

Connection to the system shall be made via the female connector H15; see *Accessories*. Other installation methods may not meet the safety requirements.

Pin no. 22 () is connected with the case. For safety reasons it is essential to connect this pin reliably to protective earth.

Notes:

- The PUL function (pin 24) must be programmed to enable the outputs. PUL should be connected to Vi- (pins 30 + 32) by a resistor to adjust the start-up voltage; see table 3. Otherwise, the input current may become too high at low input voltage.
- Do not open the converter0, or warranty will be invalidated.
- If the second output of double-output models is not used, connect it parallel with the main output.

Make sure that there is sufficient airflow available for convection cooling and verify it by measuring the case temperature $T_{\rm C}$, when the converter is installed and operated in the end-use application; see *Thermal Considerations*.

Ensure that a converter failure (e.g. an internal short-circuit) does not result in a hazardous condition.

Standards and Approvals

The converters are safety-approved to UL/CSA 60950-1 2^{nd} Ed. and IEC/EN 60950-1 2^{nd} Ed.

The converters correspond to Class I equipment (with case connected to ground). They have been evaluated for:

- Building-in
- Basic insulation between input and case based on 250 VA. Double or reinforced insulation between input and outputs
- · Functional insulation between outputs
- Overvoltage category II
- Pollution degree 2 environment
- · Max. altitude: 2000 m
- · The converters fulfill the requirements of a fire enclosure.

The output voltage is considered as SELV, except HR/ HRP2880 with both outputs in series connection.

The converters are subject to manufacturing surveillance in accordance with the above mentioned standards and ISO 9001:2008. CB-scheme is available on request.

Cleaning Liquids

In order to avoid possible damage, any penetration of cleaning fluids has to be prevented, since the power supplies are not hermetically sealed.

Protection Degree

The protection degree is IP 40, provided that the female connector is fitted to the converter.

Railway Application and Fire Protection

The converters have been designed by observing the railway standards EN 50155, EN 50121-3-2, and EN 50121-4. All boards are coated with a protective lacquer.

The converters comply with NF-F16 (I2/F1). They also comply with EN 45545-1, EN 45545-2 (2013), if installed in a technical compartment or cabinet.







Table 11: Isolation

Characteristic		Input to case and output(s)	Output(s) to case	Output 1 to output 2	Unit
Electric	Factory test >1 s	2.8 ¹	1.4	0.3	kVDC
strength test	AC test voltage equivalent to factory test	2.0	1.0	0.21	kVAC
Insulation resistance at 500 VDC		>300	>300	>100 2	MΩ
Creepage distances		\geq 3.5 3	≥2.5		mm

¹ According to EN 50116 and IEC/EN 60950, subassemblies connecting input to output are pre-tested with 5.6 kVDC or 4 kVAC.

² Tested at 150 VDC

³ Input to outputs: 6.4 mm

Isolation and Protective Earth

The electric strength test is performed in the factory as routine test according to EN 50116 and IEC/EN 60950 and should not be repeated in the field. Power-One will not honor any warranty claims resulting from electric strength field tests. The resistance case to the earth pin (<0.1 Ω) is tested as well.

Description of Options

B, **B1** Cooling Plates

Where a cooling surface is available, we recommend the use of a cooling plate instead of the standard heat sink. The mounting system should ensure that the maximum case temperature $T_{C max}$ is not exceeded. The cooling capacity is calculated by (η see *Model Selection*):

$$P_{\text{Loss}} = \frac{(100\% - \eta)}{\eta} \cdot V_0 \cdot I_0$$

For the dimensions of the cooling plates; see *Mechanical Data*.





Accessories

A variety of electrical and mechanical accessories are available including:

- Front panels for 19" DIN-rack: Schroff or Intermas, 12 or 16TE / 3U; see fig. 25.
- Mating H15 connectors with screw, solder, faston, or press-fit terminals.
- Coding clips for connector coding HZZ00202
- Pair of connector retention clips HZZ01209-G
- Connector retention brackets HZZ01216-G (CRB-HKMS)
- Cable hood for H15 connectors:
 - HZZ00141-G, screw version
 - HZZ00142-G, use with retention brackets HZZ01218-G
 - HZZ00143-G, metallic version providing fire protection
- Cage clamp adapter HZZ00144-G; see fig. 28.
- DIN-rail mounting assembly HZZ0615-G (DMB-K/S)
- Wall-mounting plate K02 (HZZ01213-G) for models with option B1
- Additional external input and output filters
- Different battery sensors S-KSMH ... for using the converter as a battery charger. Different cell characteristics can be selected; see fig. 32, table 12, and Battery Charging/Temperature Sensors.

For additional accessory product information, see the accessory data sheets listed with each product series or individual model at our web site:

www.belpowersolutions.com/power



Fig. 25 Different front panels

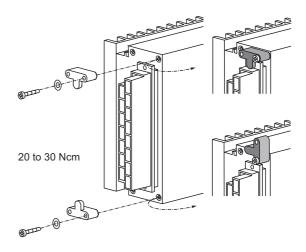








Fig. 27

Connector retention clips to fasten the H15 connector to the rear plate; see fig. 24. HZZ01209-G consists of 2 clips.

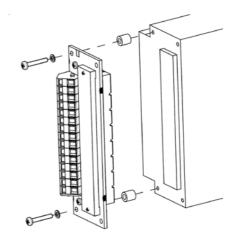


Fig. 28 Cage clamp adapter HZZ00144-G



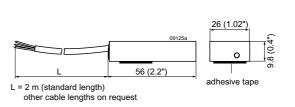




Fig 29 Different cable hoods



Fig. 30 Chassis- or wall-mounting plate HZZ01213-G (Mounting plate K02)



European Projection

Fig. 32 Battery temperature sensor

Battery voltage nom. [V]	Sensor type	Cell voltage [V]	Cell temp. coefficient [mV/K]	Cable length [m]
12	S-KSMH12-2.27-30-2	2.27	-3.0	2
12	S-KSMH12-2.27-35-2	2.27	-3.5	2
24	S-KSMH24-2.27-30-2	2.27	-3.0	2
24	S-KSMH24-2.27-35-2	2.27	-3.5	2
24	S-KSMH24-2.31-35-0	2.31	-3.5	4.5
24	S-KSMH24-2.31-35-2	2.31	-3.5	2
24	S-KSMH24-2.35-35-2	2.35	-3.5	2
48	S-KSMH48-2.27-30-2	2.27	-3.0	2
48	S-KSMH48-2-27-35-2	2.27	-3.5	2

Table 12: Battery temperature sensors

Note: Other temperature coefficients and cable lengths are available on request.



Fig. 31 DIN-rail mounting assembly HZZ00615-G (DMB-K/S)

NUCLEAR AND MEDICAL APPLICATIONS - These products are not designed or intended for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems.

TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.

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