

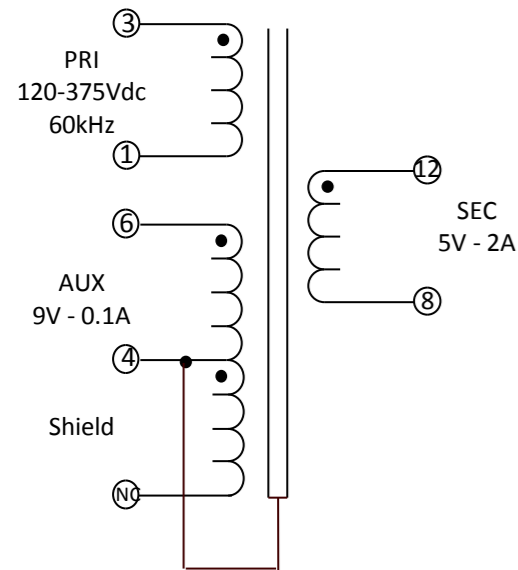
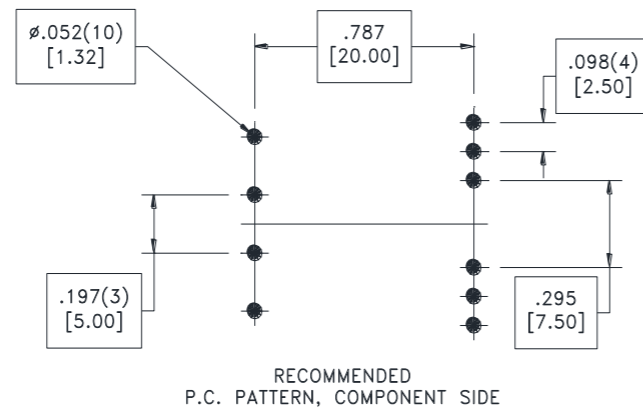
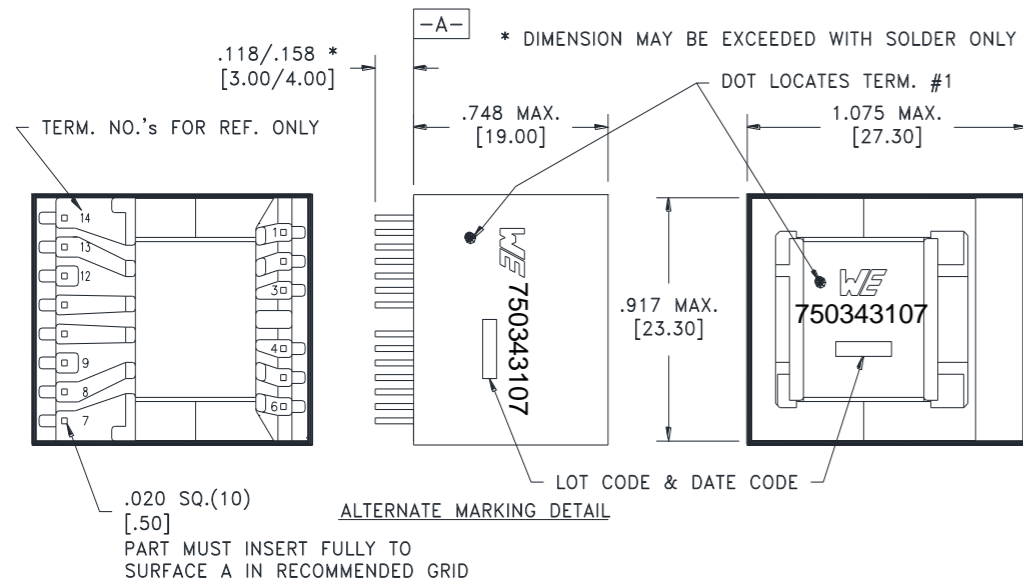
CUSTOMER TERMINAL	RoHS	LEAD(Pb)--FREE
Sn 96%, Ag 4%	Yes	Yes

more than you expect



ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:

PARAMETER	TEST CONDITIONS	VALUE
D.C. RESISTANCE	3-1 @20°C	1.177 ohms max.
D.C. RESISTANCE	6-4 @20°C	0.087 ohms max.
D.C. RESISTANCE	12-8 @20°C	0.019 ohms max.
INDUCTANCE	3-1 10kHz, 1V, Ls	750.0µH ±7%
LEAKAGE INDUCTANCE	3-1 tie(6+4+12+8),100kHz, 1V, Ls	37µH max.
DIELECTRIC	1-12 tie(4+1), 3750VAC, 1 second	
TURNS RATIO	(3-1):(6-4)	8.11:1, ±2%
TURNS RATIO	(3-1):(12-8)	14.6:1, ±2%



GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Design to meet IEC60335 reinforced insulation.

Design to meet UL1310 , a working voltage at 110VAC.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

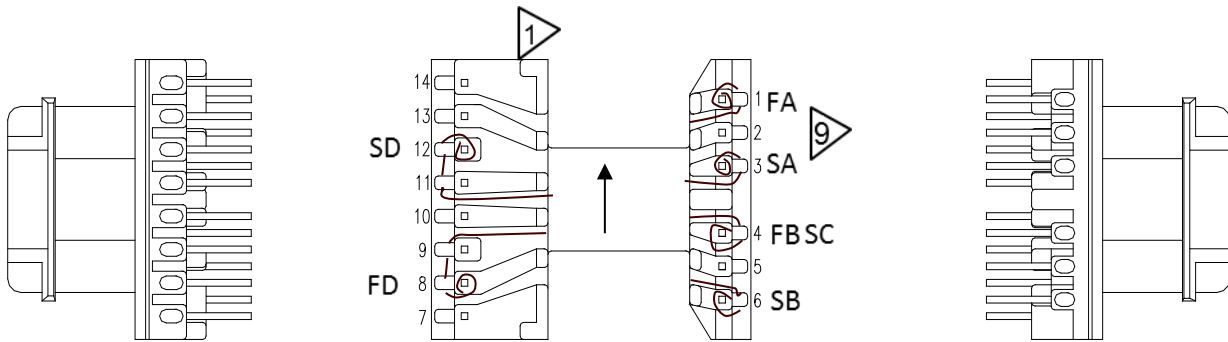
DFM		Packaging Specifications	<p>CONVENTION PLACEMENT</p>	Tolerances unless otherwise specified:	DRAWING TITLE	PART NO.
DATE		Method: Tray		Angles: ±1° Decimals: ±.005 [.13]		
ENG	SLC	PKG-0736		Fractions: ±1/64 Footprint: ±.001 [.03]		
REV.	01			This drawing is dual dimensioned. Dimensions in brackets are in millimeters.		
DATE	2016-2-26	www.we-online.com/midcom				SPECIFICATION SHEET 1 OF 1

Winding	A	B	C	D
Barrel	1 3(1L)			
Tubing				
Crossover				
Magnetwire	2 12	3 2X13	2X15	4 14
Turns	73	9 Bifil		5
Inter Layer	2 4(2L)			
Section				
Wrapper	6(2L)	7(2L)		
Crossover	5 5(2pc)	6 5(2pc)		7 5(2pc)
Tubing				
Splice				
Anchor				
Finish				10(3L)

- Notes:
- Apply 1 layer of barrel tape to bobbin.
 - Wind 37T in first layer, apply 2 layers interlayer then wind rest 36T in second layer.
 - Wind in 1 layer. Cut FC and Bury it into coil, drag FB over tape continue with number layers shown.
 - Wind in 1 layer. Start tape and drag finish back over tape. Continue with number of layers shown over finish leads.
 - Apply 2pcs crossover tape item 5 to SA&FA,
 - Apply 2pcs crossover tape item 5 to SB&FB&SC.
 - Apply 2pc crossover tape item 5 to terminal 7-14 side before winding D.
 - Each wire can't be touched exception for winding B and C.
 - Cut pin 2,5,10 & 11 if present.

Note: Refer to Bill of Materials for items above.

PINOUT FIXED



Bill of materials continued on Assembly Sheet 2

Item #	No. Reqd.	Part Number	Description	Drawing Number	Rev.
17	A.R.	999-4145	Solder Bar, Sn96Ag4	Confirm weight (lb) or length (m)	
16			Magnetwire	_____ #	_____ m _____ %
15	0.003#	9285-0331	Magnetwire	_____ #	_____ m _____ %
14	0.24m	9220-0149	Magnetwire	_____ #	_____ m _____ %
13	0.0030#	9285-0031	Magnetwire	_____ #	_____ m _____ %
12	0.0030#	9285-0031	Magnetwire	_____ #	_____ m _____ %
11			Leadwire		
10	#VALUE!	440-9713	Tape, Finish	#1351-1	6-3/4" LG
9			Tape, Wrapper		
8			Tape, Wrapper		#44
7	#VALUE!	440-9704	Tape, Wrapper	#1351-1	2-3/8" LG
6	0.25000'	440-9704	Tape, Wrapper	#1351-1	3 " Lg.
5	#VALUE!	440-9704	Tape, Crossover	#1351-1	6pc, 3/16" LG
4	#VALUE!	440-9368	Tape, Interlayer		2-11/16" LG
3	#VALUE!	440-9368	Tape, Barrel		1-3/8" LG
2			Tape, Shelf		
1	1	070-6410	Bobbin		Wurth Electronics Midcom Inc.

Item #	No. Reqd.	Part Number	Description	Drawing Number	Rev.
Bill of Material				750343107	01
Revisions: See Sheet 1				Scale ---	Assy Sheet 1 of 3
DFM:		ENG: SLC		02-26-2016	

NOTE:

1. Solder leadwire, item 19, to copper foil tape, item 20. Apply copper foil tape to one core half to shield. Reference figure 1.
2. Apply 2 layers of insulation tape, item 17, around to one without shield core half as shown in figure 2.
 - a) Place tape along inside edge of backiron and fold around outside edge up to inside edge of backiron on other side.
 - b) Fold around outer E leg side of core.
 - c) Fold excess around backiron edge toward the E leg, core tape will hold in place.
3. Core mating surfaces must be clean. Assemble core halves to coil with shielded core half on terminal 1-6 side and core tape core half on terminal 7-14 side and secure with 2 layers of core tape. Route and solder leadwire to terminal #4 as shown in Figure 1.
4. Dip varnish to level shown in Figure 2.
Viscosity: 15 ±3cps. (60% Varnish/40% Thinner).
5. Apply 3 layers tape around transformer as figure 3.

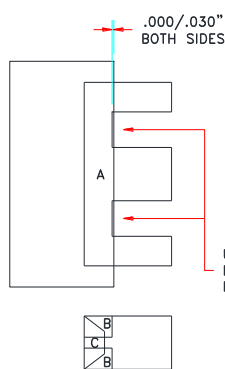


Figure 2

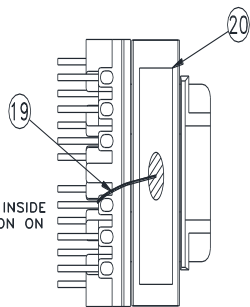


Figure 1

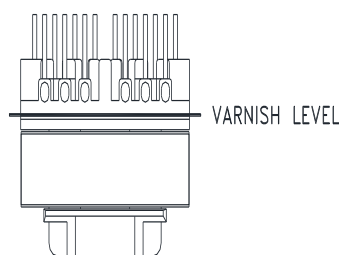


Figure 3

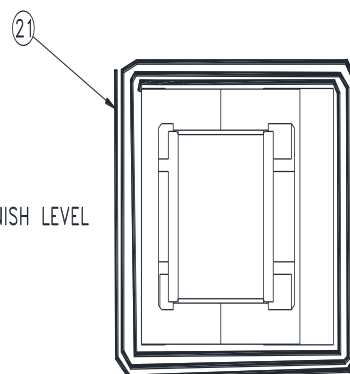


Figure 4

Gapped-Core Correlation Data

No. of turns on design coil at time of measurement:	Inductance with Design Sample Coil		
	Min	Nom	Max
AL: 100 (Gapped with test coil)			

35	A.R.	PKG-0736	Packaging Specification
34	0.01429	230-1091	Shipping Tray
33			
32			
31			
30			
29			
28			
27			
26			
25	A.R.	999-2043	Ink, Black
24	A.R.	999-2438	Varnish
23			Enclosure
22			Header
21	0.9'	440-8061	Tape 10-5/8" LG
20	0.05208'	440-9125	Tape, copper foil 0-7/16" LG
19	0.00001#	9285-0030	Magnet wire 0-7/16" LG
18	0.60417'	440-9987	Tape, Core 7" LG (1pc)
17	0.14'	440-9776	Tape, Insulation 2pc, 0-7/16" LG
16	1	070-6410	Lamination

Lab Use:

EE20/10/6(EF20)
150-1170
Gap to AL: 100

Core:

Supplier info:

Materials

TP4A or equivalent

Würth Electronics Midcom Inc.

Item #	No. Req'd.	Part Number	Description	Drawing Number	Rev.
Bill of Material Continued from Assembly Sheet 1				750343107	01
Revisions: See Sheet 1				Scale ----	Assy Sheet 2 of 3

Cu	Eng	Prod	Spec												
x	x	x	x	Inductance: Test Method 999-2281-I Q: 999-4134-1							Inductance (µH)				
				Freq.	Level	Terminals	Tie	Equiv.	DC Bias	Tolerance	Min.	Nom.	Max.		
				10k	1V	3-1		Ls	0mA	7%	697.5	750.0	802.5		
x	x	x	x	Leakage Inductance: Test Method 999-4128					Leakage Inductance (µH)						
				Freq.	Level	Terminals	Tie	Equiv.	Min.	Nom.	Max.				
				100k	1V	3-1	6+4+12+8	Ls		---	37	TBD			
x	x	x	x	D.C. Resistance: 20°C Test Method 999-4132				Resistance (Ω)							
				Term.	Tie	Match(Ω)	Tolerance	Min.	Nom.	Max.					
				3-1				0.945		1.177	TBD				
x	x	x	x	6-4				0.069		0.087	TBD				
x	x	x	x	12-8						0.013	TBD				
x	x	x	x	Dielectric Test: Test Method 999-4133					Leakage Current						
				Terminals	Tie	Voltage	Units	Dwell Time	Max.						
				1-12	4+1	3750	VAC	1 s.	500µA						
x	x	x	x	Turns Ratio: Test Method 999-2357		Measured Terminals		Reference Terminals		Test Using an Ungapped Core		Measured Resistance			
				Freq.	Voltage	W	X	Y	Z	Tie	Ref. Res.	Tolerance	Min.	Nom.	Max.
				10k	1.0 V	6	4	3	1		1000	2 %	121	123	126
x	x	x	x	10k	1.0 V	12	8	3	1		10000	2 %	671	685	699

Lab	Prod	Spec	Spec
This component must comply with all environment, physical, electrical, packaging and Midcom workmanship standard (999-0010) unless otherwise specified.			
Engineer: SLC		02-26-2016	
Tolerance Unless otherwise specified Resistors: ± 1% Capacitors: ± 3% Inductors: ± 3%		AQL Specifications <input type="checkbox"/> Electrical: ___% Mechanical ___% Visual ___% <input type="checkbox"/> PER 999-2523	
Wurth Electronics Midcom Inc.		Drawing Number	
750343107		Rev. 01	
Scale ----		Assy Sheet 3 of 3	