

SPECIFICATION

- Supplier : Samsung electro-mechanics
- Product : Multi-layer Ceramic Capacitor

- Samsung P/N : **CL10Y105MR5NJNC**
- Description : **CAP, 1 μ F, $\pm$ 20%, 4V, X7S, 0603**

A. Samsung Part Number

CL **10** **Y** **105** **M** **R** **5** **N** **J** **N** **C**
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor						
② Size	0603 (inch code)	L: 1.6 ± 0.1	mm	W: 0.8 ± 0.1	mm		
③ Dielectric	X7S	⑧ Inner electrode	Ni				
④ Capacitance	1 μF	Termination	Cu				
⑤ Capacitance tolerance	±20 %	Plating	Sn 100% (Pb Free)				
⑥ Rated Voltage	4 V	⑨ Product	SLIC				
⑦ Thickness	0.5 +0.05/-0.1	⑩ Special	Reserved for future use				
	mm	⑪ Packaging	Cardboard Type,7"reel(4,000ea)				

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition
Capacitance	Within specified tolerance	1kHz $\pm$ 10% 0.5 $\pm$ 0.1Vrms
Tan δ (DF)	0.12 max.	
Insulation Resistance	More than 50Mohm $\cdot\mu$ F	Rated Voltage 60~120 sec.
Appearance	No abnormal exterior appearance	Visual inspection
Withstanding Voltage	No dielectric breakdown or mechanical breakdown	250% of the rated voltage
Temperature Characteristics	X7S (From -55 $^{\circ}$ C to 125 $^{\circ}$ C, Capacitance change should be within $\pm$ 22%)	
Adhesive Strength of Termination	No peeling shall be occur on the terminal electrode	500g $\cdot$ F, for 10 $\pm$ 1 sec.
Bending Strength	Capacitance change : within $\pm$ 12.5%	Bending to the limit (1mm) with 1.0mm/sec.
Solderability	More than 75% of terminal surface is to be soldered newly	SnAg3.0Cu0.5 solder 245 $\pm$ 5 $^{\circ}$ C, 3 $\pm$ 0.3sec. (preheating : 80~120 $^{\circ}$ C for 10~30sec.)
Resistance to Soldering heat	Capacitance change : within $\pm$ 7.5% Tan δ , IR : initial spec.	Solder pot : 270 $\pm$ 5 $^{\circ}$ C, 10 $\pm$ 1sec.

	Performance	Test condition
Vibration Test	Capacitance change : within $\pm 20\%$ Tan δ , IR : initial spec.	Amplitude : 1.5mm From 10Hz to 55Hz (return : 1min.) 2hours $\times$ 3 direction (x, y, z)
Moisture Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.2 max IR : More than $12.5\text{M}\Omega \cdot \mu\text{F}$	With rated voltage $40 \pm 2^\circ\text{C}$, 90~95%RH, 500+12/-0 hours
High Temperature Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.2 max IR : More than $25\text{M}\Omega \cdot \mu\text{F}$	With 150% of the rated voltage Max. operating temperature 1000+48/-0 hours
Temperature Cycling	Capacitance change : within $\pm 12.5\%$ Tan δ , IR : initial spec.	1 cycle condition Min. operating temperature $\rightarrow 25^\circ\text{C}$ $\rightarrow$ Max. operating temperature $\rightarrow 25^\circ\text{C}$ 5 cycles test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : $260 \pm 0/-5^\circ\text{C}$, 10sec. Max)

* For the more detail Specification, Please refer to the Samsung MLCC catalogue.