

Transistors

2.5V Drive Nch+SBD MOSFET

US6U37

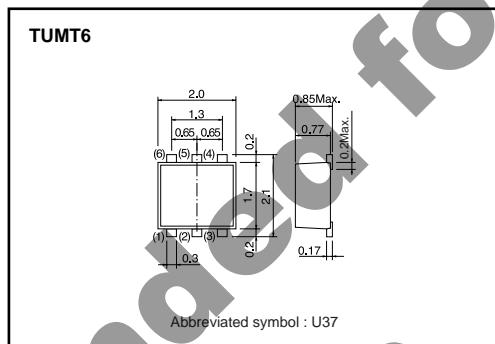
●Structure

Silicon N-channel MOSFET /
Schottky barrier diode

●Features

- 1) Nch MOSFET and schottky barrier diode are put in TUMT6 package.
- 2) High-speed switching, Low On-resistance.
- 3) Low voltage drive (2.5V drive).
- 4) Built-in Low V_F schottky barrier diode.

●Dimensions (Unit : mm)



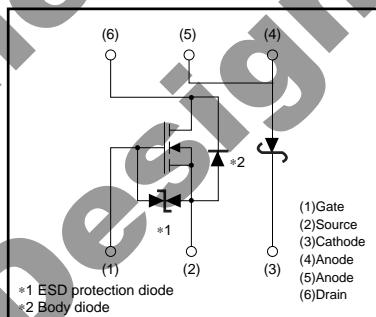
●Applications

Switching

●Package specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
US6U37		○

●Inner circuit



●Absolute maximum ratings (Ta=25°C)

<MOSFET>

Parameter	Symbol	Limits	Unit
Drain-source voltage	V _{DSS}	30	V
Gate-source voltage	V _{GSS}	±12	V
Drain current	Continuous	I _D	A
	Pulsed	I _{DP} *1	A
Source current (Body diode)	Continuous	I _S	A
	Pulsed	I _{SP} *1	A
Channel temperature	T _{ch}	150	°C
Power dissipation	P _D *2	0.7	W / ELEMENT

*1 Pw≤10μs, Duty cycle≤1%

*2 Mounted on a ceramic board

<Di>

Parameter	Symbol	Limits	Unit
Repetitive peak reverse voltage	V _{RM}	25	V
Reverse voltage	V _R	20	V
Forward current	I _F	0.7	A
Forward current surge peak	I _{FSM} *1	10	A
Junction temperature	T _j	150	°C
Power dissipation	P _D *2	0.5	W / ELEMENT

*1 60Hz • 1cycle

*2 Mounted on ceramic board

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Parameter	Symbol	Limits	Unit
Power dissipation	P _D *1	1.0	W / TOTAL
Range of storage temperature	T _{stg}	-55 to +150	°C

*1 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

<MOSFET>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	—	—	±10	μA	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	30	—	—	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 30V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.5	—	1.5	V	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state resistance	R _{DS (on)*}	—	170	240	mΩ	I _D = 1.5A, V _{GS} = 4.5V
		—	180	250	mΩ	I _D = 1.5A, V _{GS} = 4V
		—	240	340	mΩ	I _D = 1.5A, V _{GS} = 2.5V
Forward transfer admittance	Y _{fs} *	1.5	—	—	S	V _{DS} = 10V, I _D = 1.5A
Input capacitance	C _{iss}	—	80	—	pF	V _{DS} = 10V
Output capacitance	C _{oss}	—	14	—	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	—	12	—	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	—	7	—	ns	V _{DD} =15V I _D = 0.75A V _{GS} = 4.5V
Rise time	t _r *	—	9	—	ns	R _L = 20Ω R _E =10Ω
Turn-off delay time	t _{d (off)} *	—	15	—	ns	
Fall time	t _f *	—	6	—	ns	
Total gate charge	Q _g *	—	1.6	2.2	nC	V _{DD} =15V, V _{GS} = 4.5V
Gate-source charge	Q _{gs} *	—	0.5	—	nC	I _D = 1.5A
Gate-drain charge	Q _{gd} *	—	0.3	—	nC	R _L =10Ω, R _G =10Ω

*Pulsed

<Body diode characteristics (Source-drain)>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{SD}	—	—	1.2	V	I _S = 0.6A, V _{GS} =0V

<Di>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _F	—	—	0.49	V	I _F = 0.7A
Reverse current	I _R	—	—	200	μA	V _R = 20V

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● Electrical characteristics curves

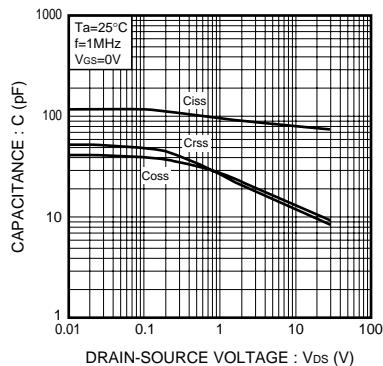


Fig.1 Typical Capacitance vs. Drain-Source Voltage

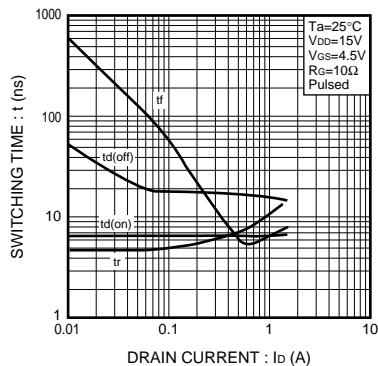


Fig.2 Switching Characteristics

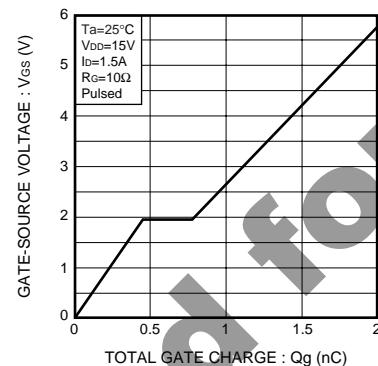


Fig.3 Dynamic Input Characteristics

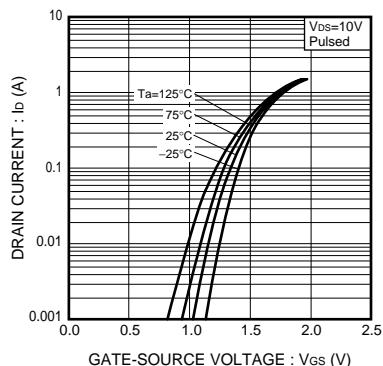


Fig.4 Typical Transfer Characteristics

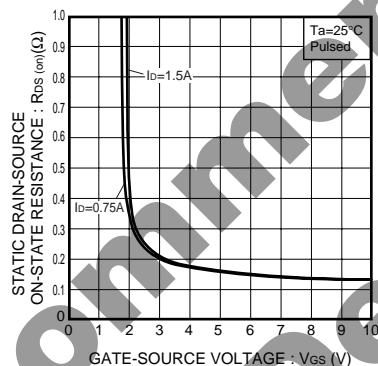


Fig.5 Static Drain-Source On-State Resistance vs. Gate source Voltage

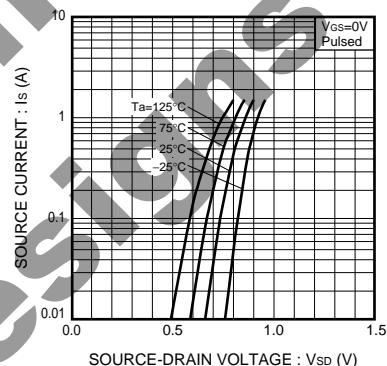


Fig.6 Source Current vs. Source-Drain Voltage

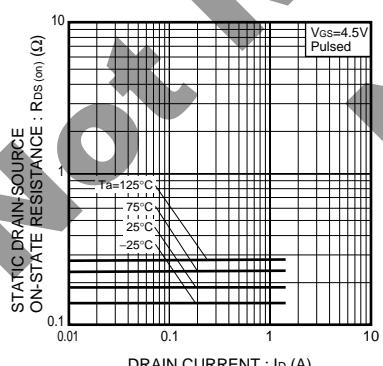


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

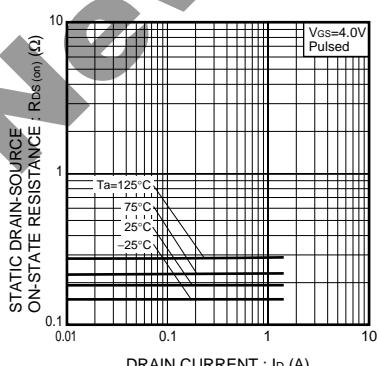


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

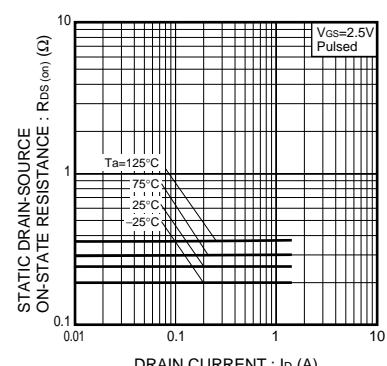


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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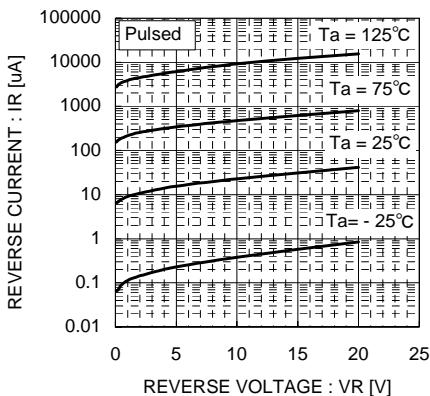


Fig.10 Reverse Current vs. Reverse

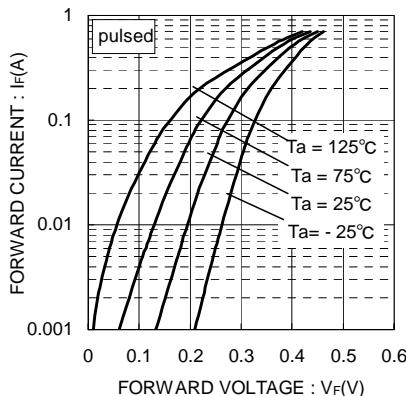


Fig.11 Forward Current vs. Forward Voltage

●Measurement circuit

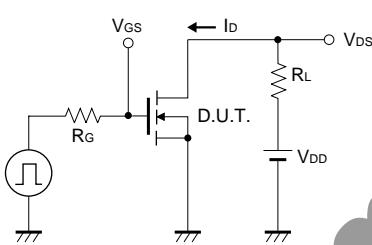


Fig.12 Switching Time Test Circuit

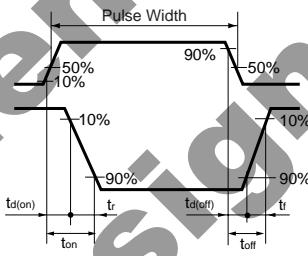


Fig.13 Switching Time Waveforms

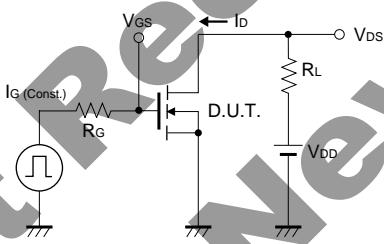


Fig.14 Gate Charge Measurement Circuit

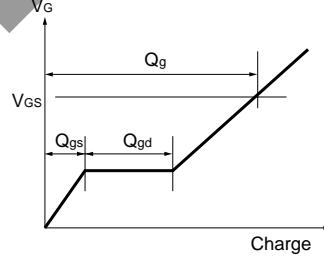


Fig.15 Gate Charge Waveform

●Notice

1. SBD has a large reverse leak current compared to other type of diode. Therefore; it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway.
This built-in SBD has low V_F characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.
2. This product might cause chip aging and breakdown under the large electrified environment.
Please consider to design ESD protection circuit.

Appendix

Notes

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