

General purpose (dual digital transistor)

Parameter	DTr1 and DTr2
V _{CEO}	50V
I _C	100mA
R ₁	10kΩ

Features

- 1)Two DTC114T chips in a UMT or SMT package.
- 2)Mounting possible with UMT3 or SMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

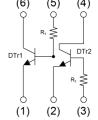
Outline



•Inner circuit

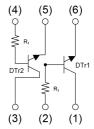
UMH8N

- (1) DTr1 Emitter
- (2) DTr2 Emitter
- (3) DTr2 Base
- (4) DTr2 Collector
- (5) DTr1 Base
- (6) DTr1 Collector



IMH8A

- (1) DTr1 Collector
- (2) DTr1 Base
- (3) DTr2 Collector
- (4) DTr2 Base
- (5) DTr2 Emitter
- (6) DTr1 Emitter



Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
UMH8N	SOT-363 (UMT6)	2021	TR	180	8	3000	H8
IMH8A	SOT-457 (SMT6)	2928	T108	180	8	3000	H8

● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

Parameter			Values	Unit
Collector-base voltage			50	V
Collector-emitter voltage			50	V
Emitter-base voltage			5	V
Collector current		I _C	100	mA
Danier dia dia attau	UMH8N	P _D *1*2	150	
Power dissipation IMH8A		P _D *1*3	300	mW/Total
Junction temperature		T _j	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

Davameter	Cymabal	Conditions	Values			Linit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V
Emitter-base breakdown voltage	BV _{EBO}	I _E = 50μA	5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	500	nA
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	-	-	500	nA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 10mA, I _B = 1mA	-	-	300	mV
DC current gain	h _{FE}	V _{CE} = 5V, I _C = 1mA	100	250	600	-
Input resistance	R ₁	-	7	10	13	kΩ
Transition frequency	f _T *4	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz

^{*1} Each terminal mounted on a reference land.



^{*2 120}mW per element must not be exceeded.

^{*3 200}mW per element must not be exceeded.

^{*4} Characteristics of built-in transistor.

● Electrical characteristic curves (T_a = 25°C)

<For DTr1 and DTr2 in common>

Fig.1 Grounded Emitter Propagation Characteristics

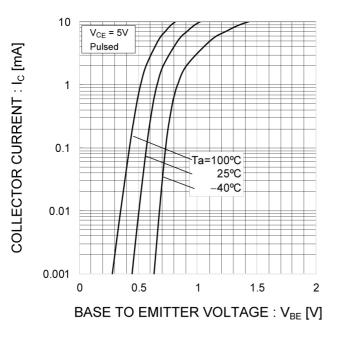
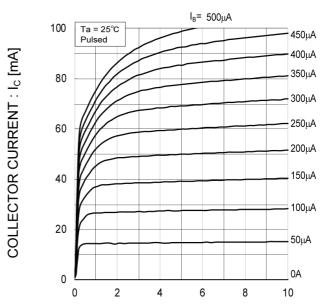


Fig.2 Grounded Emitter Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current Gain vs. Collector Current

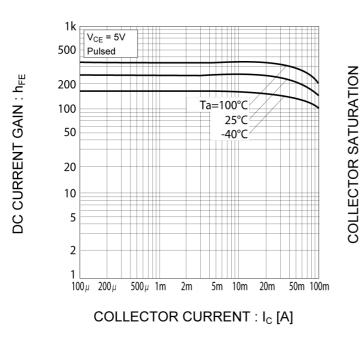
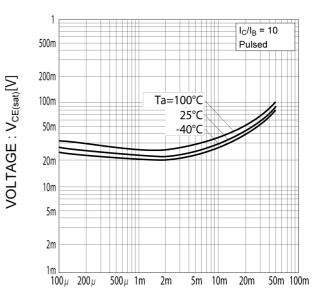
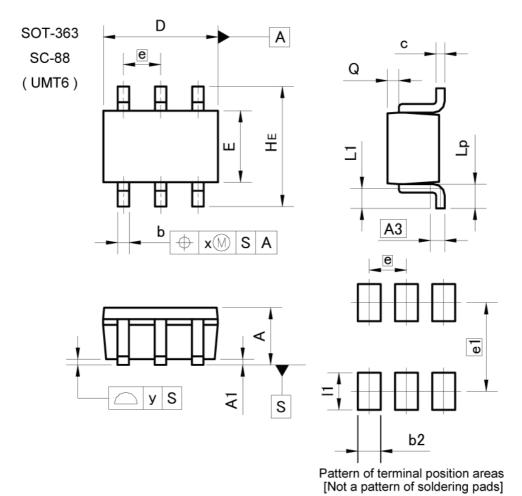


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current



COLLECTOR CURRENT: Ic [A]

Dimensions

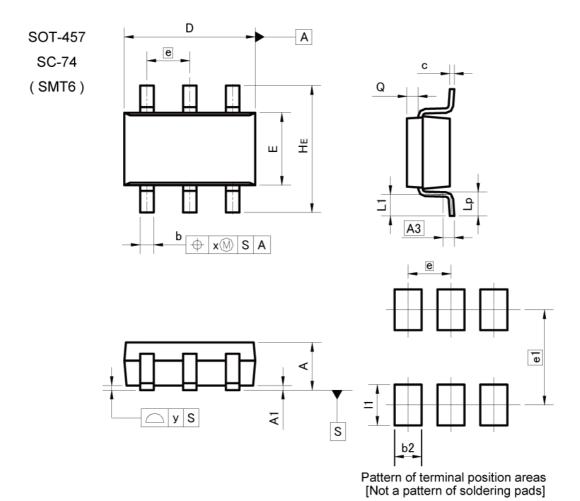


DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.	25	0.0	10	
b	0.15	0.30	0.006	0.012	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
Е	1.15	1.35	0.045	0.053	
е	0.	65	0.026		
HE	2.00	2.20	0.079	0.087	
L1	0.20	0.50	0.008	0.020	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
х	-	0.10	_	0.004	
У		0.10	-	0.004	

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	- 1	0.40	-	0.016
e1	1.55		0.0	61
11	-	0.65	-	0.026

Dimension in mm/inches

Dimensions



DIM MILIME		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.:	25	0.0	10
b	0.25	0.40	0.010	0.016
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
Е	1.50	1.80	0.059	0.071
е	0.9	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
х		0.20		0.008
У	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2		0.60	-	0.024
e1	2.10		0.0	83
l1	- -2	0.90		0.035

Dimension in mm/inches

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