TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SET14F

Schmitt Inverter

Features

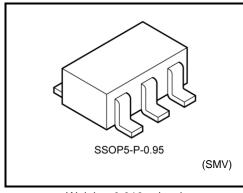
• High speed : $t_{pd} = 5.0 \text{ ns (typ.)}$

at $V_{CC} = 5 \text{ V}$, $C_L = 15 \text{pF}$

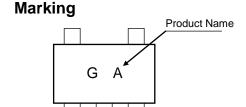
• Low power dissipation : $I_{CC} = 2 \mu A \text{ (max)}$ at $Ta = 25^{\circ}C$

• Compatible with TTL outputs.

• 5.5V tolerant input.



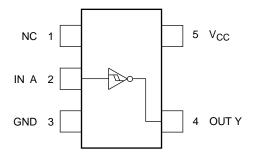
Weight: 0.016 g (typ.)



Absolute Maximum Ratings (Ta = 25°C)

		1		
Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	-0.5 to 7	V	
DC input voltage	V _{IN}	-0.5 to 7	V	
DC output voltage	V _{OUT}	-0.5 to $V_{CC} + 0.5$	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	±20 (Note 1)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	P_{D}	200	mW	
Storage temperature	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 2004-02

IEC Logic Symbol



Truth Table

А	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

Electrical Characteristics

DC Characteristics

Characteristics Symbol					Ta = 25°C			Ta = -40 to 85°C		
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Positive Threshold	\/-			4.5			1.90	_	1.90	
Voltage	V _P	_	_				2.10	_	2.10	V
Negative Threshold	\/	_		4.5	0.50		_	0.50	_	
Voltage	V _N			5.5	0.60	_	_	0.60	_	
Hysteresis Voltage	\/	/н —		4.5	0.40	_	1.40	0.40	1.40	
Hysteresis voltage	۷Н			5.5	0.40	_	1.50	0.40	1.50	
High-level output voltage V _{OH} V _{IN} =	V V	$I_{OH} = -50 \mu A$	4.5	4.4	4.5	_	4.4	_		
	VOH	VIN - VIL	$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	V
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu A$	4.5	_	0.0	0.10	_	0.10	V
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА
Ouissant sumply sumset	Icc	V _{IN} = V _{CC} or GND		5.5			2.0	_	20.0	μΑ
Quiescent supply current	I _{CCT}	V _{IN} = 3.4 V		5.5	_	_	1.35	_	1.50	mA

AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition		Ta = 25°C			Ta = -40 to 85°C			
	Symbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	t _{pLH}		5.0 ± 0.5	15	_	5.0	7.6	1.0	9.0	- ns
	t _{pHL}	_		50	_	6.5	9.6	1.0	11.0	
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note 2)	_	18	_		_	pF

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

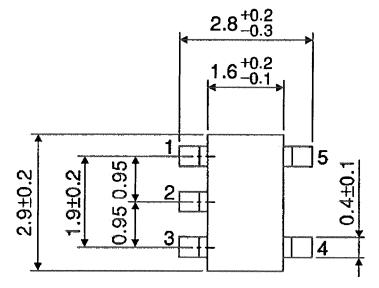
Average operating current can be obtained by the equation:

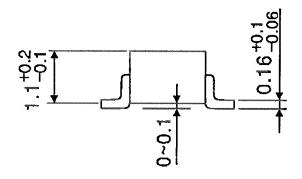
$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

TC7SET14F

Package Dimensions

SSOP5-P-0.95 Unit: mm





Weight: 0.016 g (typ.)

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