

Using the **EVAL-3CH4CHSOICEBZ** *i*Coupler Data Isolator Evaluation Board

FEATURES

- Access to all 4 data channels
- Enable/disable controls
- Multiple connection options
- Support for active probes
- Provision for cable terminations
- Support for PCB edge-mounted coaxial connectors
- Easy configuration
- Sample *i*Coupler digital isolator must be ordered separately

SUPPORTED *i*Coupler GENERICS

- [ADuM1300/ADuM1301](#)
- [ADuM1310/ADuM1311](#)
- [ADuM1400/ADuM1401/ADuM1402](#)
- [ADuM1410/ADuM1411/ADuM1412](#)
- [ADuM2400/ADuM2401/ADuM2402](#)
- [ADuM3300/ADuM3301](#)
- [ADuM3400/ADuM3401/ADuM3402](#)
- [ADuM4400/ADuM4401/ADuM4402](#)
- [ADuM130D/ADuM130E/ADuM131D/ADuM131E](#)
- [ADuM140D/ADuM140E/ADuM141D/ADuM141E/ADuM142D/
ADuM142E](#)
- [ADuM230D/ADuM230E/ADuM231D/ADuM231E](#)
- [ADuM240D/ADuM240E/ADuM241D/ADuM241E/ADuM242D/
ADuM242E](#)

GENERAL DESCRIPTION

The [EVAL-3CH4CHSOICEBZ](#) evaluation board supports triple- and quad-channel standard data isolators in 16-lead SOIC packages. The evaluation board provides a JEDEC standard 16-lead SOIC_N and SOIC_W pad layout and routing appropriate for the evaluation of supported devices. The evaluation board provides a connection to power supplies by screw terminals and includes optimal bypass capacitors. Signal channel routing supports signal distribution, loopback, and provides positions for loads referenced to the VDD1/VDD2 and GND1/GND2 planes. Signal sources can be conducted to the board through header pins or edge-mounted SMA connectors; SMA connectors must be ordered separately. Positions for through-hole 0.2 inch headers compatible with active probes are provided on the printed circuit board (PCB); probe header pins must be ordered separately.

The board follows PCB design practices for 4-layer boards, including a full power and ground plane on each side of the isolation barrier. No other EMI or noise mitigation design features are included on the board. In cases of very high speed operation or when ultralow emissions are required, refer to the [AN-1109](#) application note for additional board layout techniques.

Full specifications for the device under test (DUT) are available in the corresponding product data sheet, which should be consulted in conjunction with this user guide when using the evaluation board.

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REVISION HISTORY

5/2016—Revision 0: Initial Version

EVALUATION BOARD PHOTOGRAPH

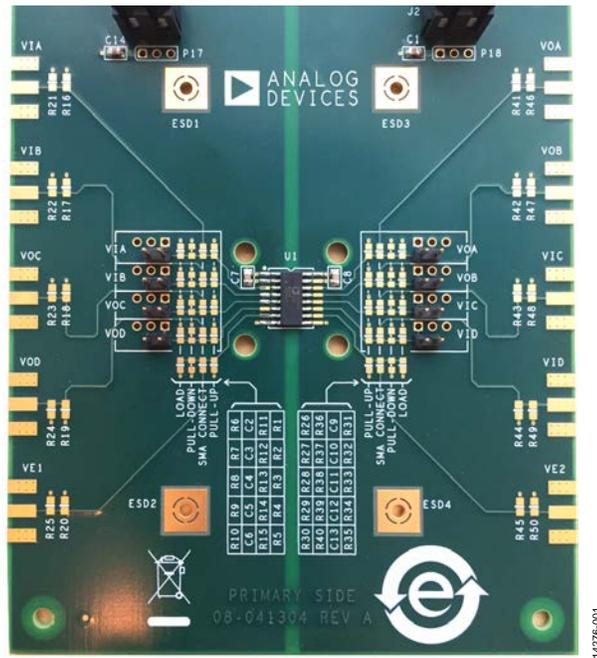


Figure 1. EVAL-3CH4CHSOICEBZ Evaluation Board with the iCoupler Digital Isolator Installed

EVALUATION BOARD CIRCUITRY

PCB EVALUATION FUNCTIONS

The EVAL-3CH4CHSOICEBZ board evaluates the full range of iCoupler® data transfer functions, powers each side of the iCoupler isolator independently, and applies high differential voltages between the two sides of the isolator. The board is intended for evaluation of the components, but has not been safety certified for high voltage operation. If differential voltages above 60 V are applied, external safety measures appropriate for the voltage must be in place.

The evaluation board comes with power terminals, bypass capacitors, and header pins installed. The compatible iCoupler digital isolator must be ordered and installed separately. The EVAL-3CH4CHSOICEBZ is compatible with triple- and quad-channel iCoupler standard data isolator devices, including the on-off keying architecture devices such as the ADuM130D/ADuM130E/ADuM131D/ADuM131E and ADuM140D/ADuM140E/ADuM141D/ADuM141E/ADuM142D/ADuM142E in 16-lead SOIC_W and 16-lead SOIC_N packages. Figure 2 shows a general footprint of a supported device, showing the data channel, power, and ground pins.

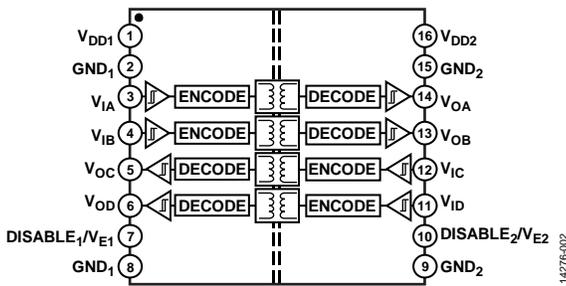


Figure 2. Functional Block Diagram of the ADuM140D/ADuM140E

CONNECTORS

The PCB provides support for three types of interconnections:

- SMA edge-mounted connectors
- Through-hole signal ground pairs
- Terminal blocks for power connections

With these three options, it is possible to make temporary and permanent connections to the board.

When coaxial connections are required, SMA connector positions are available for digital input and output signals and VDD1 and VDD2 power supplies. The SMA connector positions must be ordered from a distributor separately. Figure 3 shows examples of installed SMA connectors; these connectors are not only low profile and provide excellent mechanical connections to the PCB but also support 50 Ω coaxial cabling.

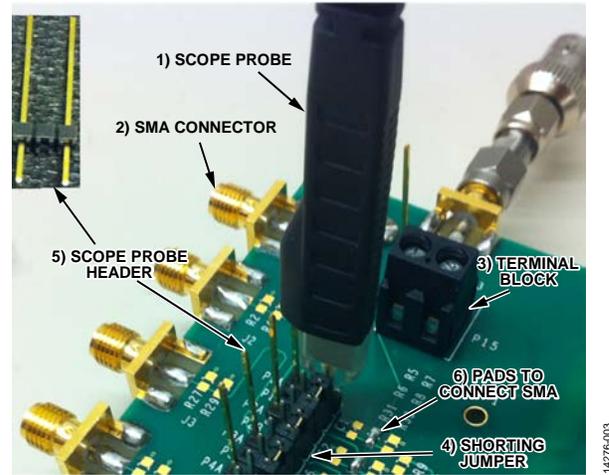


Figure 3. Optional Components

Power can be connected through the J1 and J2 (Terminal 1 power, Terminal 2, and GND, as marked on silkscreen) terminal blocks or the optional VDD1 and VDD2 SMA connectors. Signals can be routed in or out of the board with the provided header pins or optional SMA connectors. The pin spacing of each through-hole connector is 0.1 inch between centers. There are additional signal test points with 0.2 inch spacing provided for active scope probes. These header pins must be added separately. Installed probe points are shown in Figure 3.

POWER INPUT

Each side of the iCoupler standard data isolator requires an off-board power source. The power source must be independent if common-mode voltages are applied across the isolation barrier or damage may occur to the power supply.

Divided power and ground planes are present on Layer 2 and Layer 3 of the PCB on each side of the isolation barrier shown in Figure 7 and Figure 8. Power connects to VDD1 for Side 1 and to VDD2 for Side 2.

DATA INPUT/OUTPUT STRUCTURES

Each data channel has a variety of structures to configure, load, and monitor both the input and output. Figure 4 shows an example of the routing from an external connection to a DUT data channel. Each data channel has similar connections available.

Starting at the external connection, the signal path is constructed as follows:

- A pad layout for a PCB board, edge-mounted SMA connector.
- Two 0805 pads are provided where 100 Ω resistors to ground can be installed. The combined resistance is 50 Ω to provide a termination for a standard coaxial cable.
- A standard 0805 pad layout where the coaxial and termination structures can connect to the rest of the signal path.
- A 0603 pad layout between the signal path and VDD1/VDD2 for a pull-up resistor.

- A populated 2-pin header provides a signal ground pair that can be used for clip leads or shorting a channel to ground temporarily.
- There are groupings of three open through holes, consisting of a signal and two ground connections. These holes can be used for hardwiring signal wires into the PCB, installing a header to accept an active probe, or installing a 2-pin header to allow adjacent channels to temporarily be shorted together.
- A 0805 pad layout between the signal and ground where a load capacitor or resistor can be installed.

Figure 3 shows many of the optional components installed as well as how jumpers can temporarily connect channels. The figure shows a signal connecting to the first-channel SMA and fanning out to the top three channels, monitored by an active scope probe.

BYPASS ON THE PCB

Optional surface-mount bulk capacitors, C1 and C14, are installed near the power connectors compensate for long cables to the power supply. Bypass capacitors, C7 and C8, are installed near the iCoupler isolator and consist of a 0.1 μF capacitor for both DUT power supply pins.

The PCB also implements a distributed capacitive bypass. The capacitive structure consists of power and ground planes spaced closely on the inner layers of the PCB. This minimizes noise and the transmission of electromagnetic interference (EMI) without using complex design features.

HIGH VOLTAGE CAPABILITY

The PCB is designed in adherence with 2500 V basic insulation practices. High voltage testing beyond 2500 V is not recommended. The evaluation board must not be relied on for safety functions.

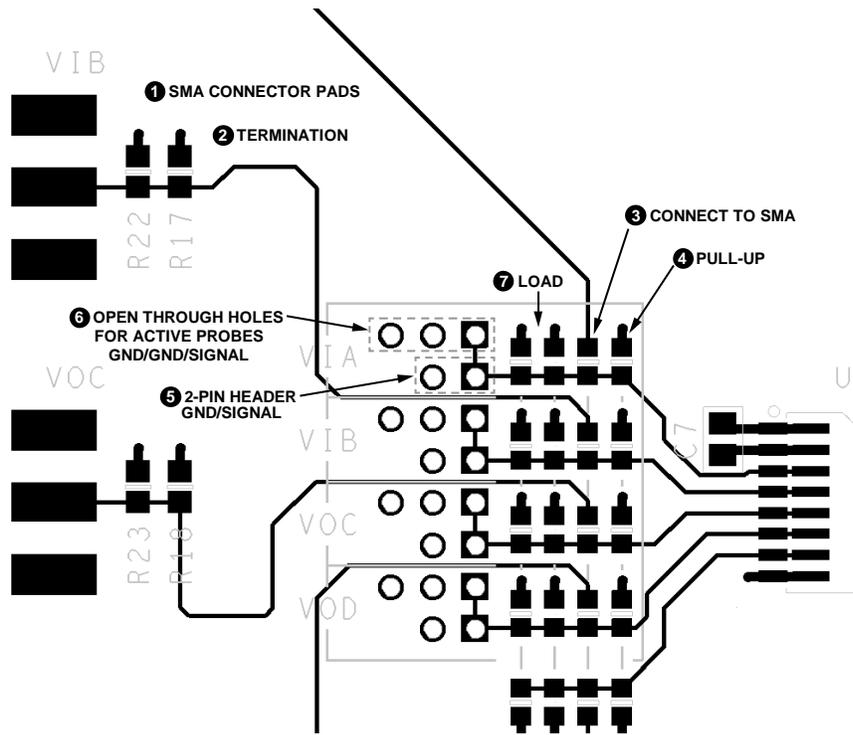
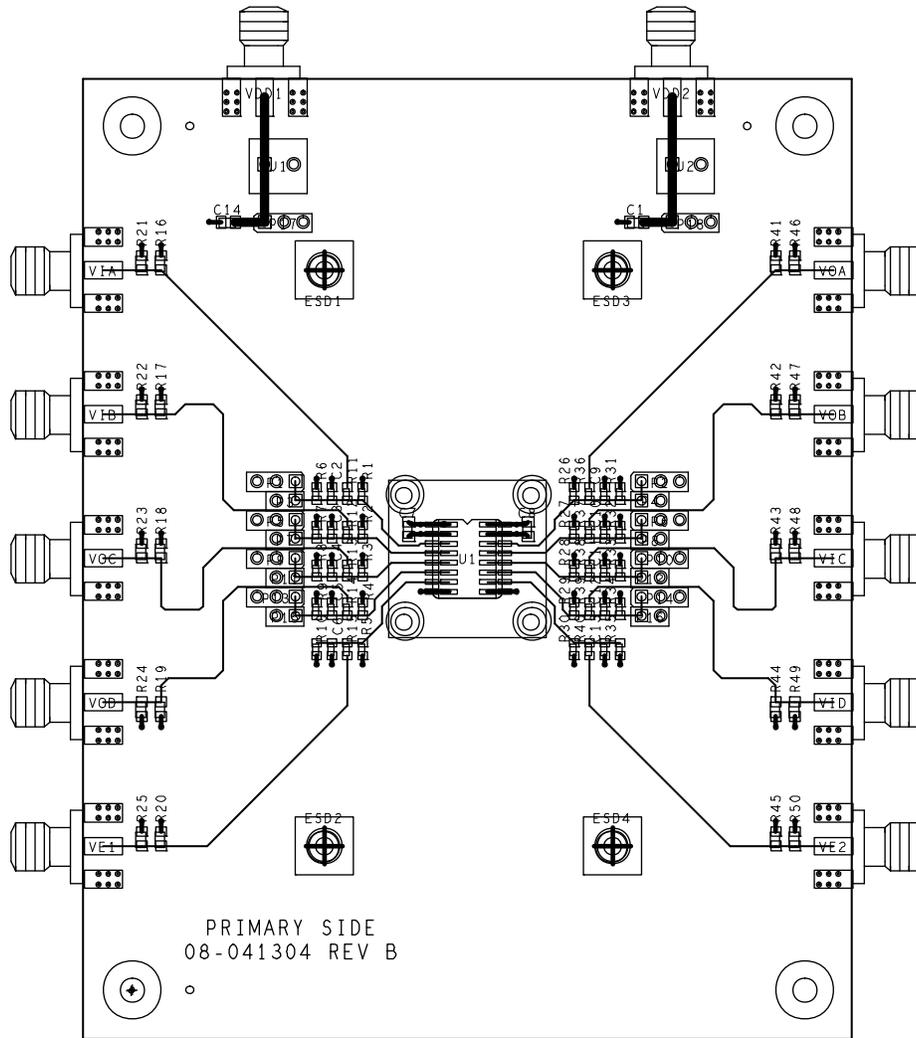
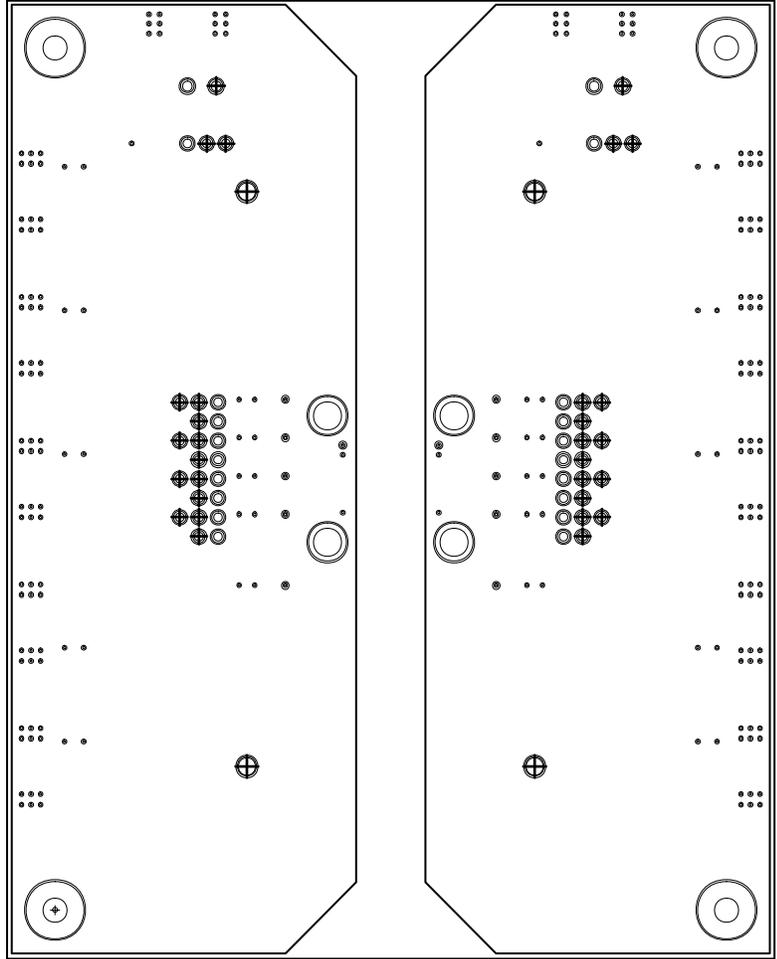


Figure 4. Configuration and Monitoring Structures (Showing a Datapath from an External Connection to the DUT Pin)



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Figure 6. Top Level Signal Routing and Assembly



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Figure 7. GND1 and GND2 Planes

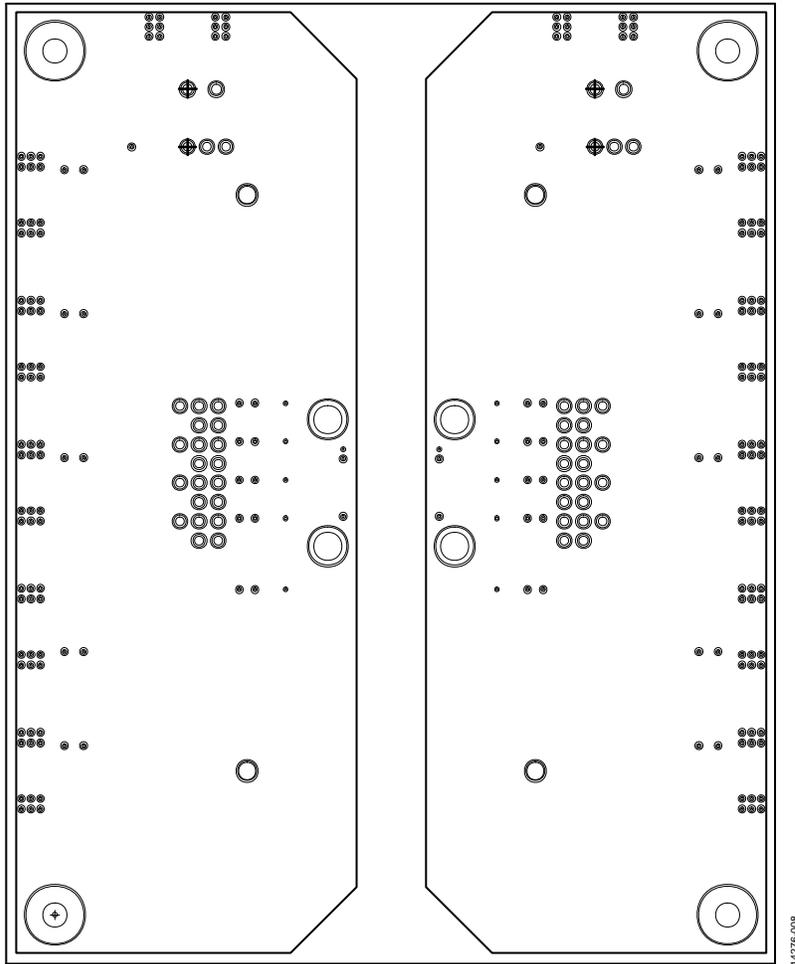


Figure 8. VDD1 and VDD2 Planes

ORDERING INFORMATION

BILL OF MATERIALS

Table 1.

Qty	Reference Designator	Description	Part Number/Manufacturer
0	U1	Triple-/quad-channel digital isolator (not installed)	Analog Devices, Inc.
2	C1, C14	0805, 10 μ F, ceramic capacitors	Not applicable
2	C7, C8	0805, 0.1 μ F, bypass capacitors	Not applicable
2	J1, J2	Screw terminal blocks	Multicomp/MC000044
8	P3, P4, P7, P8, P11, P12, P15, P16	2-pin headers, 0.1 inch spacing	Not applicable
12	VE1, VE2, VIA to VID, VOA to VOD, VDD1, VDD2	SMA edge connectors (not installed)	Johnson/142-0701-851
10	C2 to C6, C9 to C13	Data channel load (not installed)	Not applicable
10	P1, P2, P5, P6, P9, P10, P13, P14, P17, P18	2-pin header, 0.2 inch spacing (not installed)	Samtec MTSW-202-12-G-S-730
30	R1 to R15, R26 to R40	0603, 0 Ω , SMA connection resistors (not installed)	Not applicable
20	R16 to R25, R41 to R50	0805, 100 Ω , input signal termination resistors (not installed)	Not applicable



ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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