

# GaAs INTEGRATED CIRCUIT $\mu PG2415T6X$

# 0.05 to 6.0 GHz SPDT SWITCH

#### DESCRIPTION

The  $\mu$ PG2415T6X is a GaAs MMIC SPDT (Single Pole Double Throw) switch for 0.05 to 6.0 GHz applications, including dual-band wireless LAN.

This device operates with dual control switching voltages of 2.7 to 3.3 V. This device can operate at frequencies from 0.05 to 6.0 GHz, with low insertion loss and high isolation.

This device is housed in a 6-pin plastic TSON (<u>Thin Small Out-line Non-leaded</u>) (T6X) package and is suitable for high-density surface mounting.

#### FEATURES

• 5	Switch control voltage	: Vcont (H) = 3.0 V TYP.
		: $V_{\text{cont}}(L) = 0 \text{ V TYP}.$
• [	_ow insertion loss	: Lins = 0.45 dB TYP. @ f = 2.5 GHz
		: Lins = 0.55 dB TYP. @ f = 6.0 GHz
•	High isolation	: ISL = 28 dB TYP. @ f = 2.5 GHz
		: ISL = 26 dB TYP. @ f = 6.0 GHz
•	Handling power	: Pin (0.1 dB) = +31.0 dBm TYP. @ f = 2.0 to 6.0 GHz
•	High-density surface mounting	: 6-pin plastic TSON (T6X) package ( $1.5 \times 1.5 \times 0.37$ mm)
AP	PLICATIONS	

• Dual-band wireless LAN etc.

#### ORDERING INFORMATION

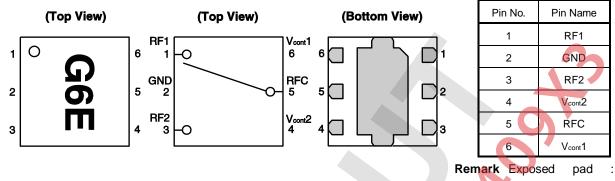
Part Number	Order Number	Package	Marking	Supplying Form
μPG2415T6X-E2	μPG2415T6X-E2-A	6-pin plastic TSON (T6X) (Pb-Free)	G6E	<ul><li>Embossed tape 8 mm wide</li><li>Pin 1, 6 face the perforation side of the tape</li><li>Qty 3 kpcs/reel</li></ul>

**Remark** To order evaluation samples, please contact your nearby sales office. Part number for sample order:  $\mu$ PG2415T6X-A

<u>Caution</u> Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

#### PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM





#### SW TRUTH TABLE

Vcont1	V <sub>cont</sub> 2	RFC-RF1	RFC-RF2	
High	Low	ON	OFF	
Low	High	OFF	ON	

#### ABSOLUTE MAXIMUM RATINGS (TA = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	Vcont	+6.0 Note	V
Input Power	Pin	+36	dBm
Operating Ambient Temperature	Та	-45 to +85	°C
Storage Temperature	Tstg	-55 to +150	°C

**Note**  $|V_{cont}1 - V_{cont}2| \le 6.0 V$ 

#### **RECOMMENDED OPERATING RANGE (TA = +25°C, unless otherwise specified)**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.05	1	6.0	GHz
Switch Control Voltage (H)	Vcont (H)	2.7	3.0	3.3	V
Switch Control Voltage (L)	Vcont (L)	-0.2	0	0.2	V
Control Voltage Difference	⊿Vcont (H), ⊿Vcont (L)	-0.1	0	0.1	V

Note  $\Delta V \text{cont}(H) = V \text{cont} 1 (H) - V \text{cont} 2 (H)$  $\Delta V \text{cont}(L) = V \text{cont} 1 (L) - V \text{cont} 2 (L)$ 

#### ELECTRICAL CHARACTERISTICS

(TA = +25°C, V<sub>cont (H)</sub> = 3.0 V, V<sub>cont (L)</sub> = 0 V, Z<sub>0</sub> = 50  $\Omega$ , DC blocking capacitors = 8 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	Lins1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	-	0.35	-	dB
Insertion Loss 2	Lins2	f = 0.5 to 2.0 GHz <sup>Note 2</sup>	-	0.40	0.65	dB
Insertion Loss 3	Lins3	f = 2.0 to 2.5 GHz	_	0.45	0.70	dB
Insertion Loss 4	Lins4	f = 2.5 to 3.8 GHz	-	0.55	0.80	dB
Insertion Loss 5	Lins5	f = 3.8 to 6.0 GHz	-	0.55	0.80	dB
Isolation 1	ISL1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	_	30	-	dB
Isolation 2	ISL2	f = 0.5 to 2.0 GHz <sup>Note 2</sup>	25	28	-	dB
Isolation 3	ISL3	f = 2.0 to 2.5 GHz	25	28	-	dB
Isolation 4	ISL4	f = 2.5 to 3.8 GHz	25	28	-	dB
Isolation 5	ISL5	f = 3.8 to 6.0 GHz	22	26	-	dB
Return Loss 1	RL1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>		20	-	dB
Return Loss 2	RL2	f = 0.5 to 2.0 GHz Note 2	15	20	-	dB
Return Loss 3	RL3	f = 2.0 to 2.5 GHz	15	20	-	dB
Return Loss 4	RL4	f = 2.5 to 6.0 GHz	10	15	-	dB
0.1 dB Loss Compression	Pin (0.1 dB)	f = 0.5 to 2.0 GHz <sup>Note 2</sup>	_	+32.0	_	dBm
Input Power <sup>Note 3</sup>		f = 2.0 to 6.0 GHz	_	+31.0	-	dBm
1 dB Loss Compression	Pin (1 dB)	f = 0.5 to 2.0 GHz Note 2	—	+36.0	-	dBm
Input Power <sup>Note 4</sup>		f = 2.0 to 6.0 GHz	-	+35.0	-	dBm
2nd Harmonics	2fo	f = 2.5 GHz, Pin = +20 dBm	-	80	-	dBc
3rd Harmonics	3fo	f = 2.5 GHz, Pin = +20 dBm	-	80	-	dBc
Input 3rd Order Intercept Point	_ IIP₃	f = 2.5 GHz, Pin = +20 dBm	_	+60	_	dBm
Switch Control Current	Icont	No RF input	_	0.1	10	μA
Switch Control Speed	tsw	50% CTL to 90/10% RF	_	50	250	ns

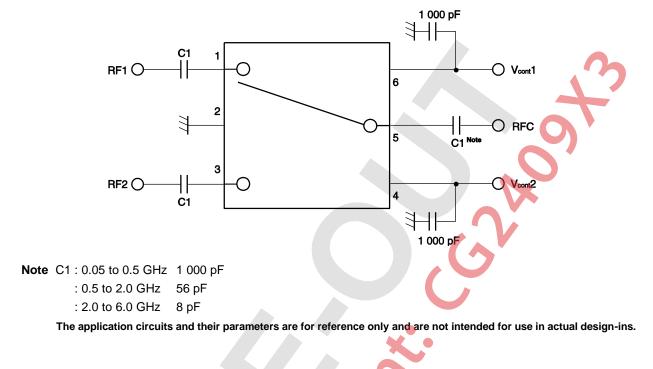
Notes 1. DC blocking capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

- **2.** DC blocking capacitors = 56 pF at f = 0.5 to 2.0 GHz
- **3.** Pin (0.1 dB) is the measured input power level when the insertion loss increases 0.1 dB more than that of the linear range.
- **4.** Pin (1 dB) is the measured input power level when the insertion loss increases 1 dB more than that of the linear range.

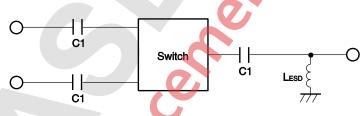
Caution It is necessary to use DC blocking capacitors with this device.

The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system.

#### **EVALUATION CIRCUIT**

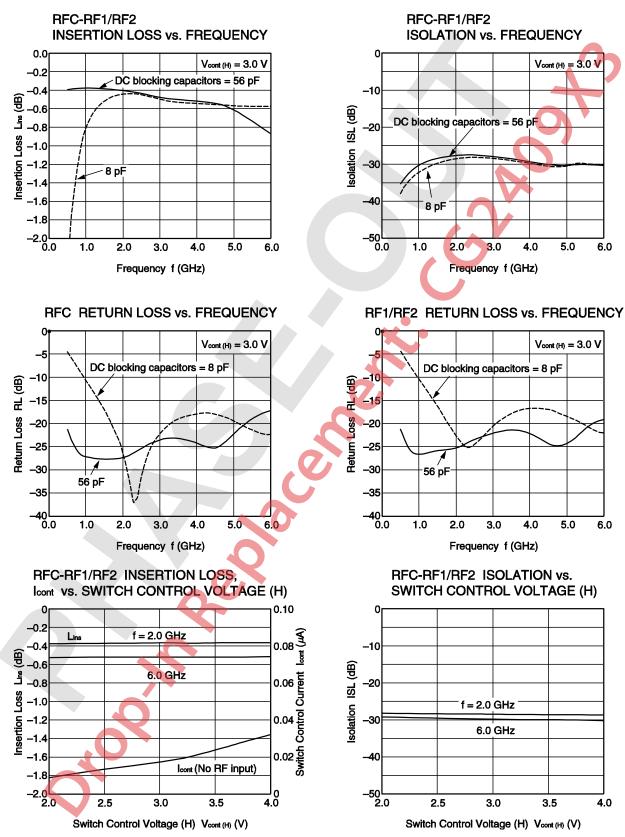


## APPLICATION INFORMATION

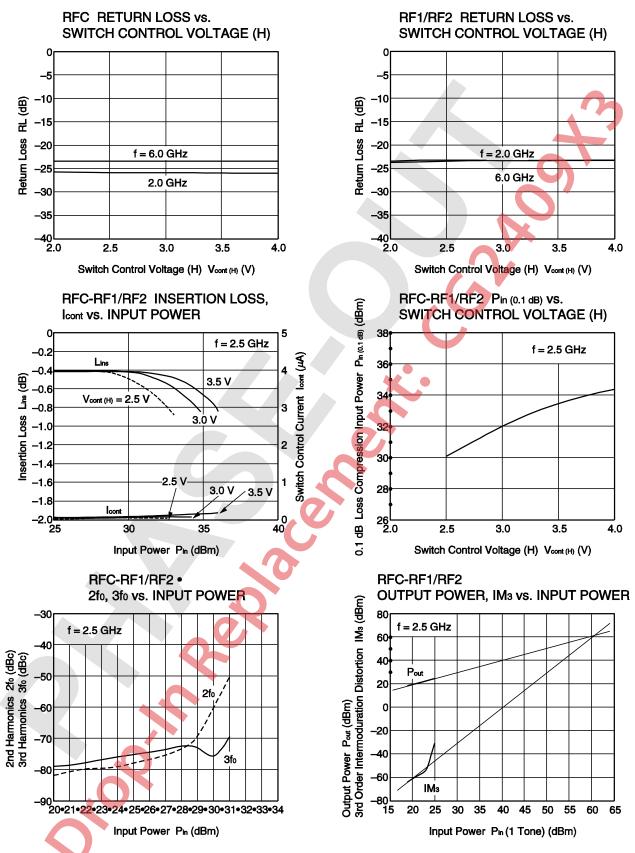


- C1 are DC blocking capacitors external to the device.
- The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.
- LESD provides a means to increase the ESD protection on a specific RF port, typically the port attached to the antenna.

TYPICAL CHARACTERISTICS (TA = +25°C, V<sub>cont</sub> (H) = 3.0 V, V<sub>cont</sub> (L) = 0 V, Z<sub>0</sub> = 50  $\Omega$ , DC blocking capacitors = 8 pF, unless otherwise specified)



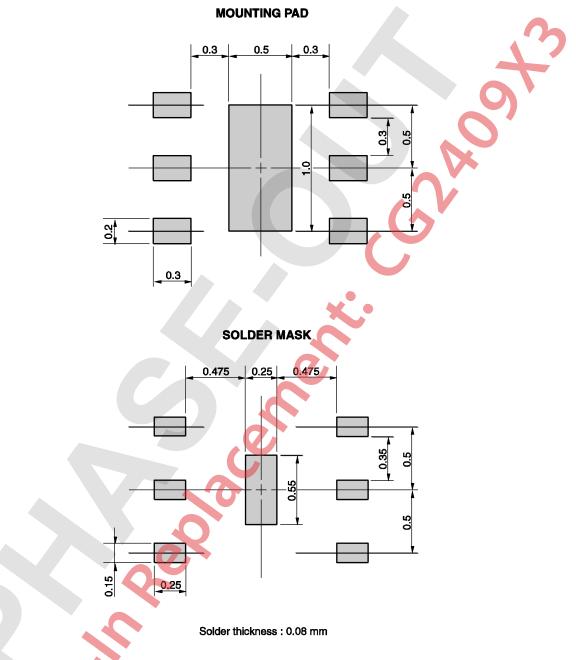
**Remark** The graphs indicate nominal characteristics.



**Remark** The graphs indicate nominal characteristics.

# MOUNTING PAD AND SOLDER MASK LAYOUT DIMENSIONS

## 6-PIN PLASTIC TSON (UNIT: mm)

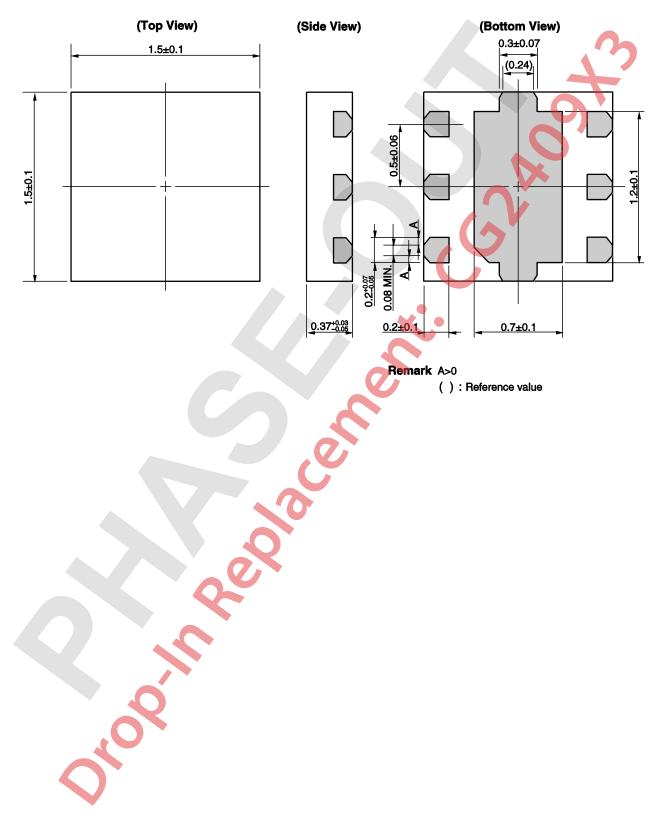


Remark The mounting pad and solder mask layouts in this document are for reference only. When designing PCB, please consider workability of mounting, solder joint reliability, prevention of solder bridge and so on, in order to optimize the design.

Data Sheet PG10797EJ01V0DS

# PACKAGE DIMENSIONS

#### 6-PIN PLASTIC TSON (T6X) (UNIT: mm)



#### **RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	<ul> <li>Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.</li> </ul>
	1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	<ol><li>Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.</li></ol>
	Do not burn, destroy, cut, crush, or chemically dissolve the product.
	Do not lick the product or in any way allow it to enter the mouth.

, cerr

x

#### NOTICE

- 1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. California Eastern Laboratories and Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- California Eastern Laboratories has used reasonable care in preparing the information included in this document, but California Eastern Laboratories does not warrant that such information is error free. California Eastern Laboratories and Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- 3. California Eastern Laboratories and Renesas Electronics do not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of California Eastern Laboratories or Renesas Electronics or others.
- 4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. "Standard": Computers, office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc. "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc. Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. California Eastern Laboratories and Renesas Electronics product for which the product is not intended by California Eastern Laboratories or Renesas Electronics.
- 6. You should use the Renesas Electronics products described in this document within the range specified by California Eastern Laboratories, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. California Eastern Laboratories shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
- 8. Please contact a California Eastern Laboratories sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. California Eastern Laboratories and Renesas Electronics assume no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations.
- 10. It is the responsibility of the buyer or distributor of California Eastern Laboratories, who distributes, disposes of, or otherwise places the Renesas Electronics product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
- 11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of California Eastern Laboratories.
- 12. Please contact a California Eastern Laboratories sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

NOTE 1: "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries. NOTE 2: "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics. NOTE 3: Products and product information are subject to change without notice.

CEL Headquarters • 4590 Patrick Henry Drive, Santa Clara, CA 95054 • Phone (408) 919-2500 • www.cel.com

For a complete list of sales offices, representatives and distributors, Please visit our website: www.cel.com/contactus