



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

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# DTC144TUA

## **Features**

- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisure Sensitivity Level 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy
- Halogen free available upon request by adding suffix "-HF"

#### **Absolute Maximum Ratings**

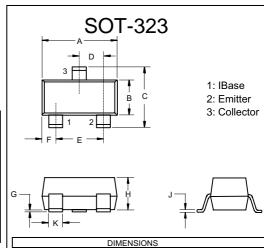
Absolute maximum natings				
Parameter	Symbol	Value	Unit	
Collector-Base Voltage	$V_{CBO}$	50	V	
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V	
Emitter-Base voltage	$V_{EBO}$	5	V	
Collector Current-Continuous	I <sub>C</sub>	100	mA	
Collector Dissipation	Pc	200	mW	
Junction Temperature	TJ	150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55~150	$^{\circ}\mathbb{C}$	

#### **Electrical Characteristics**

Sym	Parameter	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage (I <sub>C</sub> =50uA, I <sub>E</sub> =0)	50			V
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage (I <sub>C</sub> =1mA, I <sub>B</sub> =0)	50			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage (I <sub>E</sub> =50uA, I <sub>C</sub> =0)	5			V
I <sub>CBO</sub>	Collector Cut-off Current (V <sub>CB</sub> =50V, I <sub>E</sub> =0)			0.5	uA
I <sub>EBO</sub>	Emitter Cut-off Current (V <sub>EB</sub> =4V, I <sub>C</sub> =0)			0.5	uA
h <sub>FE</sub>	DC Current Gain (V <sub>CE</sub> =5V, I <sub>C</sub> =1mA)	100	300	600	
$V_{\text{CE}(\text{sat})}$	Collector-Emitter Saturation Voltage (I <sub>C</sub> =10mA, I <sub>B</sub> =1mA)			0.3	>
R <sub>1</sub>	Input resistance	32.9	47	61.1	KΩ
f⊤	Transition Frequency (V <sub>CE</sub> =10V, I <sub>C</sub> =-5mA, f=100MHz)		250		MHz

<sup>\*</sup>Marking: 06

# **NPN Digital Transistor**



DIMENSIONS						
	INCHES		ММ			
DIM	MIN	MAX	MIN	MAX	NOTE	
Α	.071	.087	1.80	2.20		
В	.045	.053	1.15	1.35		
С	.083	.096	2.10	2.45		
D	.026 Nominal		0.65Nominal			
Ш	.047	.055	1.20	1.40		
F	.012	.016	.30	.40		
Ð	.000	.004	.000	.100		
I	.035	.039	.90	1.00		
٦	.004	.010	.100	.250		
K	.006	.016	.15	.40		
Suggested Solder						

# Pad Layout 0.70 0.90 1.90 mm

0.65

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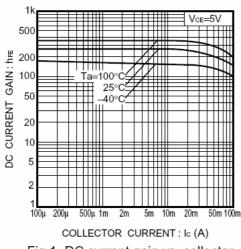


Fig.1 DC current gain vs. collector current

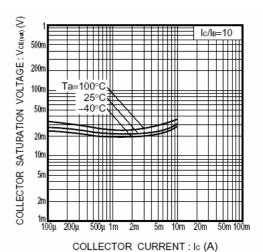
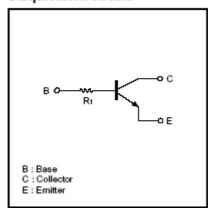


Fig.2 Collector-emitter saturation voltage vs. collector current

## ●Equivalent circuit





#### **Micro Commercial Components**

### **Ordering Information:**

Device	Packing
Part Number-TP	Tape&Reel 3Kpcs/Reel

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