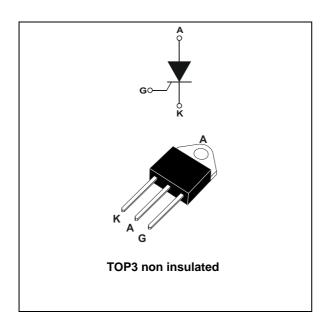
BTW69-1200N



50 A - 1200 V non insulated SCR thyristor

Datasheet - production data



Features

On-state rms current: 50 A
Blocking voltage: 1200 V
Gate current: 50 mA

Applications

- Solid state relay
- Battery charging system
- Uninterruptible power supply
- Variable speed motor drive
- Industrial welding systems
- · By pass AC switch

Description

Available in non insulated TOP3 high power package, the BTW69-1200N is suitable for applications where power switching and power dissipation are critical, such as by-pass switch, controlled AC rectifier bridge, in solid state relay, battery charger, uninterruptible power supply, welding equipment and motor driver applications.

Based on a clip assembly technology, the BTW69-1200N offers a superior performance in surge current handling and thermal cooling capabilities.

Table 1. Device summary

Symbol	Value
I _{T(RMS)}	50 A
V_{DRM}/V_{RRM}	1200 V
I _{GT}	50 mA

Characteristics BTW69-1200N

1 Characteristics

Table 2. Absolute maximum ratings (limiting values)

Symbol	Parameter	Value	Unit		
I _{T(RMS)}	On-state current rms (180° conduction and	50	Α		
IT _(AV)	Average on-state current (180° conduction	31	Α		
	Non repetitive surge peak on-state current	$t_p = 8.3 \text{ ms}$	T _i = 25 °C	763	Α
ITSM		$t_p = 10 \text{ ms}$	$i_j = 25$ C	700	A
l ² t	I ² t Value	$t_p = 10 \text{ ms}$	T _j = 25 °C	2450	A ² s
dl/dt	Critical rate of rise of on-state current Gate supply: $I_G = 100 \text{ mA}$, $dI_G/dt = 1 \text{ A/µs}$				A/µs
I _{GM}	Peak gate current	8	Α		
P _{G(AV)}	Average gate power dissipation	1	W		
T _{stg}	Storage junction temperature range			- 40 to + 150	°C
T _j	Operating junction temperature range			- 40 to + 125	0
V_{GM}	Maximum peak reverse gate voltage			5	V

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Test conditions			Value	Unit
			MIN.	8	mΛ
I _{GT}	$V_D = 12 \text{ V}, R_L = 33 \Omega$		MAX.	50	mA
V _{GT}			MAX.	1.3	V
V _{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	T _j = 125 °C	MIN.	0.2	V
I _H	I _T = 500 mA, gate open		MAX.	100	mA
ΙL	$I_G = 1.2 \times I_{GT}$			125	mA
t _{gt}	$I_T = 50 \text{ A}, V_D = V_{DRM}, I_G = 200 \text{ mA}, dI_G/dt = 0.2 \text{ A/}\mu\text{s}$		TYP.	2	μs
dV/dt	V _D = 67% V _{DRM,} gate open	T _j = 125 °C	MIN.	1000	V/µs
tq	$V_D = 800 \text{ V}, I_{TM} = 50 \text{ A}, V_R = 75 \text{ V},$ $t_p = 100 \mu\text{s}, dI_{TM}/dt = 30 \text{ A/}\mu\text{s},$ $dV_D/dt = 20 \text{ V/}\mu\text{s}$	T _j = 125 °C	TYP.	100	μs
V _{TM}	I _{TM} = 100 A, t _p = 380 μs	T _j = 25 °C	MAX.	1.6	V
V _{t0}	Threshold voltage	T _j = 125 °C	MAX.	0.9	V
R _D	Dynamic resistance	T _j = 125 °C	MAX.	8.5	mΩ
I _{DRM}	$V_D = V_{DRM}$	T _j = 25 °C	MAX.	10	μΑ
I _{RRM}	$V_R = V_{RRM}$	T _j = 125 °C	IVIAA.	5	mA

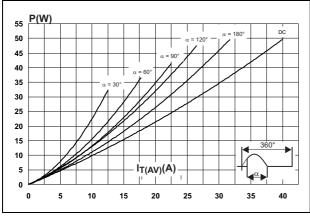
BTW69-1200N Characteristics

Table 4. Thermal resistance

Symbol	Parameter	Value	Unit	
R _{th(j-c)}	Junction to case (DC, typ.)	0.45	°C/W	
R _{th(j-a)}	Junction to ambient (DC)	50	°C/W	

Figure 1. Maximum average power dissipation versus average on-state current

Figure 2. Correlation between maximum average power dissipation and maximum allowable temperatures



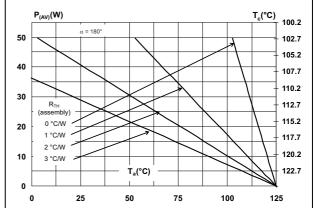
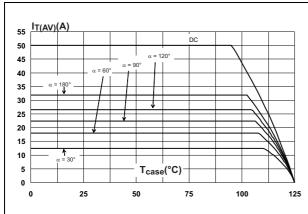
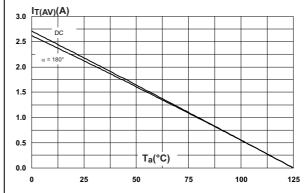


Figure 3. Average and DC on-state current versus case temperature

Figure 4. Average and DC on-state current versus ambient temperature





Characteristics BTW69-1200N

Figure 5. Relative variation of thermal impedance versus pulse duration

Figure 6. Relative variation of gate trigger current and gate trigger voltage versus junction temperature (typical value)

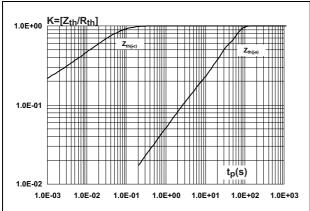
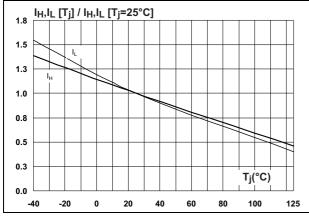


Figure 7. Relative variation of holding, and latching currents versus junction temperature (typical values)

Figure 8. Surge peak on-state current versus number of cycles



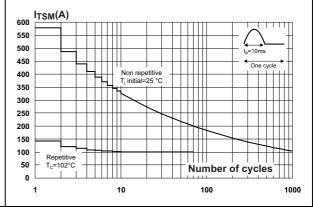
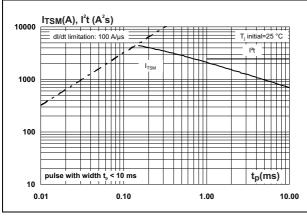
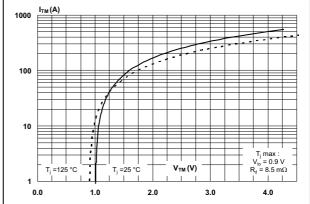


Figure 9. Non repetitive surge peak on-state current and corresponding value of I²t versus sinusoidal pulse

Figure 10. On-state characteristics (maximum values)



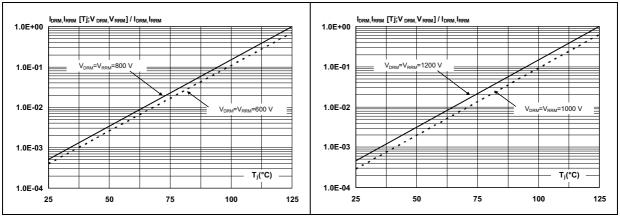


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Figure 11. Relative variation of leakage current versus junction temperature for different values of blocking voltage (600 and 800 V)

Figure 12. Relative variation of leakage current versus junction temperature for different values of blocking voltage (1000 and 1200 V)



Package information BTW69-1200N

2 Package information

- Epoxy meets UL94,V0
- Lead-free packages
- Cooling method: by conduction (C)
- Recommended torque value: 0.9 to 1.2 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Figure 13. TOP3 dimension definitions

Table 5. TOP3 dimension values

	Dimensions				
Ref.	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
Α	4.4	4.6	0.173	0.181	
В	1.45	1.55	0.057	0.061	
С	14.35	15.60	0.565	0.614	
D	0.5	0.7	0.020	0.028	
E	2.7	2.9	0.106	0.114	
F	15.8	16.5	0.622	0.650	
G	20.4	21.1	0.815	0.831	
Н	15.1	15.5	0.594	0.610	
J	5.4	5.65	0.213	0.222	
K	3.4	3.65	0.134	0.144	
ØL	4.08	4.17	0.161	0.164	
Р	1.20	1.40	0.047	0.055	
R	4.60 typ.		0.181	typ.	

Ordering information BTW69-1200N

3 Ordering information

Figure 14. Ordering information scheme

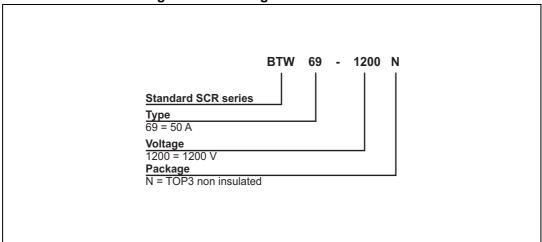


Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
BTW69-1200N	BTW691200N	TOP3	4.55 g	30	Tube

4 Revision history

Table 7. Document revision history

Date	Revision	Changes
14-Jun-2013	1	Initial release.

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