

QUADRUPLE OPERATIONAL AMPLIFIER

FEATURES

- Controlled Baseline
 - One Assembly/Test Site, One Fabrication Site
- Extended Temperature Performance of -55°C to 125°C
- Enhanced Diminishing Manufacturing Sources (DMS) Support
- Enhanced Product-Change Notification
- Qualification Pedigree
- (1) Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.
- ESD Protection <500 V Per MIL-STD-883, Method 3015; Exceeds 200 V Using Machine Model C = 200 pF, R = 0); 1500 V Using Charged Device Model
- ESD Human Body Model >2 kV Machine Model >200 V and Charge Device Model = 2 kV For K-Suffix Devices.
- Low Supply-Current Drain Independent of Supply Voltage . . . 0.8 mA Typ
- Low Input Bias and Offset Parameters:
 - Input Offset Voltage ... 3 mV Typ
 - Input Offset Current . . . 2 nA Typ
 - Input Bias Current . . . 20 nA Typ

DESCRIPTION

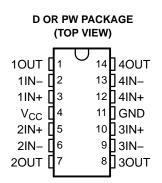
This device consists of four independent high-gain frequency-compensated operational amplifiers that are designed specifically to operate from a single supply over a wide range of voltages. Operation from split supplies is possible when the difference between the two supplies is 3 V to 26 V (3 V to 32 V for V-suffixed devices) and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. The low supply-current drain is independent of the magnitude of the supply voltage.

Applications include transducer amplifiers, dc amplification blocks, and all the conventional operational-amplifier circuits that now can be more easily implemented in single-supply voltage systems. For example, the LM2902 can be operated directly from the standard 5-V supply that is used in digital systems and easily provides the required interface electronics without requiring additional ±15-V supplies.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

- Common-Mode Input Voltage Range Includes Ground, Allowing Direct Sensing Near Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage:
 - Non-V devices . . . 26 V
 - V-Suffix devices . . . 32 V
- V-Suffix devices . . . 32 V D Open-Loop Differential Voltage Amplification . . . 100 V/mV Typ
- Internal Frequency Compensation



LM2902-EP SGLS335A-APRIL 2006-REVISED APRIL 2006



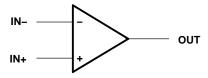
T _A	V _{IO} max AT 25°C	MAX V _{CC}	PACKAGE ⁽¹⁾		ORDERABLE PART NUMBER	TOP-SIDE MARKING		
	7 mV	26 V	SOIC (D)	Reel of 2500	LM2902QDREP ⁽²⁾	2902EP		
	7 mv	20 V	TSSOP(PW)	Reel of 2500	LM2902QPWREP ⁽²⁾	2902EP		
4000 to 40500	7>/	22.1/	SOIC (D)	Reel of 2500	LM2902KVQDREP ⁽²⁾	2902KVE		
–40°C to 125°C	7 mV	32 V	TSSOP(PW)	Reel of 2500	LM2902KVQPWREP ⁽²⁾	2902KVE		
	3 mV	32 V	SOIC (D)	Reel of 2500	LM2902KAVQDREP ⁽²⁾	LM2902E		
			TSSOP(PW)	Reel of 2500	LM2902KAVQPWREP	LM2902E		
	7 mV	20.1/	SOIC (D)	Reel of 2500	LM2902MDREP ⁽²⁾	2902ME		
		26 V	TSSOP(PW)	Reel of 2000	LM2902MPWREP ⁽²⁾	2902ME		
	7	22.1/	SOIC (D)	Reel of 2500	LM2902KVMDREP ⁽²⁾	2902KME		
–55°C to 125°C	7 mV	32 V	TSSOP(PW)	Reel of 2000	LM2902KVMPWREP ⁽²⁾	2902KME		
	2) (22.1/	SOIC (D)	Reel of 2500	LM2902KAVMDREP ⁽²⁾	2902KAE		
	3 mV	32 V	TSSOP(PW)	Reel of 2000	LM2902KAVMPWREP ⁽²⁾	2902KAE		

ORDERING INFORMATION

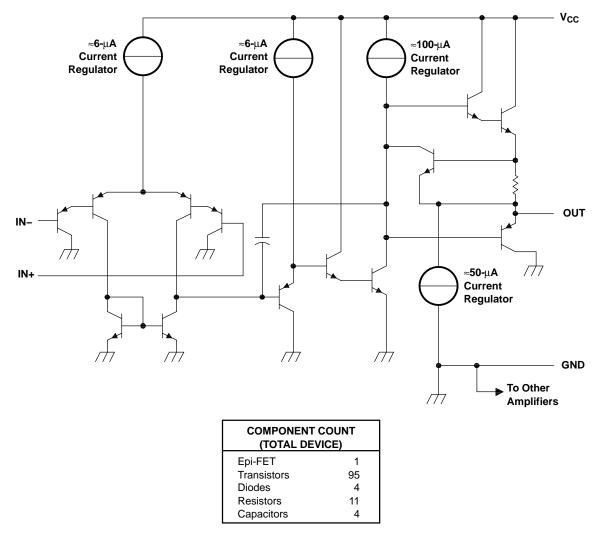
(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

(2) Product Preview

SYMBOL (EACH AMPLIFIER)



SCHEMATIC (EACH AMPLIFIER)



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ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range (unless otherwise noted)⁽¹⁾

			LM2902-EP	LM2902KV-EP	UNIT
V_{CC}	Supply voltage ⁽²⁾		26	32	V
V_{ID}	Differential input voltage ⁽³⁾	±26	±32	V	
VI	Input voltage (either input)	-0.3 to 26	-0.3 to 32	V	
	Duration of output short circuit (one amplifier) to gro $V_{CC} \le 15 \ V^{(4)}$	Unlimited	Unlimited		
0	Package thermal impedance ⁽⁵⁾⁽⁶⁾	D package (0 LFPM)	101	101	°C/W
θ_{JA}	Package therman impedance (0)(0)	PW package	113	113	C/VV
TJ	Operating virtual junction temperature	142	142	°C	
T _{stg}	Storage temperature range ⁽⁷⁾	-65 to 150	-65 to 150	°C	

(1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

All voltage values, except differential voltages and V_{CC} specified for the measurement of I_{OS}, are with respect to the network GND. (2)

(3) Differential voltages are at IN+ with respect to IN-.

(d) Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.
(f) Maximum power dissipation is a function of T_J(max), θ_{JA}, and T_A. The maximum allowable power dissipation at any allowable ambient temperature is P_D = (T_J(max) - T_A)/θJ_A. Operating at the absolute maximum T_J of 142°C can affect reliability.

The package thermal impedance is calculated in accordance with JESD 51-7. (6)

(7) Long term high-temperature storage and/or extended use at maximum recommended operating conditions may result in reduction of overall device life. See http://www.ti.com/ep_quality for additional information on enhanced plastic packaging.

ELECTRICAL CHARACTERISTICS

at specified free-air temperature, V_{CC} = 5 V (unless otherwise noted)

	DADAMETED	PARAMETER TEST CONDITIONS ⁽¹⁾		LM2902-EP				
	PARAMETER	TEST CONDITIONS()		MIN	TYP ⁽³⁾	MAX	UNIT	
	Input offect veltage	$V_{CC} = 5 V \text{ to } 26 V,$	25°C		3	7	mV	
V _{IO}	Input offset voltage	$V_{IC} = V_{ICR}$ min, $V_O = 1.4$ V	Full range			10		
I _{IO}	Input offset current	V _O = 1.4 V	25°C		2	50	nA	
	input onset current	$v_0 = 1.4 v$	Full range			300	ΠA	
	Input biog ourrent	$V_{0} = 1.4 V$	25°C		-20	-250	nA	
IB	Input bias current	$v_0 = 1.4 v$	Full range			-500	ΠA	
V _{ICR} Common-mode	Common-mode input voltage	$V_{1} = 5 V_{1} to 26 V_{2}$	25°C	0 to V _{CC} – 1.5				
	range	$V_{CC} = 5 V \text{ to } 26 V$	Full range	0 to V _{CC} – 2			V	
V _{OH} ł		$R_L = 10 \ k\Omega$	25°C	V _{CC} - 1.5				
	High-level output voltage	V_{CC} = 26 V, R_L = 2 k Ω	Full range	22			V	
		V_{CC} = 26 V, $R_L \ge 10 \ k\Omega$	$_{\rm C}$ = 26 V, R _L \geq 10 k Ω 25°C 23		24			
V _{OL}	Low-level output voltage	$R_{L} \le 10 \ k\Omega$	Full range		5	20	mV	
	Large-signal differential voltage	$V_{CC} = 15 \text{ V}, V_{O} = 1 \text{ V} \text{ to } 11 \text{ V},$	25°C		100		V/mV	
A _{VD}	amplification	$R_L \ge 2 k\Omega$	Full range	15				
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR}min$	25°C	50	80		dB	
k _{SVR}	Supply-voltage rejection ratio $(\Delta V_{CC}/\Delta V_{IO})$		25°C	50	100		dB	
V ₀₁ /V ₀₂	Crosstalk attenuation	f = 1 kHz to 20 kHz	25°C		120		dB	
			25°C	-20	-30			
		$V_{CC} = 15 \text{ V}, \text{ V}_{ID} = 1 \text{ V}, \text{ V}_{O} = 0$	Full range	-10		mA		
I _O	Output current		25°C	10	20		~ ^	
		$V_{CC} = 15 \text{ V}, \text{ V}_{ID} = -1 \text{ V}, \text{ V}_{O} = 15 \text{ V}$	Full range	5			mA	
		$V_{ID} = -1 V, V_O = 200 mV$	25°C		30		μA	
l _{os}	Short-circuit output current	V_{CC} at 5 V, V_{O} = 0, GND at –5 V	25°C		±40	±60	mA	
ı	Supply current (four amplificre)	$V_0 = 2.5 V$, No load	Full range		0.7	1.2	٣^	
I _{CC}	Supply current (four amplifiers)	V_{CC} = 26 V, V_{O} = 0.5 V_{CC} , No load	Full range		1.4	3	mA	

All characteristics are measured under open-loop conditions, with zero common-mode input voltage, unless otherwise specified.
Full range is -55°C to 125°C.
All typical values are at T_A = 25°C.

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ELECTRICAL CHARACTERISTICS

at specified free-air temperature, V_{CC} = 5 V (unless otherwise noted)

			NIO (1)	T _A ⁽²⁾	LM2	902KV-EP		
	PARAMETER	TEST CONDITION	UNS()		MIN	TYP ⁽³⁾	MAX	UNIT
			Non-A	25°C		3	7	
N/	land offerst coltant	$V_{CC} = 5 V \text{ to } 32 V,$	devices	Full range			10	
V _{IO}	Input offset voltage	$V_{IC} = V_{ICR}$ min, $V_O =$ 1.4 V	A-suffix	25°C		1	3	mV
			devices	Full range			4.5	
$\Delta V_{\text{IO}} / \Delta T$	Temperature drift	R _S = 0 Ω		Full range		7		μV/°C
	Input offset current	V _O = 1.4 V		25°C		2	50	nA
I _{IO}	input onset current			Full range			150	ПА
$\Delta V_{IO} / \Delta T$	Temperature drift			Full range		10		pA/°C
l	Input bias current	V _O = 1.4 V		25°C		-20	-250	nA
I _{IB}	Input bias current			Full range			-500	ΠA
V	Common-mode input voltage	$V_{CC} = 5 V \text{ to } 32 V$		25°C	0 to V _{CC} - 1.5			V
V _{ICR} rai	range	$v_{\rm CC} = 5 \ v \ 10 \ 32 \ v$	Full range	0 to V _{CC} - 2			V	
V _{OH}	High-level output voltage	$R_L = 10 \text{ k}\Omega$		25°C	V _{CC} - 1.5			
		$V_{CC} = 32 \text{ V}, \qquad \qquad R_{L} = 2 \text{ k}\Omega$		Full range	26			V
		V _{CC} = 32 V,	$R_L \ge 10 \ k\Omega$	Full range	27			
V _{OL}	Low-level output voltage	$R_L = 10 \ k\Omega$		Full range		5	20	mV
٨	Large-signal differential	$V_{CC} = 15 \text{ V}, V_{O} = 1 \text{ V} \text{ to } 11 \text{ V},$		25°C	25	100		V/mV
A _{VD}	voltage amplification	$R_L \ge 2 k\Omega$	Full range	15				
	Amplifier-to-amplifier coupling ⁽⁴⁾	f = 1 kHz to 20 kHz, inp	ut referred	25°C		120		dB
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR}min$		25°C	60	80		dB
k _{SVR}	Supply-voltage rejection ratio $(\Delta V_{CC} / \Delta V_{IO})$			25°C	60	100		dB
V ₀₁ / V ₀₂	Crosstalk attenuation	f = 1 kHz to 20 kHz		25°C		120		dB
		$V_{CC} = 15, V_{ID} = 1 V,$	$V_0 = 0$	25°C	-20	-30		mA
				Full range	-10			ША
I _O	Output current	$V_{CC} = 15 \text{ V}, \text{ V}_{ID} = -1 \text{ V},$	V _O = 15 V	25°C	10	20		mA
-0				Full range	5			ША
		$V_{ID} = -1 V,$	V _O = 200 mV	25°C	12	40		μΑ
I _{OS}	Short-circuit output current	V _{CC} at 5 V, GND at –5 V	$V_{O} = 0,$	25°C		±40	±60	mA
	Supply current (four	V _O = 2.5 V,	No load	Full range		0.7	1.2	
I _{CC}	amplifiers)	$V_{CC} = 32 \text{ V},$ $V_{O} = 0.5 \text{ V}_{CC},$	No load	Full range		1.4	3	mA

All characteristics are measured under open-loop conditions, with zero common-mode input voltage, unless otherwise specified.
Full range is -55°C to 125°C.
All typical values are at T_A = 25°C.
Due to proximity of external components, ensure that coupling is not originating via stray capacitance between these external parts. Typically, this can be detected, as this type of coupling increases at higher frequencies.

OPERATING CONDITIONS

	PARAMETER	TEST CONDITIONS	ТҮР	UNIT
SR	Slew rate at unity gain	$R_L = 1 M\Omega$, $C_L = 30 pF$, $V_I = \pm 10 V$ (see Figure 1)	0.5	V/µs
B ₁	Unity-gain bandwidth	$R_L = 1 M\Omega$, $C_L = 20 pF$ (see Figure 1)	1.2	MHz
Vn	Equivalent input noise voltage	$R_S = 100 \Omega$, $V_I = 0 V$, f = 1 kHz (see Figure 2)	35	nV/√ Hz

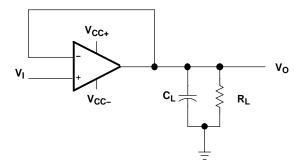


Figure 1. Unity-Gain Amplifier

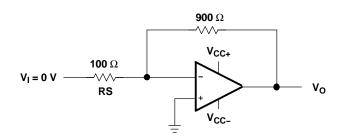


Figure 2. Noise-Test Circuit

PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION

REEL DIMENSIONS

Texas Instruments





TAPE AND REEL INFORMATION

TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

*All dimensions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM2902KAVMPWREP	TSSOP	PW	14	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1
LM2902KAVQPWREP	TSSOP	PW	14	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1

TEXAS INSTRUMENTS

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PACKAGE MATERIALS INFORMATION

14-Jul-2012



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM2902KAVMPWREP	TSSOP	PW	14	2000	367.0	367.0	35.0
LM2902KAVQPWREP	TSSOP	PW	14	2000	367.0	367.0	35.0

PW (R-PDSO-G14)

PLASTIC SMALL OUTLINE



A. An integration of the information o

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.

Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.

E. Falls within JEDEC MO-153





NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



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