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Vishay Siliconix

# P-Channel 40 V (D-S) 175 °C MOSFET

| PRODUCT SUMMARY     |                                    |                                 |  |  |  |
|---------------------|------------------------------------|---------------------------------|--|--|--|
| V <sub>DS</sub> (V) | R <sub>DS(on)</sub> (Ω)            | I <sub>D</sub> (A) <sup>d</sup> |  |  |  |
| -40                 | 0.0042 at V <sub>GS</sub> = -10 V  | -110                            |  |  |  |
| -40                 | 0.0062 at V <sub>GS</sub> = -4.5 V | -110                            |  |  |  |

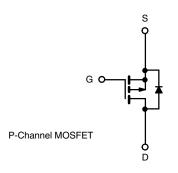
#### **FEATURES**

- TrenchFET® Power MOSFET
- Low thermal resistance









#### **Ordering Information:**

SUM110P04-04L-E3 (Lead (Pb)-free)

| ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> =                      | = 25 °C, unless otherw   | rise noted)                       |                  |    |
|---|--------------------------|-----------------------------------|------------------|----|
| PARAMETER   | SYMBOL                   | LIMIT                             | UNIT             |    |
| Drain-Source Voltage  | V <sub>DS</sub>          | -40                               | V                |    |
| Gate-Source Voltage   | V <sub>GS</sub>          | ± 20                              | v                |    |
| Continuous Drain Coursent /T 175 °C\ d                          | T <sub>C</sub> = 25 °C   |                                   | -110             |    |
| Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>d</sup> | T <sub>C</sub> = 125 °C  | l l <sup>D</sup>                  | -110             | A  |
| Pulsed Drain Current  | I <sub>DM</sub>          | -240                              | A .              |    |
| valanche Current L = 0.1 mH                                     |                          | I <sub>AS</sub>                   | -75              |    |
| Single Pulse Avalanche Energy <sup>a</sup>                      | L = 0.1 IIII             | E <sub>AS</sub>                   | 281              | mJ |
| Dower Discinstion   | T <sub>C</sub> = 25 °C   | PD                                | 375 <sup>c</sup> | w  |
| Power Dissipation   | T <sub>A</sub> = 25 °C b |                                   | 3.75             | vv |
| Operating Junction and Storage Temperature Range                |                          | T <sub>J</sub> , T <sub>stg</sub> | -55 to 175       | °C |

| THERMAL RESISTANCE RATINGS                 |                   |       |      |  |  |
|--|-------------------|-------|------|--|--|
| PARAMETER                                  | SYMBOL            | LIMIT | UNIT |  |  |
| Junction-to-Ambient PCB Mount <sup>b</sup> | R <sub>thJA</sub> | 40    | °C/W |  |  |
| Junction-to-Case                           | R <sub>thJC</sub> | 0.4   | C/W  |  |  |

#### Notes

- a. Duty cycle  $\leq$  1 %.
- b. When mounted on 1" square PCB (FR-4 material).
- c. See SOA curve for voltage derating.
- d. Limited by package.



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| PARAMETER                             | SYMBOL               | TEST CONDITIONS  | MIN. | TYP.   | MAX.   | UNIT |  |
|---------------------------------------|----------------------|--|------|--------|--------|------|--|
| Static                                | •                    |  |      |        |        |      |  |
| Drain-Source Breakdown Voltage        | $V_{DS}$             | $V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$                           | -40  |        |        | V    |  |
| Gate Threshold Voltage                | V <sub>GS(th)</sub>  | $V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$                              | -1   |        | -3     |      |  |
| Gate-Body Leakage                     | I <sub>GSS</sub>     | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$                        |      |        | ± 100  | nA   |  |
|                                       |                      | V <sub>DS</sub> = -40 V, V <sub>GS</sub> = 0 V                           |      |        | -1     |      |  |
| Zero Gate Voltage Drain Current       | I <sub>DSS</sub>     | V <sub>DS</sub> = -40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C  |      |        | -50    | μA   |  |
|                                       |                      | V <sub>DS</sub> = -40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C  |      |        | -250   |      |  |
| On-State Drain Current <sup>a</sup>   | I <sub>D(on)</sub>   | $V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$                          | -120 |        |        | Α    |  |
|                                       |                      | V <sub>GS</sub> = -10 V, I <sub>D</sub> = -30 A                          |      | 0.0034 | 0.0042 | Ω    |  |
| Drain-Source On-State Resistance a    |                      | V <sub>GS</sub> = -10 V, I <sub>D</sub> = -30 A, T <sub>J</sub> = 125 °C |      |        | 0.0063 |      |  |
| Drain-Source On-State Resistance 4    | R <sub>DS(on)</sub>  | V <sub>GS</sub> = -10 V, I <sub>D</sub> = -30 A, T <sub>J</sub> = 175 °C |      |        | 0.0076 |      |  |
|                                       |                      | $V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$                           |      | 0.005  | 0.0062 | Ì    |  |
| Forward Transconductance <sup>a</sup> | 9fs                  | V <sub>DS</sub> = -15 V, I <sub>D</sub> = -30 A                          | 20   |        |        | S    |  |
| Dynamic <sup>b</sup>                  |                      |  |      |        |        |      |  |
| Input Capacitance                     | C <sub>iss</sub>     |  |      | 11 200 |        | pF   |  |
| Output Capacitance                    | Coss                 | $V_{GS} = 0 \text{ V}, V_{DS} = -25 \text{ V}, f = 1 \text{ MHz}$        |      | 1650   |        |      |  |
| Reverse Transfer Capacitance          | C <sub>rss</sub>     |  |      | 1200   |        |      |  |
| Total Gate Charge <sup>c</sup>        | Qg                   |  |      | 235    | 350    | nC   |  |
| Gate-Source Charge <sup>c</sup>       | Q <sub>gs</sub>      | $V_{DS} = -20 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -110 \text{ A}$   |      | 45     |        |      |  |
| Gate-Drain Charge <sup>c</sup>        | $Q_{gd}$             |  |      | 65     |        |      |  |
| Gate Resistance                       | $R_g$                |  |      | 3      |        | Ω    |  |
| Turn-On Delay Time <sup>c</sup>       | t <sub>d(on)</sub>   |  |      | 25     | 40     |      |  |
| Rise Time <sup>c</sup>                | t <sub>r</sub>       | $V_{DD} = -20 \text{ V}, R_1 = 0.18 \Omega$                              |      | 30     | 45     | ns   |  |
| Turn-Off Delay Time <sup>c</sup>      | t <sub>d(off)</sub>  | $I_D \cong -110 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 2.5 \Omega$    |      | 190    | 300    |      |  |
| Fall Time <sup>c</sup>                | t <sub>f</sub>       |  |      | 110    | 165    |      |  |
| Source-Drain Diode Ratings and Cha    | racteristics         | (T <sub>C</sub> = 25 °C) <sup>b</sup>                                    |      |        |        |      |  |
| Continuous Current                    | I <sub>S</sub>       |  |      |        | -110   | ^    |  |
| Pulsed Current                        | I <sub>SM</sub>      |  |      |        | -240   | A    |  |
| Forward Voltage <sup>a</sup>          | $V_{SD}$             | $I_F = -85 \text{ A}, V_{GS} = 0 \text{ V}$                              |      | -1     | -1.5   | V    |  |
| Reverse Recovery Time                 | t <sub>rr</sub>      |  |      | 65     | 100    | ns   |  |
| Peak Reverse Recovery Current         | I <sub>RM(REC)</sub> | I <sub>F</sub> = -85 A, dl/dt = 100 A/μs                                 |      | -3.7   | -5.6   | Α    |  |
| Reverse Recovery Charge               | Q <sub>rr</sub>      |  |      | 0.12   | 0.28   | μC   |  |

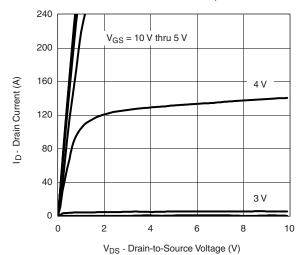
#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

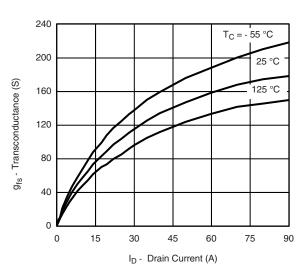
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



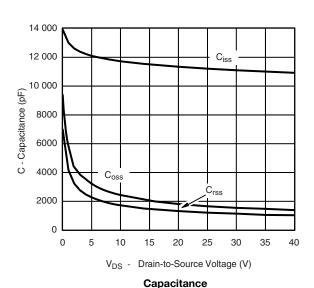
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

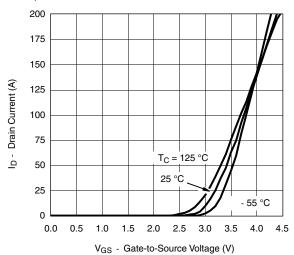




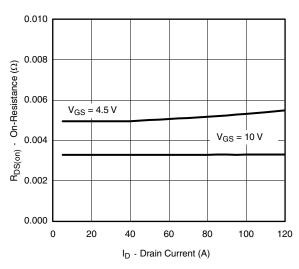


### Transconductance

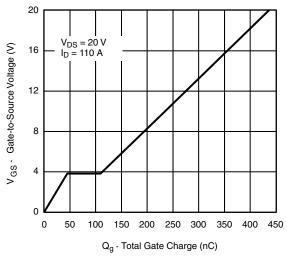




#### **Transfer Characteristics**



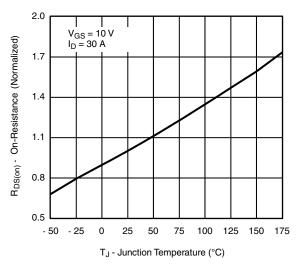
#### On-Resistance vs. Drain Current



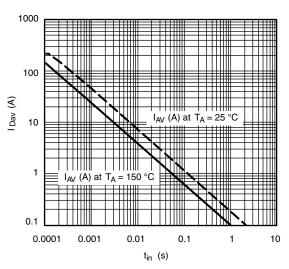
**Gate Charge** 



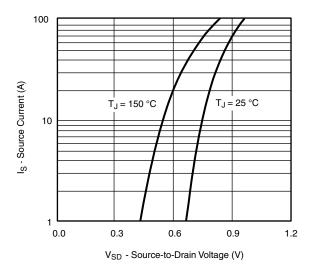
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



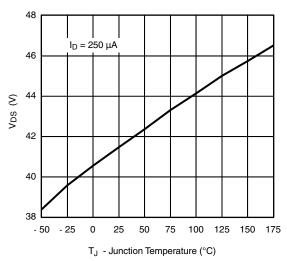
#### On-Resistance vs. Junction Temperature



Avalanche Current vs. Time



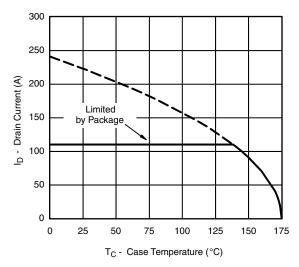
Source-Drain Diode Forward Voltage



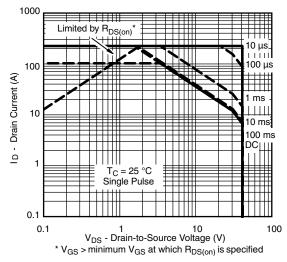
**Drain Source Breakdown vs. Junction Temperature** 



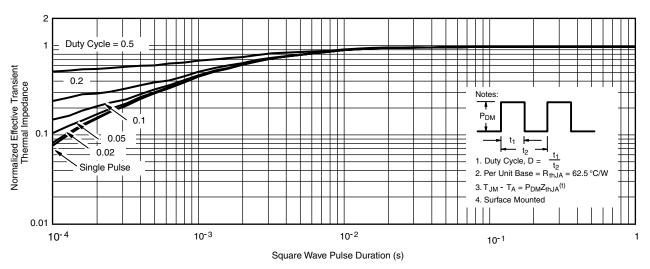
#### THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area

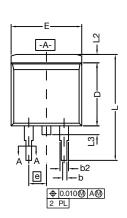


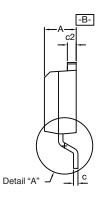
Normalized Thermal Transient Impedance, Junction-to-Case

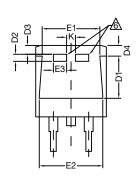
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# TO-263 (D<sup>2</sup>PAK): 3-LEAD

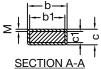








DETAIL A (ROTATED 90°)



| _ | ,  | —b<br><del>-</del> -b | <br>1 |        |     | 1        |
|---|----|-----------------------|-------|--------|-----|----------|
| 2 | T  |                       |       | C      | _ ( | <u>-</u> |
|   | SE | ^TIC                  | M     | ا<br>م |     | 1        |

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

6 This feature is for thick lead.

|                                 |            | INCHES    |       | MILLIMETERS |        |  |
|---------------------------------|------------|-----------|-------|-------------|--------|--|
|                                 | DIM.       | MIN.      | MAX.  | MIN.        | MAX.   |  |
| Α                               |            | 0.160     | 0.190 | 4.064       | 4.826  |  |
|                                 | b          | 0.020     | 0.039 | 0.508       | 0.990  |  |
|                                 | b1         | 0.020     | 0.035 | 0.508       | 0.889  |  |
|                                 | b2         | 0.045     | 0.055 | 1.143       | 1.397  |  |
| c*                              | Thin lead  | 0.013     | 0.018 | 0.330       | 0.457  |  |
|                                 | Thick lead | 0.023     | 0.028 | 0.584       | 0.711  |  |
| c1                              | Thin lead  | 0.013     | 0.017 | 0.330       | 0.431  |  |
| CI                              | Thick lead | 0.023     | 0.027 | 0.584       | 0.685  |  |
|                                 | c2         | 0.045     | 0.055 | 1.143       | 1.397  |  |
|                                 | D          | 0.340     | 0.380 | 8.636       | 9.652  |  |
|                                 | D1         | 0.220     | 0.240 | 5.588       | 6.096  |  |
|                                 | D2         | 0.038     | 0.042 | 0.965       | 1.067  |  |
|                                 | D3         | 0.045     | 0.055 | 1.143       | 1.397  |  |
|                                 | D4         | 0.044     | 0.052 | 1.118       | 1.321  |  |
|                                 | Е          | 0.380     | 0.410 | 9.652       | 10.414 |  |
|                                 | E1         | 0.245     | -     | 6.223       | -      |  |
|                                 | E2         | 0.355     | 0.375 | 9.017       | 9.525  |  |
|                                 | E3         | 0.072     | 0.078 | 1.829       | 1.981  |  |
|                                 | е          | 0.100     | BSC   | 2.54 BSC    |        |  |
|                                 | K          | 0.045     | 0.055 | 1.143       | 1.397  |  |
|                                 | L          | 0.575     | 0.625 | 14.605      | 15.875 |  |
| L1                              |            | 0.090     | 0.110 | 2.286       | 2.794  |  |
|                                 | L2         | 0.040     | 0.055 | 1.016       | 1.397  |  |
| L3                              |            | 0.050     | 0.070 | 1.270       | 1.778  |  |
|                                 | L4         | 0.010 BSC |       | 0.254 BSC   |        |  |
| М                               |            | -         | 0.002 | -           | 0.050  |  |
| ECN: T13-0707-Rev. K, 30-Sep-13 |            |           |       |             |        |  |

DWG: 5843





### RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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Revision: 13-Jun-16 1 Document Number: 91000