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Vishay Semiconductors

Schottky Rectifier, 2 x 10 A



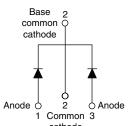
V_F at I_F

I_{RM} max.

T_J max.

Diode variation

 E_{AS}



0.57 V

15 mA at 125 °C

150 °C

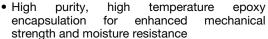
Common cathode

8 mJ

TO-220AB	Anode 0 2 O Anode 1 Common 3 cathode
PRODUCT SUMMARY	
Package	TO-220AB
I _{F(AV)}	2 x 10 A
V_{R}	35 V, 45 V

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	SYMBOL CHARACTERISTICS VALUES UNI								
I _{F(AV)}	Rectangular waveform (per device)	20	Α						
V _{RRM}		35/45	V						
I _{FRM}	T _C = 135 °C (per leg)	20							
I _{FSM}	t _p = 5 μs sine	1060	A						
V _F	10 A _{pk} , T _J = 125 °C	0.57	V						
TJ	Range	- 65 to 150	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-MBR2035CTPbF	VS-MBR2035CT-N3	VS-MBR2045CTPbF	VS-MBR2045CT-N3	UNITS			
Maximum DC reverse voltage	V _R	35	35	45	45	V			
Maximum working peak reverse voltage	V_{RWM}	33	33	43	43	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS		CONDITIONS	VALUES	UNITS		
Maximum average per leg	I	T 135 °C rated V-		10			
forward current per device	I _{F(AV)}	IC = 133 O, Taled VR	$T_C = 135$ °C, rated V_R				
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20	kHz, T _C = 135 °C	20			
Non-repetitive peak surge current	I _{FSM} .	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1060	Α		
		Surge applied at rated load condition half wave, single phase, 60 Hz		150			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \ x \ V_R$ typical		2			
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4 mH		8	mJ		



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		20 A	T _J = 25 °C	0.84		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	10 A	T 405 00	0.57	V	
		20 A	T _J = 125 °C	0.72		
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.1	mA	
Maximum instantaneous reverse current		T _J = 125 °C	hated DC voltage	15		
Threshold voltage	V _{F(TO)}	T. – T. maximum		0.354	٧	
Forward slope resistance	r _t	rj = rj maximum	$T_J = T_J$ maximum			
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	600	pF		
Typical series inductance	L _S	Measured from top of term	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _R 10 000 V/ _k			

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		BOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature	range T	J		- 65 to 150	°C	
Maximum storage temperature r	ange T _S	itg		- 65 to 175	- U	
Maximum thermal resistance, junction to case per leg		JC	DC operation	2.0	°C/W	
Typical thermal resistance, case to heatsink	R _{th}	cs	Mounting surface, smooth and greased (only for TO-220)	0.50	C/VV	
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque r	minimum		Non-lubricated threads	6 (5)	kgf · cm	
	naximum		Non-lubricated tilleads	12 (10)	(lbf · in)	
Marking device			Constitution TO 220AB	MBR2035CT		
		Case style TO-220AB		MBR2045CT		

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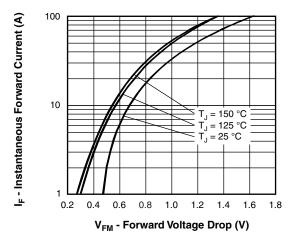


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

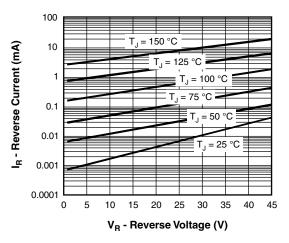


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

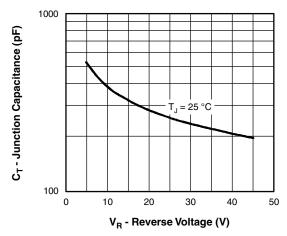


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

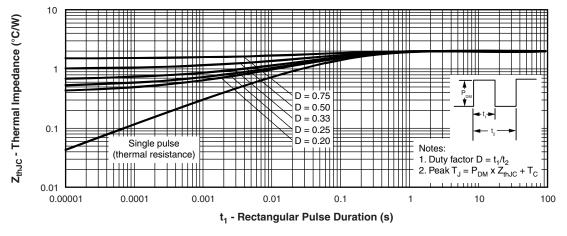


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

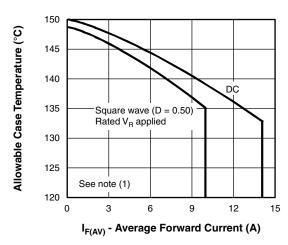


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

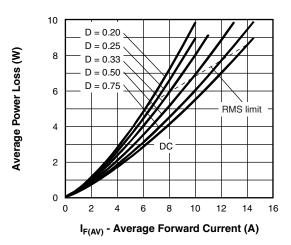


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

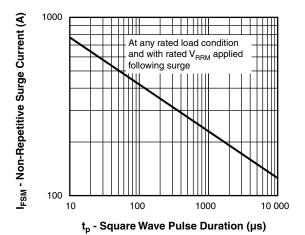


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

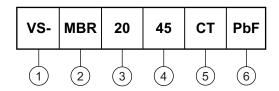
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Schottky MBR series

- Current rating (20 = 20 A)

35 = 35 V 45 = 45 V

CT = Essential part number

6 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBR2035CTPbF	50	1000	Antistatic plastic tube				
VS-MBR2035CT-N3	50	1000	Antistatic plastic tube				
VS-MBR2045CTPbF	50	1000	Antistatic plastic tube				
VS-MBR2045CT-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95222				
Dout moulting information	TO-220AB PbF	www.vishay.com/doc?95225				
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028				
SPICE model		www.vishay.com/doc?95295				



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches



Lead assignments

Diodes

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
		•	•	•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip



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