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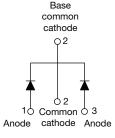
Vishay Semiconductors

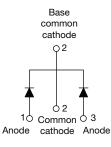
High Performance Schottky Rectifier, 2 x 5 A

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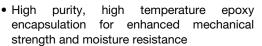
D²PAK

TO-262

PRODUCT SUMMARY				
Package	D ² PAK, TO-262			
I _{F(AV)}	2 x 5 A			
V_{R}	150 V			
V _F at I _F	0.93 V			
I _{RM}	7 mA at 125 °C			
T _J max.	175 °C			
Diode variation	Common cathode			
E _{AS}	5 mJ			

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- · High frequency operation





ROHS COMPLIANT HALOGEN FREE

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	10	A		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	620	A		
V _F	5 A _{pk} , T _J = 125 °C (per leg)	0.73	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS					
PARAMETER SYMBOL VS-10CTQ150SPbF VS-10CTQ150-1PbF UNITS					
Maximum DC reverse voltage	V_{R}	150	V		
Maximum working peak reverse voltage	V_{RWM}	150	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS			VALUES	UNITS
Maximum average per leg		50 % duty ovalo at T = 155 °C	rootongular wayafarm	5	۸
forward current, see fig. 5 per device	I _{F(AV)}	50 % duty cycle at T_C = 155 °C, rectangular waveform		10	Α
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load		
surge current per leg, see fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	115	A
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25$ °C, $I_{AS} = 1$ A, $L = 10$ mH	l	5	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu	o in 1 µs um V _A = 1.5 x V _R typical	1	Α



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		5 A	T _{.1} = 25 °C	0.93	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	10 A	1j = 23 °C	1.10	V
See fig. 1	VFM (1)	5 A	T 405.00	0.73	
		10 A	T _J = 125 °C	0.86	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Datad V	0.05	A
See fig. 2		T _J = 125 °C	V _R = Rated V _R	7	mA
Threshold voltage	V _{F(TO)}	T T maying una		0.468	V
Forward slope resistance	r _t	$T_J = T_J$ maximum		28	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal rang	ge 100 kHz to 1 MHz), 25 °C	200	рF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0 nF		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECH	THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Maximum junction and sto temperature range	rage	T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation	3.50		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.75	°C/W	
Typical thermal resistance, case to heatsink (only for TO-220)		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mauratia a taurana minimum				6 (5)	kgf · cm	
Mounting torque -	maximum			12 (10)	(lbf · in)	
Marking davisa	And the desired		Case style D ² PAK	10CTQ	150S	
Marking device			Case style TO-262	10CTQ1	50-1	

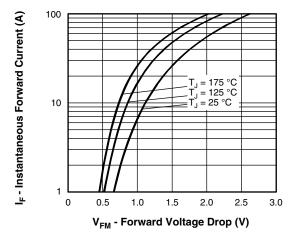


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

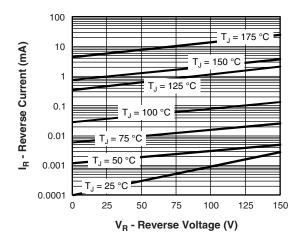


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

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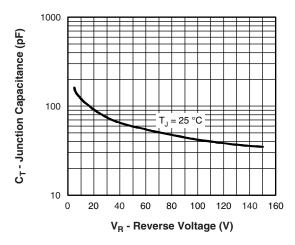


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

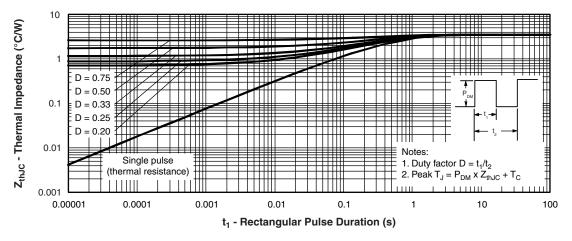


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

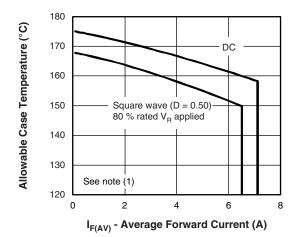


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

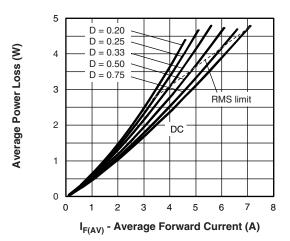


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

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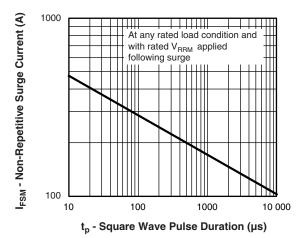


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

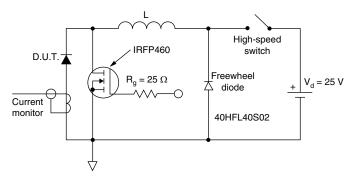


Fig. 8 - Unclamped Inductive Test Circuit

Note

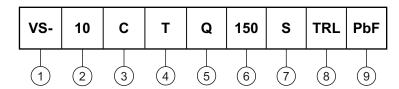
 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = 10 \mbox{ V} \\ \end{array}$

VS-10CTQ150SPbF, VS-10CTQ150-1PbF

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (10 A)

- Circuit configuration: C = common cathode

4 - T = TO-220

5 - Schottky "Q" series

Voltage rating (150 = 150 V)

7 - • S = D²PAK

• -1 = TO-262

8 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented - for D²PAK only)

• TRR = tape and reel (right oriented - for D²PAK only)

9 - PbF = lead (Pb)-free

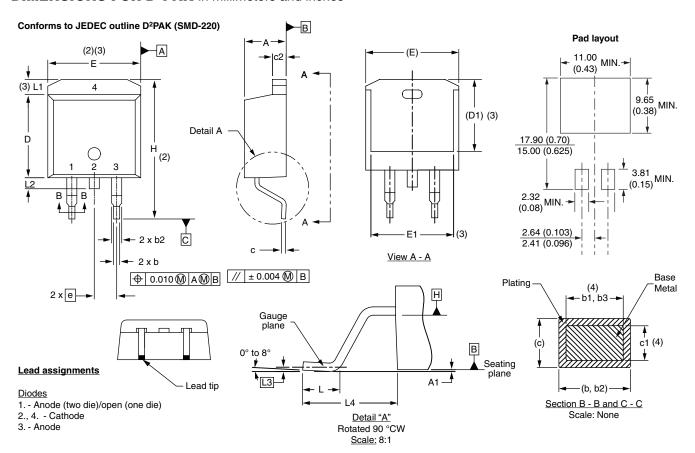
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			



Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches



ovupo.	MILLIM	IETERS	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

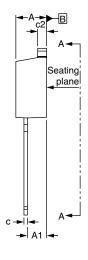
(7) Outline conforms to JEDEC outline TO-263AB

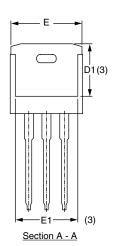
Vishay High Power Products

D²PAK, TO-262



DIMENSIONS FOR TO-262 in millimeters and inches





⊕ 0.010**⋒**|A**⋒**|B

Lead assignments

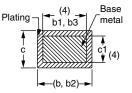


<u>Diodes</u>

-3 x b2 --3 x b

> 1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

OVMDOL	MILLIMETERS		INC	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.10	0 BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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