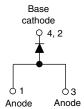


## Schottky Rectifier, 5.5 A





**D-PAK (TO-252AA)** 

PRODUCT SUMMARY			
Package	D-PAK (TO-252AA)		
I <sub>F(AV)</sub>	5.5 A		
$V_{R}$	100 V		
V <sub>F</sub> at I <sub>F</sub>	See Electrical table		
I <sub>RM</sub>	4 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
Diode variation	Single die		
Eas	6 mJ		

#### **FEATURES**

- Popular D-PAK outline
- Small foot print, surface mountable



- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- $\bullet$  Meets MSL level 1, per J-STD-020, LF maximum peak of 260  $^{\circ}\text{C}$
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-50WQ10FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	5.5	А	
$V_{RRM}$		100	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	330	А	
V <sub>F</sub>	5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.63	V	
T <sub>J</sub>	Range	- 40 to 150	°C	

VOLTAGE RATINGS				
PARAMETER SYMBOL		VS-50WQ10FNPbF	UNITS	
Maximum DC reverse voltage	$V_{R}$	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 135 °C, rectangular waveform		5.5	
Maximum peak one cycle non-repetitive surge current I <sub>ESM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	330	Α	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	110	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.5 A, L = 40 mH		6.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		0.5	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1		5 A	T <sub>J</sub> = 25 °C	0.77	
	V <sub>FM</sub> <sup>(1)</sup>	10 A		0.91	V
	VFM ('')	5 A	T <sub>J</sub> = 125 °C	0.63	v
		10 A		0.74	
Maximum reverse leakage current	. (1)	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1	mA
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C		4	IIIA
Threshold voltage	V <sub>F(TO)</sub>	$T_{\rm J} = T_{\rm J}  {\rm maximum}$ 0.47 21.46		0.47	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C 183		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.0		nΗ	

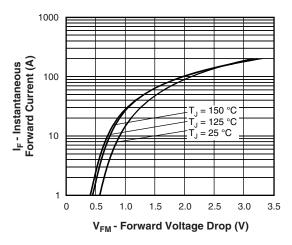
#### Note

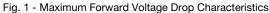
 $^{(1)}$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 40 to 150	°C
Maximum thermal resistance, junction to case	$R_{thJC}$	DC operation See fig. 4	3.0	°C/W
Approximate weight			0.3	g
Approximate weight			0.01	OZ.
Marking device		Case style D-PAK (similar to TO-252AA)	50WQ10FN	

#### Note

$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$





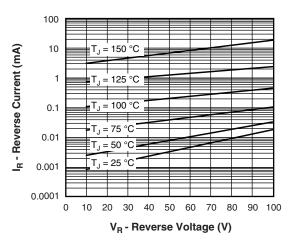


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

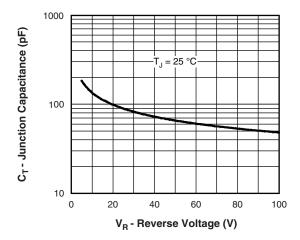


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

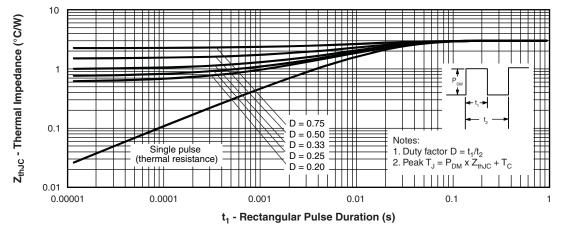
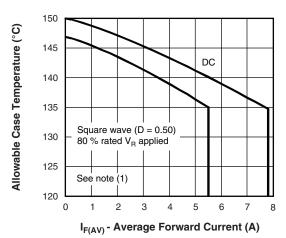


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

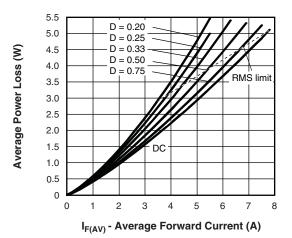


Fig. 6 - Forward Power Loss Characteristics

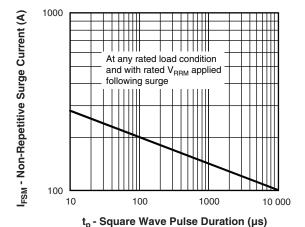


Fig. 7 - Maximum Non-Repetitive Surge Current

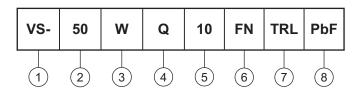
#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (5.5 A)

Package identifier:

W = D-PAK

4 - Schottky "Q" series

Voltage rating (10 = 100 V)

- FN = TO-252AA (D-PAK)

7 - • None = Tube (50 pieces)

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

8 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95016		
Part marking information	www.vishay.com/doc?95059		
Packaging information	www.vishay.com/doc?95033		
SPICE model	www.vishay.com/doc?95549		



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