

Hyperfast Rectifier, 6 A FRED Pt®

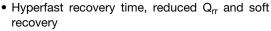




TO-252AA (D-PAK)

PRODUCT SUMMARY						
Package	TO-252AA (D-PAK)					
I _{F(AV)}	6 A					
V_{R}	600 V					
V _F at I _F	1.26 V					
t _{rr} (typ.)	18 ns					
T _J max.	175 °C					
Diode variation	Single die					

FEATURES





- 175 °C maximum operating junction temperature
- For PFC CRM/CCM operation
- Low forward voltage drop
- · Low leakage current

RoHS COMPLIANT **HALOGEN** FREE

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Peak repetitive reverse voltage	V_{RRM}		600	V				
Average rectified forward current	I _{F(AV)}	T _C = 144 °C	6					
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	70	Α				
Peak repetitive forward current	I _{FM}	T _C = 144 °C, f = 20 kHz, d = 50 %	12					
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-			
Forward voltage	V _F	I _F = 6 A	-	1.60	2.1	V		
Torward voltage		I _F = 6 A, T _J = 150 °C	-	1.26	1.7			
Reverse leakage current	_	$V_R = V_R$ rated	-	-	50			
neverse leakage current	IR	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	250	μA		
Junction capacitance	C _T	V _R = 600 V	-	3.5	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH		





DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1 A, dI_F/dt = 10$	$00 \text{ A/}\mu\text{s}, V_{\text{R}} = 30 \text{ V}$	ı	18	25			
		$I_F = 1 A, dI_F/dt = 50$	-	22	-	no			
	t _{rr}	T _J = 25 °C	$I_F = 6 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	27	-	ns		
		T _J = 125 °C		-	37	-			
Dook room ourrent	I _{RRM}	T _J = 25 °C		-	4.1	-	Α		
Peak recovery current		T _J = 125 °C		-	5.3	-	A		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	57	-	nC		
		T _J = 125 °C		-	103	-			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C			
Thermal resistance, junction to case per leg	R_{thJC}		-	-	3	°C/W			
Approximate weight				0.3		g			
Approximate weight				0.01		OZ.			
Marking device		Case style TO-252AA (D-PAK)	6EWH06FN						

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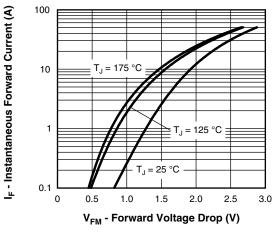


Fig. 1 - Typical Forward Voltage Drop Characteristics

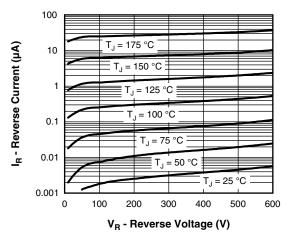


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

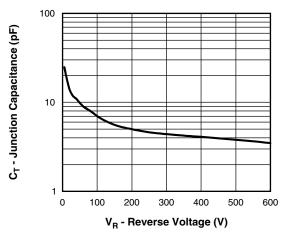


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

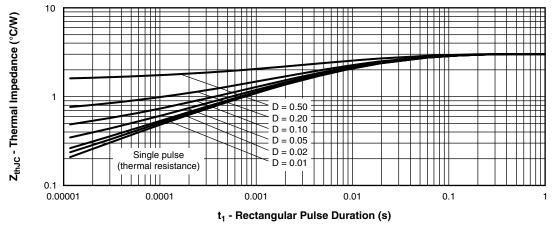


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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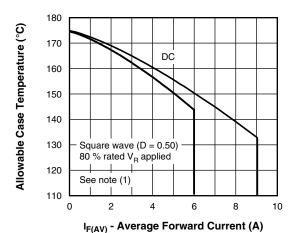


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

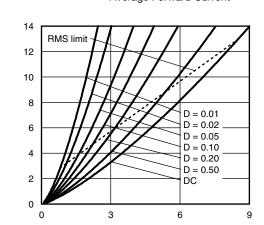


Fig. 6 - Forward Power Loss Characteristics

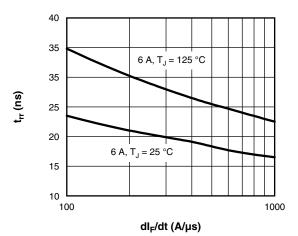


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

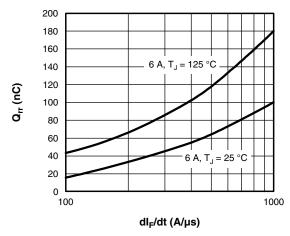


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

Average Power Loss (W)

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R

I_{F(AV)} - Average Forward Current (A)

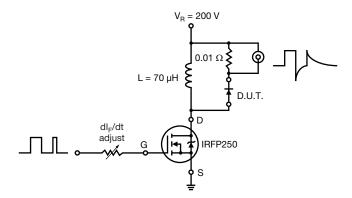
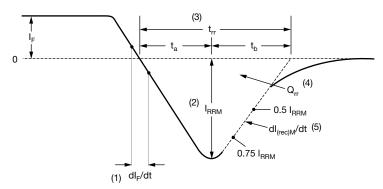


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_{r}$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

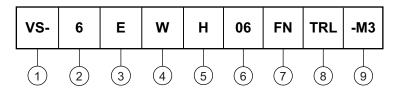
(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_{b} portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (6 = 6 A)

3 - Circuit configuration:

E = single diode

4 - Package identifier:

W = D-PAK

5 - H = hyperfast recovery

Voltage rating (06 = 600 V)

7 - FN = TO-252AA

8 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6EWH06FN-M3	75	3000	Antistatic plastic tube				
VS-6EWH06FNTR-M3	2000	2000	13" diameter reel				
VS-6EWH06FNTRL-M3	3000	3000	13" diameter reel				
VS-6EWH06FNTRR-M3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95627</u>						
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95187					



NOTES

3

2

MAX.

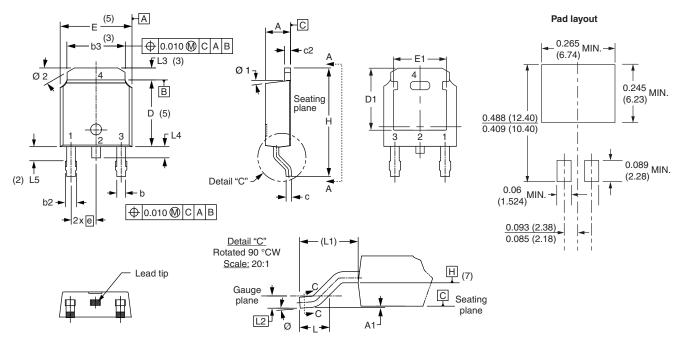
0.410 0.070

0.050

0.040 0.060

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		HES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STINIBUL	MIN.	MAX.	MIN.	MAX
Α	2.18	2.39	0.086	0.094		е	2.29	BSC	0.090	BSC
A1	-	0.13	-	0.005		Н	9.40	10.41	0.370	0.41
b	0.64	0.89	0.025	0.035		L	1.40	1.78	0.055	0.07
b2	0.76	1.14	0.030	0.045		L1	2.74	BSC	0.108	REF.
b3	4.95	5.46	0.195	0.215	3	L2	0.51	BSC	0.020	BSC
С	0.46	0.61	0.018	0.024		L3	0.89	1.27	0.035	0.05
c2	0.46	0.89	0.018	0.035		L4	-	1.02	-	0.04
D	5.97	6.22	0.235	0.245	5	L5	1.14	1.52	0.045	0.06
D1	5.21	-	0.205	-	3	Ø	0°	10°	0°	10°
Е	6.35	6.73	0.250	0.265	5	Ø1	0°	15°	0°	15°
E1	4.32	-	0.170	-	3	Ø2	25°	35°	25°	35°

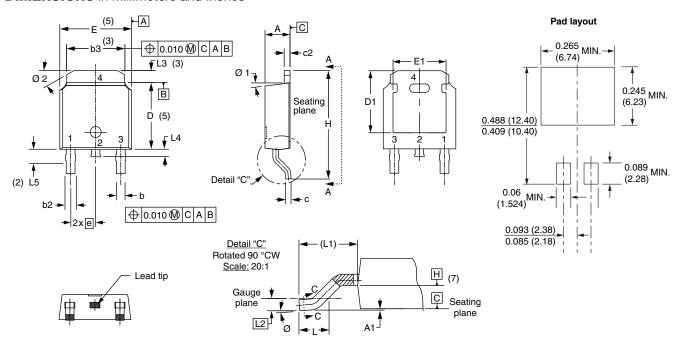
Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	2.18	2.39	0.086	0.094		
A1	-	0.13	-	0.005		
b	0.64	0.89	0.025	0.035		
b2	0.76	1.14	0.030	0.045		
b3	4.95	5.46	0.195	0.215	3	
С	0.46	0.61	0.018	0.024		
c2	0.46	0.89	0.018	0.035		
D	5.97	6.22	0.235	0.245	5	
D1	5.21	-	0.205	-	3	
Е	6.35	6.73	0.250	0.265	5	
E1	4.32	-	0.170	-	3	

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	2.74 BSC		0.108 REF.	
L2	0.51	BSC	0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0° 10°		0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
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- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC® outline TO-252AA



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