

LM4128/LM4128Q SOT-23 Precision Micropower Series Voltage Reference

Check for Samples: [LM4128](#), [LM4128Q](#)

FEATURES

- Output Voltage Initial Accuracy 0.1%
- Low Temperature Coefficient 75 ppm/°C
- Low Supply Current, 60 μ A
- Enable Pin Allowing a 3 μ A Shutdown Mode
- Up to 20 mA Output Current
- Voltage Options 1.8V, 2.048V, 2.5V, 3.0V, 3.3V, 4.096V
- Custom Voltage Options Available (1.8V to 4.096V)
- V_{IN} Range of $V_{REF} + 400$ mV to 5.5V @10 mA
- Stable with Low ESR Ceramic Capacitors
- 5-Pin SOT-23 Package
- -40°C to 125°C Junction Temperature Range
- LM4128AQ/BQ/CQ/DQ are AEC-Q100 Grade 1 Qualified and are Manufactured on an Automotive Grade Flow

APPLICATIONS

- Instrumentation & Process Control
- Test Equipment
- Data Acquisition Systems
- Base Stations
- Servo Systems
- Portable, Battery Powered Equipment
- Automotive & Industrial Electronics
- Precision Regulators
- Battery Chargers
- Communications
- Medical Equipment

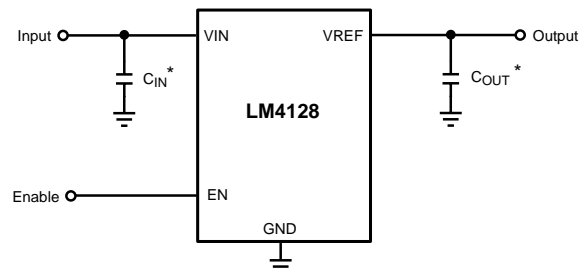
DESCRIPTION

Ideal for space critical applications, the LM4128 precision voltage reference is available in the SOT-23 surface-mount package. The LM4128's advanced design eliminates the need for an external stabilizing capacitor while ensuring stability with capacitive loads up to 10 μ F, thus making the LM4128 easy to use.

Series references provide lower power consumption than shunt references, since they do not have to idle the maximum possible load current under no load conditions. This advantage, the low quiescent current (60 μ A), and the low dropout voltage (400 mV) make the LM4128 ideal for battery-powered solutions.

The LM4128 is available in four grades (A, B, C, and D) for greater flexibility. The best grade devices (A) have an initial accuracy of 0.1% with ensured temperature coefficient of 75 ppm/°C or less, while the lowest grade parts (D) have an initial accuracy of 1.0% and a tempco of 100 ppm/°C.

Typical Application Circuit



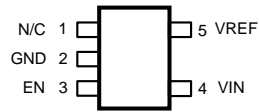
*Note: The capacitor C_{IN} is required and the capacitor C_{OUT} is optional.



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Connection Diagram



5-Pin SOT-23 Package – Top View
See Package Number DBV (R-PDSO-G5)

PIN DESCRIPTIONS

Pin #	Name	Function
1	N/C	No connect pin, leave floating
2	GND	Ground
3	EN	Enable pin
4	VIN	Input supply
5	VREF	Reference output



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings⁽¹⁾

Maximum Voltage on any input		-0.3 to 6V
Output short circuit duration		Indefinite
Power Dissipation ($T_A = 25^\circ\text{C}$) ⁽²⁾		350 mW
Storage Temperature Range		-65°C to 150°C
Lead Temperature	(soldering, 10sec)	260°C
	Vapor Phase (60 sec)	215°C
	Infrared (15sec)	220°C
ESD Susceptibility ⁽³⁾	Human Body Model	2 kV

- (1) Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. Operating Ratings indicate conditions for which the device is intended to be functional, but do not ensure specific performance limits. For ensured specifications, see Electrical Characteristics.
- (2) Without PCB copper enhancements. The maximum power dissipation must be de-rated at elevated temperatures and is limited by T_{JMAX} (maximum junction temperature), θ_{J-A} (junction to ambient thermal resistance) and T_A (ambient temperature). The maximum power dissipation at any temperature is: $P_{DissMAX} = (T_{JMAX} - T_A) / \theta_{J-A}$ up to the value listed in the Absolute Maximum Ratings. θ_{J-A} for SOT-23 package is $220^\circ\text{C}/\text{W}$, $T_{JMAX} = 125^\circ\text{C}$.
- (3) The human body model is a 100 pF capacitor discharged through a 1.5 k Ω resistor into each pin.

Operating Ratings

Maximum Input Supply Voltage	5.5V
Maximum Enable Input Voltage	V_{IN}
Maximum Load Current	20mA
Junction Temperature Range (T_J)	-40°C to $+125^\circ\text{C}$

Electrical Characteristics

LM4128-1.8 (V_{OUT} = 1.8V)

Limits in standard type are for T_J = 25°C only, and limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C unless otherwise specified. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at T_J = 25°C, and are provided for reference purposes only. Unless otherwise specified V_{IN} = 5V and I_{LOAD} = 0A.

Symbol	Parameter	Conditions	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Unit
V _{REF}	Output Voltage Initial Accuracy					
	LM4128A-1.8	(A Grade - 0.1%)	-0.1		+0.1	%
	LM4128B-1.8	(B Grade - 0.2%)	-0.2		+0.2	
	LM4128C-1.8	(C Grade - 0.5%)	-0.5		+0.5	
LM4128D-1.8	(D Grade - 1.0%)	-1.0		+1.0		
TCV _{REF} / °C ⁽³⁾	Temperature Coefficient	LM4128A-1.8			75	ppm / °C
		LM4128B-1.8			75	
		LM4128C-1.8			100	
		LM4128D-1.8			100	
I _Q	Supply Current			60	100	μA
I _{Q_SD}	Supply Current in Shutdown	EN = 0V		3	7	μA
ΔV _{REF} /ΔV _{IN}	Line Regulation	V _{REF} + 400 mV ≤ V _{IN} ≤ 5.5V		30		ppm / V
ΔV _{REF} /ΔI _{LOAD}	Load Regulation	0 mA ≤ I _{LOAD} ≤ 20 mA		25	120	ppm / mA
ΔV _{REF}	Long Term Stability ⁽⁴⁾	1000 Hrs		50		ppm
	Thermal Hysteresis ⁽⁵⁾	-40°C ≤ T _J ≤ +125°C		75		
V _{IN} - V _{REF}	Dropout Voltage ⁽⁶⁾	I _{LOAD} = 10 mA		200	400	mV
V _N	Output Noise Voltage	0.1 Hz to 10 Hz		170		μV _{PP}
I _{SC}	Short Circuit Current				75	mA
V _{IL}	Enable Pin Maximum Low Input Level				35	%V
V _{IH}	Enable Pin Minimum High Input Level		65			%V

- (1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlation using Statistical Quality Control.
- (2) Typical numbers are at 25°C and represent the most likely parametric norm.
- (3) Temperature coefficient is measured by the "Box" method; i.e., the maximum ΔV_{REF} is divided by the maximum ΔT.
- (4) Long term stability is V_{REF} @25°C measured during 1000 hrs.
- (5) Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from (-40°C to 125°C).
- (6) Dropout voltage is defined as the minimum input to output differential at which the output voltage drops by 0.5% below the value measured with a 5V input.

Electrical Characteristics

LM4128-2.0 (V_{OUT} = 2.048V)

Limits in standard type are for T_J = 25°C only, and limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C unless otherwise specified. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at T_J = 25°C, and are provided for reference purposes only. Unless otherwise specified V_{IN} = 5V and I_{LOAD} = 0A.

Symbol	Parameter	Conditions	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Unit
V _{REF}	Output Voltage Initial Accuracy					
	LM4128A-2.0	(A Grade - 0.1%)	-0.1		+0.1	%
	LM4128B-2.0	(B Grade - 0.2%)	-0.2		+0.2	
	LM4128C-2.0	(C Grade - 0.5%)	-0.5		+0.5	
LM4128D-2.0	(D Grade - 1.0%)	-1.0		+1.0		
TCV _{REF} / °C ⁽³⁾	Temperature Coefficient	LM4128A-2.0			75	ppm / °C
		LM4128B-2.0			75	
		LM4128C-2.0			100	
		LM4128D-2.0			100	
I _Q	Supply Current			60	100	μA
I _{Q_SD}	Supply Current in Shutdown	EN = 0V		3	7	μA
ΔV _{REF} /ΔV _{IN}	Line Regulation	V _{REF} + 400 mV ≤ V _{IN} ≤ 5.5V		30		ppm / V
ΔV _{REF} /ΔI _{LOAD}	Load Regulation	0 mA ≤ I _{LOAD} ≤ 20 mA		25	120	ppm / mA
ΔV _{REF}	Long Term Stability ⁽⁴⁾	1000 Hrs		50		ppm
	Thermal Hysteresis ⁽⁵⁾	-40°C ≤ T _J ≤ +125°C		75		
V _{IN} - V _{REF}	Dropout Voltage ⁽⁶⁾	I _{LOAD} = 10 mA		175	400	mV
V _N	Output Noise Voltage	0.1 Hz to 10 Hz		190		μV _{PP}
I _{SC}	Short Circuit Current				75	mA
V _{IL}	Enable Pin Maximum Low Input Level				35	%V
V _{IH}	Enable Pin Minimum High Input Level		65			%V

- (1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlation using Statistical Quality Control.
- (2) Typical numbers are at 25°C and represent the most likely parametric norm.
- (3) Temperature coefficient is measured by the "Box" method; i.e., the maximum ΔV_{REF} is divided by the maximum ΔT.
- (4) Long term stability is V_{REF} @25°C measured during 1000 hrs.
- (5) Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from (-40°C to 125°C).
- (6) Dropout voltage is defined as the minimum input to output differential at which the output voltage drops by 0.5% below the value measured with a 5V input.

Electrical Characteristics

LM4128-2.5 (V_{OUT} = 2.5V)

Limits in standard type are for T_J = 25°C only, and limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C unless otherwise specified. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at T_J = 25°C, and are provided for reference purposes only. Unless otherwise specified V_{IN} = 5V and I_{LOAD} = 0A.

Symbol	Parameter	Conditions	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Unit
V _{REF}	Output Voltage Initial Accuracy					
	LM4128A-2.5	(A Grade - 0.1%)	-0.1		+0.1	%
	LM4128B-2.5	(B Grade - 0.2%)	-0.2		+0.2	
	LM4128C-2.5	(C Grade - 0.5%)	-0.5		+0.5	
LM4128D-2.5	(D Grade - 1.0%)	-1.0		+1.0		
TCV _{REF} / °C ⁽³⁾	Temperature Coefficient	LM4128A-2.5			75	ppm / °C
		LM4128B-2.5			75	
		LM4128C-2.5			100	
		LM4128D-2.5			100	
I _Q	Supply Current			60	100	μA
I _{Q_SD}	Supply Current in Shutdown	EN = 0V		3	7	μA
ΔV _{REF} /ΔV _{IN}	Line Regulation	V _{REF} + 400 mV ≤ V _{IN} ≤ 5.5V		50		ppm / V
ΔV _{REF} /ΔI _{LOAD}	Load Regulation	0 mA ≤ I _{LOAD} ≤ 20 mA		25	120	ppm / mA
ΔV _{REF}	Long Term Stability ⁽⁴⁾	1000 Hrs		50		ppm
	Thermal Hysteresis ⁽⁵⁾	-40°C ≤ T _J ≤ +125°C		75		
V _{IN} - V _{REF}	Dropout Voltage ⁽⁶⁾	I _{LOAD} = 10 mA		175	400	mV
V _N	Output Noise Voltage	0.1 Hz to 10 Hz		275		μV _{PP}
I _{SC}	Short Circuit Current				75	mA
V _{IL}	Enable Pin Maximum Low Input Level				35	%V
V _{IH}	Enable Pin Minimum High Input Level		65			%V

(1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlation using Statistical Quality Control.

(2) Typical numbers are at 25°C and represent the most likely parametric norm.

(3) Temperature coefficient is measured by the "Box" method; i.e., the maximum ΔV_{REF} is divided by the maximum ΔT.

(4) Long term stability is V_{REF} @25°C measured during 1000 hrs.

(5) Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from (-40°C to 125°C).

(6) Dropout voltage is defined as the minimum input to output differential at which the output voltage drops by 0.5% below the value measured with a 5V input.

Electrical Characteristics

LM4128-3.0 (V_{OUT} = 3.0V)

Limits in standard type are for T_J = 25°C only, and limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C unless otherwise specified. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at T_J = 25°C, and are provided for reference purposes only. Unless otherwise specified V_{IN} = 5V and I_{LOAD} = 0A.

Symbol	Parameter	Conditions	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Unit
V _{REF}	Output Voltage Initial Accuracy					
	LM4128A-3.0	(A Grade - 0.1%)	-0.1		+0.1	%
	LM4128B-3.0	(B Grade - 0.2%)	-0.2		+0.2	
	LM4128C-3.0	(C Grade - 0.5%)	-0.5		+0.5	
LM4128D-3.0	(D Grade - 1.0%)	-1.0		+1.0		
TCV _{REF} / °C ⁽³⁾	Temperature Coefficient	LM4128A-3.0			75	ppm / °C
		LM4128B-3.0			75	
		LM4128C-3.0			100	
		LM4128D-3.0			100	
I _Q	Supply Current			60	100	μA
I _{Q_SD}	Supply Current in Shutdown	EN = 0V		3	7	μA
ΔV _{REF} /ΔV _{IN}	Line Regulation	V _{REF} + 400 mV ≤ V _{IN} ≤ 5.5V		70		ppm / V
ΔV _{REF} /ΔI _{LOAD}	Load Regulation	0 mA ≤ I _{LOAD} ≤ 20 mA		25	120	ppm / mA
ΔV _{REF}	Long Term Stability ⁽⁴⁾	1000 Hrs		50		ppm
	Thermal Hysteresis ⁽⁵⁾	-40°C ≤ T _J ≤ +125°C		75		
V _{IN} - V _{REF}	Dropout Voltage ⁽⁶⁾	I _{LOAD} = 10 mA		175	400	mV
V _N	Output Noise Voltage	0.1 Hz to 10 Hz		285		μV _{PP}
I _{SC}	Short Circuit Current				75	mA
V _{IL}	Enable Pin Maximum Low Input Level				35	%V
V _{IH}	Enable Pin Minimum High Input Level		65			%V

- (1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlation using Statistical Quality Control.
- (2) Typical numbers are at 25°C and represent the most likely parametric norm.
- (3) Temperature coefficient is measured by the "Box" method; i.e., the maximum ΔV_{REF} is divided by the maximum ΔT.
- (4) Long term stability is V_{REF} @25°C measured during 1000 hrs.
- (5) Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from (-40°C to 125°C).
- (6) Dropout voltage is defined as the minimum input to output differential at which the output voltage drops by 0.5% below the value measured with a 5V input.

Electrical Characteristics

LM4128-3.3 (V_{OUT} = 3.3V)

Limits in standard type are for T_J = 25°C only, and limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C unless otherwise specified. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at T_J = 25°C, and are provided for reference purposes only. Unless otherwise specified V_{IN} = 5V and I_{LOAD} = 0A.

Symbol	Parameter	Conditions	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Unit
V _{REF}	Output Voltage Initial Accuracy					
	LM4128A-3.3	(A Grade - 0.1%)	-0.1		+0.1	%
	LM4128B-3.3	(B Grade - 0.2%)	-0.2		+0.2	
	LM4128C-3.3	(C Grade - 0.5%)	-0.5		+0.5	
LM4128D-3.3	(D Grade - 1.0%)	-1.0		+1.0		
TCV _{REF} / °C ⁽³⁾	Temperature Coefficient	LM4128A-3.3			75	ppm / °C
		LM4128B-3.3			75	
		LM4128C-3.3			100	
		LM4128D-3.3			100	
I _Q	Supply Current			60	100	μA
I _{Q_SD}	Supply Current in Shutdown	EN = 0V		3	7	μA
ΔV _{REF} /ΔV _{IN}	Line Regulation	V _{REF} + 400 mV ≤ V _{IN} ≤ 5.5V		85		ppm / V
ΔV _{REF} /ΔI _{LOAD}	Load Regulation	0 mA ≤ I _{LOAD} ≤ 20 mA		25	120	ppm / mA
ΔV _{REF}	Long Term Stability ⁽⁴⁾	1000 Hrs		50		ppm
	Thermal Hysteresis ⁽⁵⁾	-40°C ≤ T _J ≤ +125°C		75		
V _{IN} - V _{REF}	Dropout Voltage ⁽⁶⁾	I _{LOAD} = 10 mA		175	400	mV
V _N	Output Noise Voltage	0.1 Hz to 10 Hz		310		μV _{PP}
I _{SC}	Short Circuit Current				75	mA
V _{IL}	Enable Pin Maximum Low Input Level				35	%V
V _{IH}	Enable Pin Minimum High Input Level		65			%V

- (1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlation using Statistical Quality Control.
- (2) Typical numbers are at 25°C and represent the most likely parametric norm.
- (3) Temperature coefficient is measured by the "Box" method; i.e., the maximum ΔV_{REF} is divided by the maximum ΔT.
- (4) Long term stability is V_{REF} @25°C measured during 1000 hrs.
- (5) Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from (-40°C to 125°C).
- (6) Dropout voltage is defined as the minimum input to output differential at which the output voltage drops by 0.5% below the value measured with a 5V input.

Electrical Characteristics

LM4128-4.1 ($V_{OUT} = 4.096V$)

Limits in standard type are for $T_J = 25^\circ C$ only, and limits in **boldface type** apply over the junction temperature (T_J) range of -40°C to +125°C unless otherwise specified. Minimum and Maximum limits are ensured through test, design, or statistical correlation. Typical values represent the most likely parametric norm at $T_J = 25^\circ C$, and are provided for reference purposes only. Unless otherwise specified $V_{IN} = 5V$ and $I_{LOAD} = 0A$.

Symbol	Parameter	Conditions	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Unit
V_{REF}	Output Voltage Initial Accuracy					
	LM4128A-4.1	(A Grade - 0.1%)	-0.1		+0.1	%
	LM4128B-4.1	(B Grade - 0.2%)	-0.2		+0.2	
	LM4128C-4.1	(C Grade - 0.5%)	-0.5		+0.5	
LM4128D-4.1	(D Grade - 1.0%)	-1.0		+1.0		
$TCV_{REF} / ^\circ C^{(3)}$	Temperature Coefficient	LM4128A-4.1			75	ppm / °C
		LM4128B-4.1			75	
		LM4128C-4.1			100	
		LM4128D-4.1			100	
I_Q	Supply Current			60	100	μA
I_{Q_SD}	Supply Current in Shutdown	EN = 0V		3	7	μA
$\Delta V_{REF}/\Delta V_{IN}$	Line Regulation	$V_{REF} + 400\text{ mV} \leq V_{IN} \leq 5.5V$		100		ppm / V
$\Delta V_{REF}/\Delta I_{LOAD}$	Load Regulation	$0\text{ mA} \leq I_{LOAD} \leq 20\text{ mA}$		25	120	ppm / mA
ΔV_{REF}	Long Term Stability ⁽⁴⁾	1000 Hrs		50		ppm
	Thermal Hysteresis ⁽⁵⁾	$-40^\circ C \leq T_J \leq +125^\circ C$		75		
$V_{IN} - V_{REF}$	Dropout Voltage ⁽⁶⁾	$I_{LOAD} = 10\text{ mA}$		175	400	mV
V_N	Output Noise Voltage	0.1 Hz to 10 Hz		350		μV_{PP}
I_{SC}	Short Circuit Current				75	mA
V_{IL}	Enable Pin Maximum Low Input Level				35	%V
V_{IH}	Enable Pin Minimum High Input Level		65			%V

- (1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlation using Statistical Quality Control.
- (2) Typical numbers are at 25°C and represent the most likely parametric norm.
- (3) Temperature coefficient is measured by the "Box" method; i.e., the maximum ΔV_{REF} is divided by the maximum ΔT .
- (4) Long term stability is V_{REF} @25°C measured during 1000 hrs.
- (5) Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from (-40°C to 125°C).
- (6) Dropout voltage is defined as the minimum input to output differential at which the output voltage drops by 0.5% below the value measured with a 5V input.

Typical Performance Characteristics for 2.5V

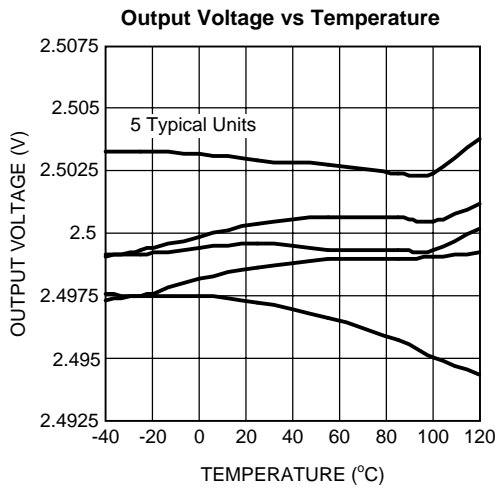


Figure 1.

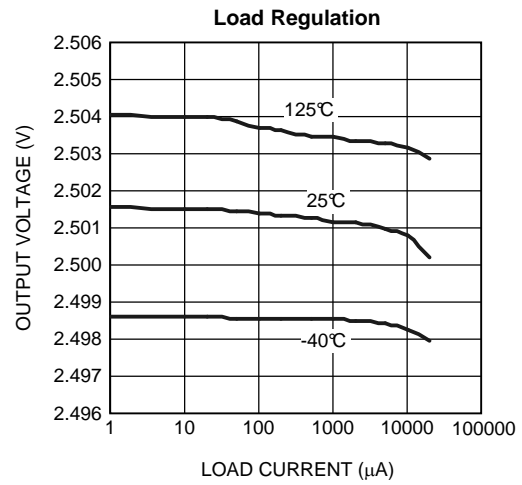


Figure 2.

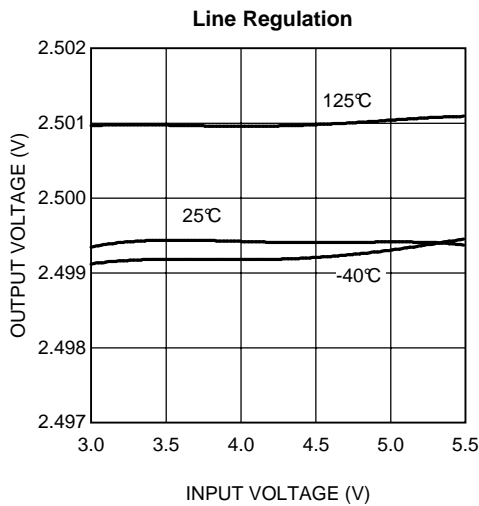


Figure 3.

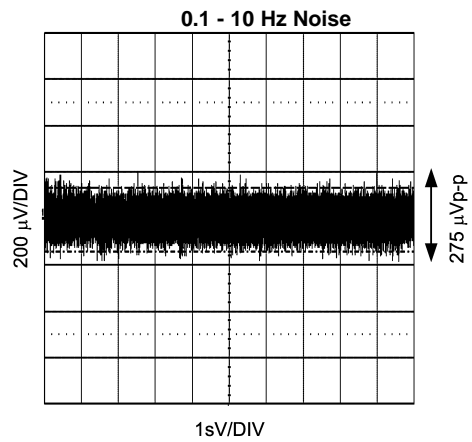


Figure 4.

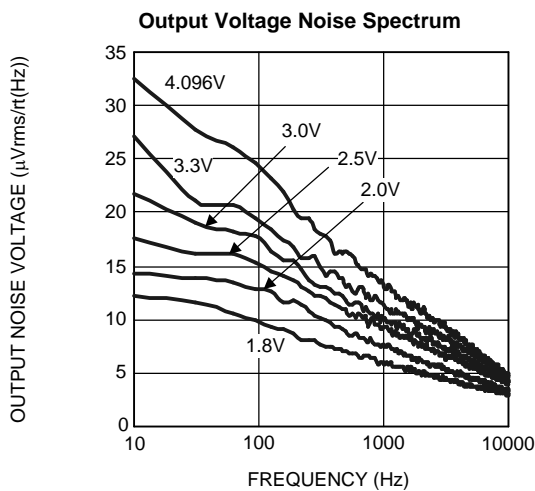


Figure 5.

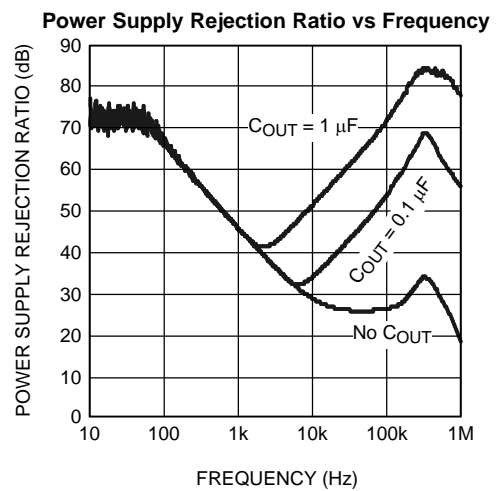


Figure 6.

Typical Performance Characteristics for 2.5V (continued)

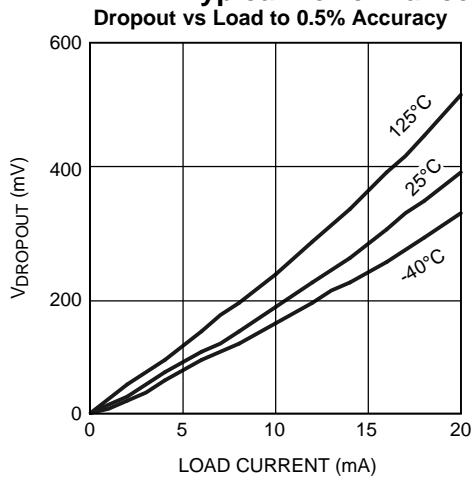


Figure 7.

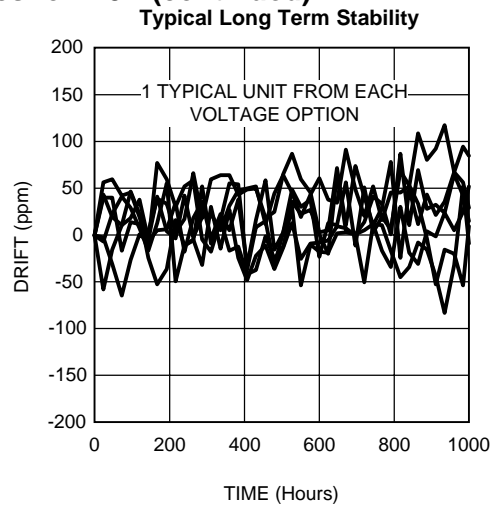


Figure 8.

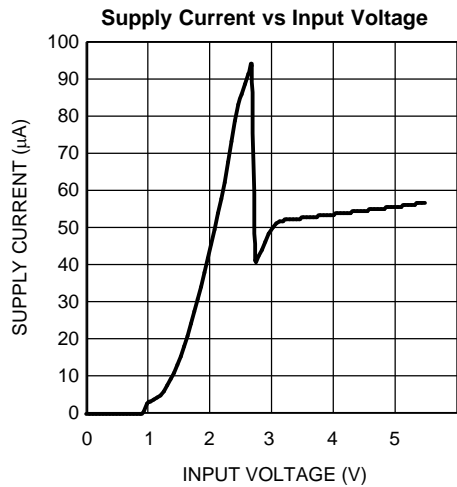


Figure 9.

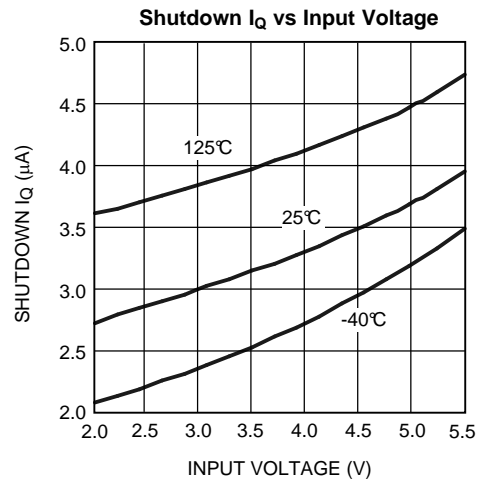


Figure 10.

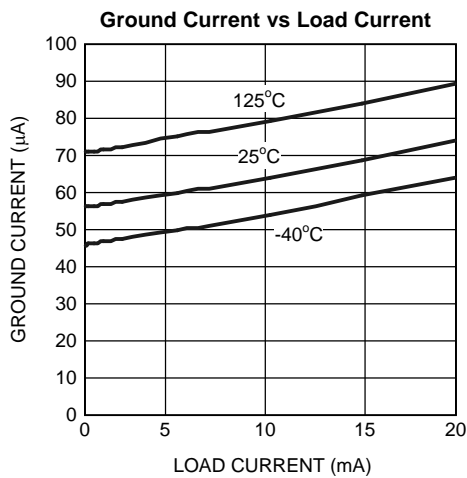


Figure 11.

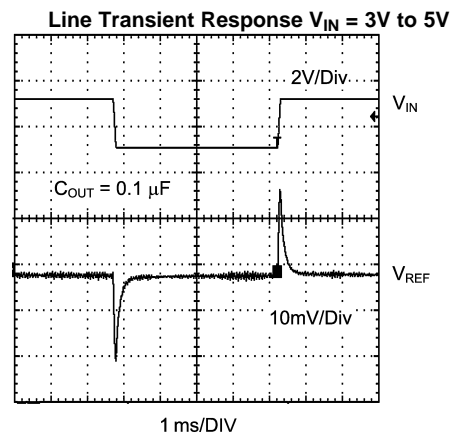


Figure 12.

Typical Performance Characteristics for 2.5V (continued)

Load Transient Response $I_{LOAD} = 0$ to 10mA

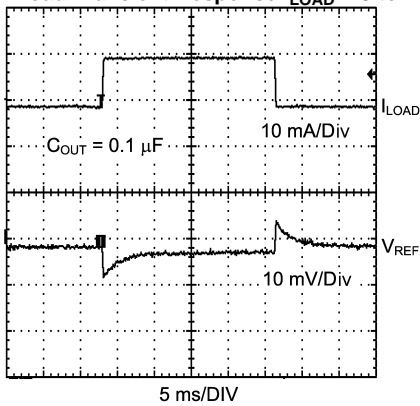


Figure 13.

Short-Circuit Protection and Recovery

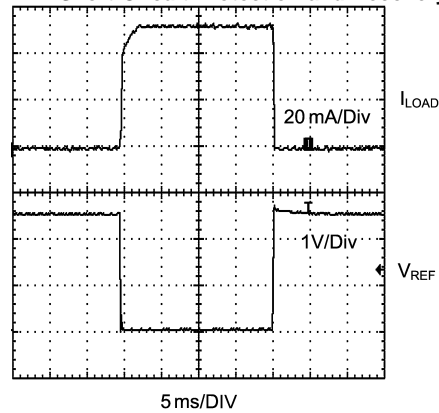


Figure 14.

Start-Up Response

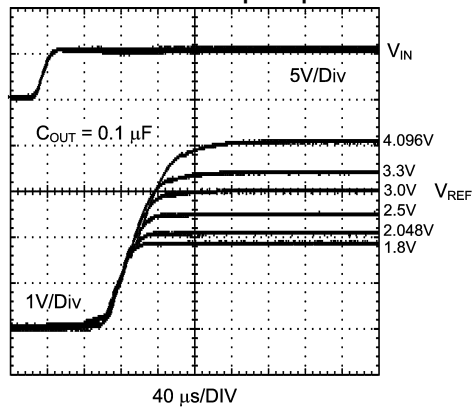


Figure 15.

APPLICATION INFORMATION

THEORY OF OPERATION

The foundation of any voltage reference is the band-gap circuit. While the reference in the LM4128 is developed from the gate-source voltage of transistors in the IC, principles of the band-gap circuit are easily understood using a bipolar example. For a detailed analysis of the bipolar band-gap circuit, see the AN-56 Application Report ([SNVA514](#)).

SUPPLY AND ENABLE VOLTAGES

To ensure proper operation, V_{EN} and V_{IN} must be within a specified range. An acceptable range of input voltages is

$$V_{IN} > V_{REF} + 400 \text{ mV} \quad (I_{LOAD} \leq 10 \text{ mA}) \quad (1)$$

The enable pin uses an internal pull-up current source ($I_{PULL_UP} \approx 2 \mu\text{A}$) that may be left floating or triggered by an external source. If the part is not enabled by an external source, it may be connected to V_{IN} . An acceptable range of enable voltages is given by the enable transfer characteristics. See the [Electrical Characteristics](#) section and Enable Transfer Characteristics figure for more detail. Note, the part will not operate correctly for $V_{EN} > V_{IN}$.

COMPONENT SELECTION

A small ceramic (X5R or X7R) capacitor on the input must be used to ensure stable operation. The value of C_{IN} must be sized according to the output capacitor value. The value of C_{IN} must satisfy the relationship $C_{IN} \geq C_{OUT}$. When no output capacitor is used, C_{IN} must have a minimum value of 0.1 μF . Noise on the power-supply input may affect the output noise. Larger input capacitor values (typically 4.7 μF to 22 μF) may help reduce noise on the output and significantly reduce overshoot during startup. Use of an additional optional bypass capacitor between the input and ground may help further reduce noise on the output. With an input capacitor, the LM4128 will drive any combination of resistance and capacitance up to $V_{REF}/20 \text{ mA}$ and 10 μF respectively.

The LM4128 is designed to operate with or without an output capacitor and is stable with capacitive loads up to 10 μF . Connecting a capacitor between the output and ground will significantly improve the load transient response when switching from a light load to a heavy load. The output capacitor should not be made arbitrarily large because it will effect the turn-on time as well as line and load transients.

While a variety of capacitor chemistry types may be used, it is typically advisable to use low esr ceramic capacitors. Such capacitors provide a low impedance to high frequency signals, effectively bypassing them to ground. Bypass capacitors should be mounted close to the part. Mounting bypass capacitors close to the part will help reduce the parasitic trace components thereby improving performance.

SHORT CIRCUITED OUTPUT

The LM4128 features indefinite short circuit protection. This protection limits the output current to 75 mA when the output is shorted to ground.

TURN ON TIME

Turn on time is defined as the time taken for the output voltage to rise to 90% of the preset value. The turn on time depends on the load. The turn on time is typically 33.2 μs when driving a 1 μF load and 78.8 μs when driving a 10 μF load. Some users may experience an extended turn on time (up to 10 ms) under brown out conditions and low temperatures (-40°C).

THERMAL HYSTERESIS

Thermal hysteresis is defined as the change in output voltage at 25°C after some deviation from 25°C. This is to say that thermal hysteresis is the difference in output voltage between two points in a given temperature profile. An illustrative temperature profile is shown in [Figure 16](#).

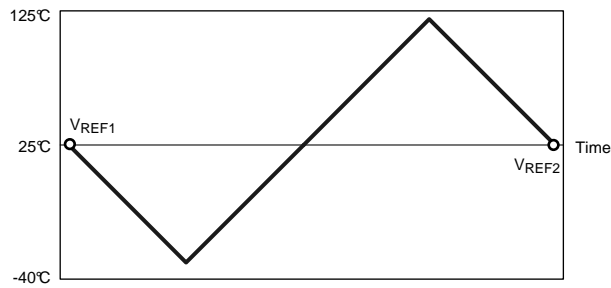


Figure 16. Illustrative Temperature Profile

This may be expressed analytically as the following:

$$V_{HYS} = \frac{|V_{REF1} - V_{REF2}|}{V_{REF}} \times 10^3 \text{ mV}$$

where

- V_{HYS} = Thermal hysteresis expressed in ppm
- V_{REF} = Nominal preset output voltage
- $V_{REF1} = V_{REF}$ before temperature fluctuation
- $V_{REF2} = V_{REF}$ after temperature fluctuation

(2)

The LM4128 features a low thermal hysteresis of 190 μV from -40°C to 125°C .

TEMPERATURE COEFFICIENT

Temperature drift is defined as the maximum deviation in output voltage over the operating temperature range. This deviation over temperature may be illustrated as shown in Figure 17.

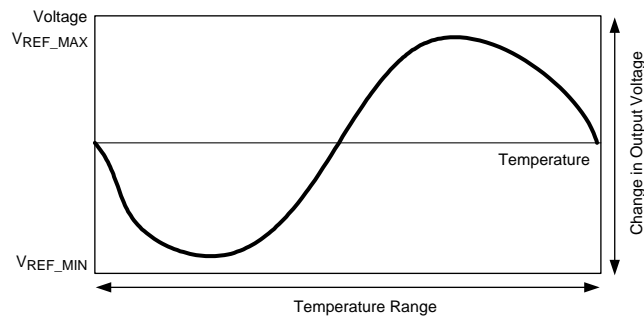


Figure 17. Illustrative Temperature Coefficient Profile

Temperature coefficient may be expressed analytically as the following:

$$T_D = \frac{(V_{REF_MAX} - V_{REF_MIN})}{V_{REF} \times \Delta T} \times 10^6 \text{ ppm}$$

where

- T_D = Temperature drift
- V_{REF} = Nominal preset output voltage
- V_{REF_MIN} = Minimum output voltage over operating temperature range
- V_{REF_MAX} = Maximum output voltage over operating temperature range
- ΔT = Operating temperature range

(3)

The LM4128 features a low temperature drift of 75 ppm (max) to 100 ppm (max), depending on the grade, from -40°C to 125°C.

LONG TERM STABILITY

Long-term stability refers to the fluctuation in output voltage over a long period of time (1000 hours). The LM4128 features a typical long-term stability of 50 ppm over 1000 hours. The measurements are made using 5 units of each voltage option, at a nominal input voltage (5V), with no load, at room temperature.

EXPRESSION OF ELECTRICAL CHARACTERISTICS

Electrical characteristics are typically expressed in mV, ppm, or a percentage of the nominal value. Depending on the application, one expression may be more useful than the other. To convert one quantity to the other one may apply the following:

ppm to mV error in output voltage:

$$\frac{V_{REF} \times \text{ppm}_{ERROR}}{10^3} = V_{ERROR}$$

where

- V_{REF} is in volts (V) and V_{ERROR} is in milli-volts (mV)

(4)

Bit error (1 bit) to voltage error (mV):

$$\frac{V_{REF}}{2^n} \times 10^3 = V_{ERROR}$$

where

- V_{REF} is in volts (V)
- V_{ERROR} is in milli-volts (mV)
- and n is the number of bits

(5)

mV to ppm error in output voltage:

$$\frac{V_{ERROR}}{V_{REF}} \times 10^3 = \text{ppm}_{ERROR}$$

where

- V_{REF} is in volts (V)
- V_{ERROR} is in milli-volts (mV)

(6)

Voltage error (mV) to percentage error (percent):

$$\frac{V_{ERROR}}{V_{REF}} \times 0.1 = \text{Percent_Error}$$

where

- V_{REF} is in volts (V)
- V_{ERROR} is in milli-volts (mV)

(7)

PRINTED CIRCUIT BOARD and LAYOUT CONSIDERATIONS

References in SOT packages are generally less prone to PC board mounting than devices in Small Outline (SOIC) packages. To minimize the mechanical stress due to PC board mounting that can cause the output voltage to shift from its initial value, mount the reference on a low flex area of the PC board, such as near the edge or a corner.

The part may be isolated mechanically by cutting a U shape slot on the PCB for mounting the device. This approach also provides some thermal isolation from the rest of the circuit.

Bypass capacitors must be mounted close to the part. Mounting bypass capacitors close to the part will reduce the parasitic trace components thereby improving performance.

Typical Application Circuits

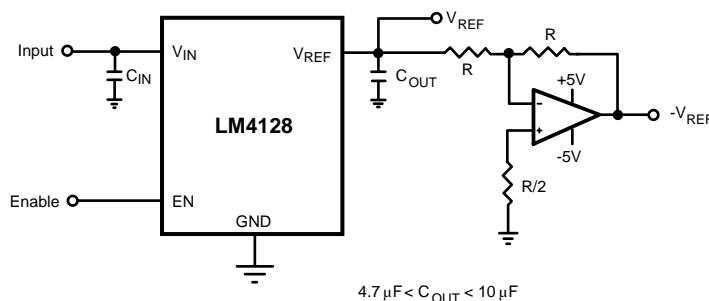


Figure 18. Voltage Reference with Complimentary Output

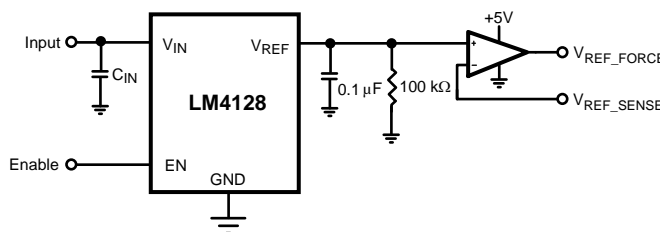


Figure 19. Precision Voltage Reference with Force and Sense Output

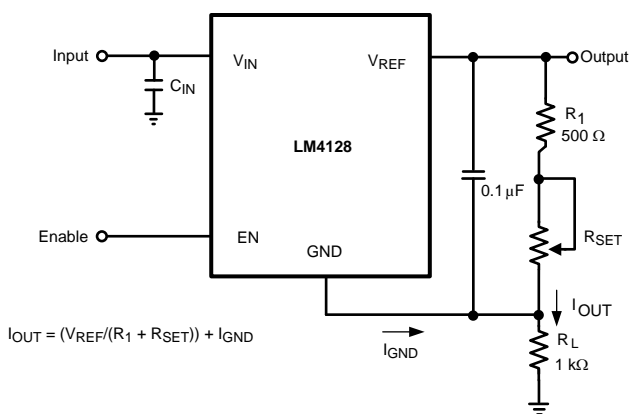


Figure 20. Programmable Current Source

REVISION HISTORY

Changes from Revision D (April 2013) to Revision E	Page
• Changed layout of National Data Sheet to TI format	15

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM4128AMF-1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AA	Samples
LM4128AMF-2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BA	Samples
LM4128AMF-2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CA	Samples
LM4128AMF-3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DA	Samples
LM4128AMF-3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5EA	Samples
LM4128AMF-4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FA	Samples
LM4128AMFX-1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AA	Samples
LM4128AMFX-2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BA	Samples
LM4128AMFX-2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CA	Samples
LM4128AMFX-3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DA	Samples
LM4128AMFX-3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5EA	Samples
LM4128AMFX-4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FA	Samples
LM4128AQ1MF1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AA	Samples
LM4128AQ1MF2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BA	Samples
LM4128AQ1MF2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CA	Samples
LM4128AQ1MF3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DA	Samples
LM4128AQ1MF3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6EA	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM4128AQ1MF4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FA	Samples
LM4128AQ1MFX1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AA	Samples
LM4128AQ1MFX2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BA	Samples
LM4128AQ1MFX2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CA	Samples
LM4128AQ1MFX3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DA	Samples
LM4128AQ1MFX3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6EA	Samples
LM4128AQ1MFX4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FA	Samples
LM4128BMF-1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AB	Samples
LM4128BMF-2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BB	Samples
LM4128BMF-2.5	NRND	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	R5CB	
LM4128BMF-2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CB	Samples
LM4128BMF-3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DB	Samples
LM4128BMF-3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5EB	Samples
LM4128BMF-4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FB	Samples
LM4128BMFX-1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AB	Samples
LM4128BMFX-2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BB	Samples
LM4128BMFX-2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CB	Samples
LM4128BMFX-3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DB	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM4128BMFX-3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5EB	Samples
LM4128BMFX-4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FB	Samples
LM4128BQ1MF1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AB	Samples
LM4128BQ1MF2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BB	Samples
LM4128BQ1MF2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CB	Samples
LM4128BQ1MF3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DB	Samples
LM4128BQ1MF3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6EB	Samples
LM4128BQ1MF4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FB	Samples
LM4128BQ1MFX1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AB	Samples
LM4128BQ1MFX2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BB	Samples
LM4128BQ1MFX2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CB	Samples
LM4128BQ1MFX3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DB	Samples
LM4128BQ1MFX3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6EB	Samples
LM4128BQ1MFX4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FB	Samples
LM4128CMF-1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AC	Samples
LM4128CMF-2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BC	Samples
LM4128CMF-2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CC	Samples
LM4128CMF-3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DC	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM4128CMF-3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5EC	Samples
LM4128CMF-4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FC	Samples
LM4128CMFX-1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AC	Samples
LM4128CMFX-2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BC	Samples
LM4128CMFX-2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CC	Samples
LM4128CMFX-3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DC	Samples
LM4128CMFX-3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5EC	Samples
LM4128CMFX-4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FC	Samples
LM4128CQ1MF1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AC	Samples
LM4128CQ1MF2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BC	Samples
LM4128CQ1MF2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CC	Samples
LM4128CQ1MF3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DC	Samples
LM4128CQ1MF3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6EC	Samples
LM4128CQ1MF4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FC	Samples
LM4128CQ1MF1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AC	Samples
LM4128CQ1MF2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BC	Samples
LM4128CQ1MF2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CC	Samples
LM4128CQ1MF3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DC	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM4128CQ1MFX3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6EC	Samples
LM4128CQ1MFX4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FC	Samples
LM4128DMF-1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AD	Samples
LM4128DMF-2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BD	Samples
LM4128DMF-2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CD	Samples
LM4128DMF-3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DD	Samples
LM4128DMF-3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5ED	Samples
LM4128DMF-4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FD	Samples
LM4128DMFX-1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5AD	Samples
LM4128DMFX-2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5BD	Samples
LM4128DMFX-2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5CD	Samples
LM4128DMFX-3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5DD	Samples
LM4128DMFX-3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5ED	Samples
LM4128DMFX-4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R5FD	Samples
LM4128DQ1MF1.8/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AD	Samples
LM4128DQ1MF2.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BD	Samples
LM4128DQ1MF2.5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CD	Samples
LM4128DQ1MF3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DD	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM4128DQ1MF3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6ED	Samples
LM4128DQ1MF4.1/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FD	Samples
LM4128DQ1MFX1.8/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6AD	Samples
LM4128DQ1MFX2.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6BD	Samples
LM4128DQ1MFX2.5/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6CD	Samples
LM4128DQ1MFX3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6DD	Samples
LM4128DQ1MFX3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6ED	Samples
LM4128DQ1MFX4.1/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	R6FD	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSELETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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OTHER QUALIFIED VERSIONS OF LM4128, LM4128-Q1 :

- Catalog: [LM4128](#)
- Automotive: [LM4128-Q1](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

DBV (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
 - Falls within JEDEC MO-178 Variation AA.

DBV (R-PDSO-G5)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
 - D. Publication IPC-7351 is recommended for alternate designs.
 - E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.

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