

## LMV431x Low-Voltage (1.24-V) Adjustable Precision Shunt Regulators

### 1 Features

- Low-Voltage Operation/Wide Adjust Range (1.24 V/30 V)
- 0.5% Initial Tolerance (LMV431B)
- Temperature Compensated for Industrial Temperature Range (39 PPM/°C for the LMV431A)
- Low Operation Current (55  $\mu$ A)
- Low Output Impedance (0.25  $\Omega$ )
- Fast Turn-On Response
- Low Cost

### 2 Applications

- Shunt Regulator
- Series Regulator
- Current Source or Sink
- Voltage Monitor
- Error Amplifier
- 3-V Off-Line Switching Regulator
- Low Dropout N-Channel Series Regulator

### 3 Description

The LMV431, LMV431A and LMV431B are precision 1.24 V shunt regulators capable of adjustment to 30 V. Negative feedback from the cathode to the adjust pin controls the cathode voltage, much like a non-inverting op amp configuration (Refer to [Symbol and Functional Diagrams](#)). A two-resistor voltage divider terminated at the adjust pin controls the gain of a 1.24 V band-gap reference. Shorting the cathode to the adjust pin (voltage follower) provides a cathode voltage of a 1.24 V.

The LMV431, LMV431A and LMV431B have respective initial tolerances of 1.5%, 1%, and 0.5%, and functionally lend themselves to several applications that require zener diode type performance at low voltages. Applications include a 3 V to 2.7 V low drop-out regulator, an error amplifier in a 3 V off-line switching regulator and even as a voltage detector. These parts are typically stable with capacitive loads greater than 10 nF and less than 50 pF.

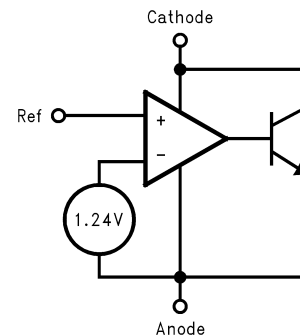
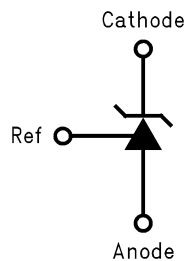
The LMV431, LMV431A and LMV431B provide performance at a competitive price.

#### Device Information<sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)
LMV431	SOT-23 (5)	2.90 mm x 1.60 mm
LMV431	TO-92 (3)	4.30 mm x 4.30 mm
LMV431	SOT-23 (3)	2.92 mm x 1.30 mm

(1) For all available packages, see the orderable addendum at the end of the datasheet.

### 4 Symbol and Functional Diagrams



## Table of Contents

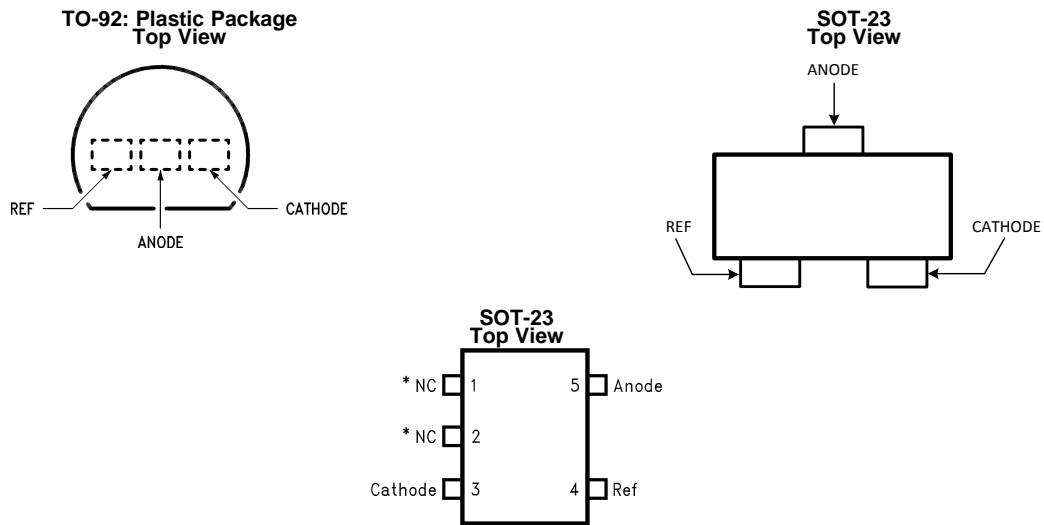
<b>1 Features</b> ..... 1 <b>2 Applications</b> ..... 1 <b>3 Description</b> ..... 1 <b>4 Symbol and Functional Diagrams</b> ..... 1 <b>5 Revision History</b> ..... 2 <b>6 Pin Configurations and Functions</b> ..... 3 <b>7 Specifications</b> ..... 4 7.1 Absolute Maximum Ratings ..... 4 7.2 Handling Ratings ..... 4 7.3 Recommended Operating Conditions ..... 4 7.4 Thermal Information ..... 4 7.5 LMV431C Electrical Characteristics ..... 5 7.6 LMV431I Electrical Characteristics ..... 6 7.7 LMV431AC Electrical Characteristics ..... 7 7.8 LMV431AI Electrical Characteristics ..... 8	7.9 LMV431BC Electrical Characteristics ..... 9 7.10 LMV431BI Electrical Characteristics ..... 10 7.11 Typical Performance Characteristics ..... 11 <b>8 Detailed Description</b> ..... 15 8.1 Functional Block Diagram ..... 15 <b>9 Application and Implementation</b> ..... 16 9.1 Typical Application ..... 16 9.2 DC/AC Test Circuit ..... 18 <b>10 Device and Documentation Support</b> ..... 18 10.1 Documentation Support ..... 18 10.2 Trademarks ..... 18 10.3 Electrostatic Discharge Caution ..... 18 10.4 Glossary ..... 19 <b>11 Mechanical, Packaging, and Orderable Information</b> ..... 19
---	---

## 5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision F (May 2005) to Revision G	Page
<ul style="list-style-type: none"> <li>• Changed formatting to match new TI datasheet guidelines; added Device Information and Handling Ratings tables, Layout, and Device and Documentation Support sections; reformatted Detailed Description and Application and Implementation sections. .... 1</li> <li>• Added spec ..... 4</li> </ul>	

## 6 Pin Configurations and Functions



\*Pin 1 is not internally connected.

\*Pin 2 is internally connected to Anode pin. Pin 2 should be either floating or connected to Anode pin.

## 7 Specifications

### 7.1 Absolute Maximum Ratings

 over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

		MIN	MAX	UNIT
Operating temperature	Industrial (LMV431AI, LMV431I)	-40	85	°C
	Commercial (LMV431AC, LMV431C, LMV431BC)	0	70	
Lead temperature	TO-92 Package/SOT-23 -5,-3 Package (Soldering, 10 sec.)		265	
Internal power dissipation <sup>(2)</sup>	TO-92		0.78	W
	SOT-23-5, -3 Package		0.28	W
Cathode voltage			35	V
Continuous cathode current		-30	30	mA
Reference input current		-0.05	3	

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) Ratings apply to ambient temperature at 25°C. Above this temperature, derate the TO-92 at 6.2 mW/°C, and the SOT-23-5 at 2.2 mW/°C. See derating curve in [Operating Condition](#) section.

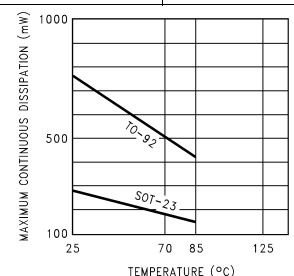
### 7.2 Handling Ratings

			MIN	MAX	UNIT
T <sub>stg</sub>	Storage temperature range		-65	150	°C
V <sub>(ESD)</sub>	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins <sup>(1)</sup>		2000	V

- (1) The human body model is a 100 pF capacitor discharged through a 1.5kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin. MIL-STD-883 3015.7.

### 7.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
Cathode voltage		V <sub>REF</sub>		30	V
Cathode current		0.1		15	mA
Temperature	LMV431AI	-40		85	°C
Derating Curve (Slope = -1/R <sub>θJA</sub> )					

### 7.4 Thermal Information

THERMAL METRIC <sup>(1)</sup>	LMV431	LMV431	LMV431	UNIT
	SOT-23	SOT-23	TO-92	
	3 PINS	5 PINS	3 PINS	
R <sub>θJA</sub> Junction-to-ambient thermal resistance <sup>(2)</sup>	455	455	161	°C/W

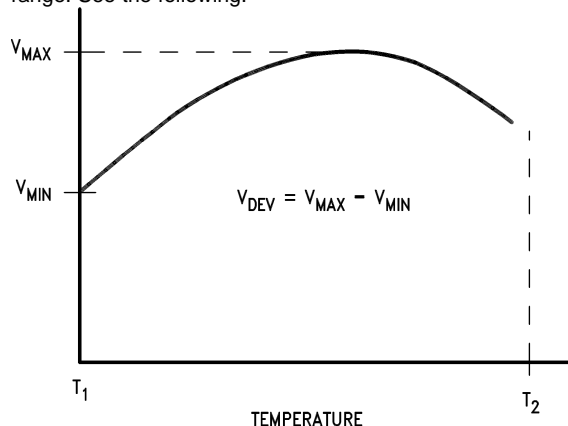
- (1) For more information about traditional and new thermal metrics, see the *IC Package Thermal Metrics* application report, [SPRA953](#).
- (2) T<sub>J Max</sub> = 150°C, T<sub>J</sub> = T<sub>A</sub> + (R<sub>θJA</sub> P<sub>D</sub>), where P<sub>D</sub> is the operating power of the device.

## 7.5 LMV431C Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$V_{REF}$	Reference Voltage	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ (See <a href="#">Figure 32</a> )	$T_A = 25^\circ\text{C}$ 1.222	1.24	1.258	V
			$T_A = \text{Full Range}$ 1.21		1.27	
$V_{DEV}$	Deviation of Reference Input Voltage Over Temperature <sup>(1)</sup>	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ , $T_A = \text{Full Range}$ (See <a href="#">Figure 32</a> )		4	12	mV
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{ mA}$ (see <a href="#">Figure 33</a> ) $V_Z$ from $V_{REF}$ to 6 V $R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ and 2.6 k $\Omega$		-1.5	-2.7	mV/V
$I_{REF}$	Reference Input Current	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ $I_1 = 10\text{ mA}$ (see <a href="#">Figure 33</a> )		0.15	0.5	$\mu\text{A}$
$\alpha I_{REF}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ , $I_1 = 10\text{ mA}$ , $T_A = \text{Full Range}$ (see <a href="#">Figure 33</a> )		0.05	0.3	$\mu\text{A}$
$I_{Z(MIN)}$	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see <a href="#">Figure 32</a> )		55	80	$\mu\text{A}$
$I_{Z(OFF)}$	Off-State Current	$V_Z = 6\text{ V}$ , $V_{REF} = 0\text{ V}$ (see <a href="#">Figure 34</a> )		0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance <sup>(2)</sup>	$V_Z = V_{REF}$ , $I_Z = 0.1\text{ mA}$ to 15 mA Frequency = 0 Hz (see <a href="#">Figure 32</a> )		0.25	0.4	$\Omega$

- (1) Deviation of reference input voltage,  $V_{DEV}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See the following:



The average temperature coefficient of the reference input voltage,  $\alpha V_{REF}$ , is defined as:

$$\alpha V_{REF} \frac{\text{ppm}}{^\circ\text{C}} = \frac{\pm \left( \frac{V_{MAX} - V_{MIN}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1} = \frac{\pm \left( \frac{V_{DEV}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1}$$

Where:  $T_2 - T_1 =$  full temperature change.  $\alpha V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{DEV} = 6\text{ mV}$ ,  $V_{REF} = 1240\text{ mV}$ ,  $T_2 - T_1 = 125^\circ\text{C}$ .

$$\alpha V_{REF} = \frac{\left( \frac{6.0\text{ mV}}{1240\text{ mV}} \right) 10^6}{125^\circ\text{C}} = +39\text{ ppm}/^\circ\text{C}$$

- (2) The dynamic output impedance,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors,  $R_1$  and  $R_2$ , (see [Figure 33](#)), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

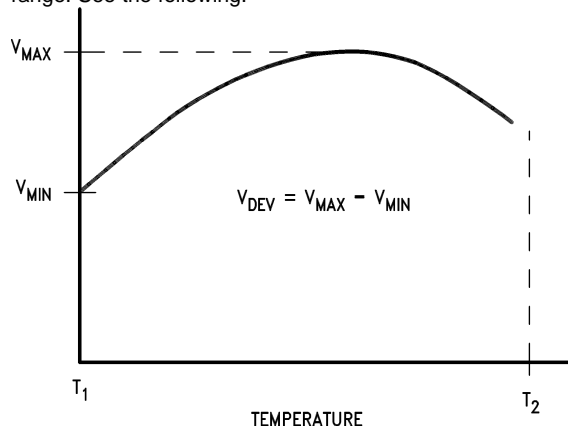
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R_1}{R_2} \right) \right]$$

## 7.6 LMV431I Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
$V_{REF}$	Reference Voltage	$V_Z = V_{REF}, I_Z = 10\text{ mA}$ (See <a href="#">Figure 32</a> )	$T_A = 25^\circ\text{C}$	1.222	1.24	1.258	V
			$T_A = \text{Full Range}$	1.202		1.278	
$V_{DEV}$	Deviation of Reference Input Voltage Over Temperature <sup>(1)</sup>	$V_Z = V_{REF}, I_Z = 10\text{ mA},$ $T_A = \text{Full Range}$ (See <a href="#">Figure 32</a> )		6	20	mV	
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{ mA}$ (see <a href="#">Figure 33</a> ) $V_Z$ from $V_{REF}$ to 6V $R_1 = 10\text{ k}\Omega, R_2 = \infty$ and 2.6k $\Omega$		-1.5	-2.7	mV/V	
$I_{REF}$	Reference Input Current	$R_1 = 10\text{ k}\Omega, R_2 = \infty$ $I_1 = 10\text{ mA}$ (see <a href="#">Figure 33</a> )		0.15	0.5	$\mu\text{A}$	
$\alpha I_{REF}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{ k}\Omega, R_2 = \infty,$ $I_1 = 10\text{ mA}, T_A = \text{Full Range}$ (see <a href="#">Figure 33</a> )		0.1	0.4	$\mu\text{A}$	
$I_{Z(MIN)}$	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see <a href="#">Figure 32</a> )		55	80	$\mu\text{A}$	
$I_{Z(OFF)}$	Off-State Current	$V_Z = 6\text{ V}, V_{REF} = 0\text{ V}$ (see <a href="#">Figure 34</a> )		0.001	0.1	$\mu\text{A}$	
$r_Z$	Dynamic Output Impedance <sup>(2)</sup>	$V_Z = V_{REF}, I_Z = 0.1\text{ mA to }15\text{ mA}$ Frequency = 0 Hz (see <a href="#">Figure 32</a> )		0.25	0.4	$\Omega$	

- (1) Deviation of reference input voltage,  $V_{DEV}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See the following:



The average temperature coefficient of the reference input voltage,  $\alpha V_{REF}$ , is defined as:

$$\alpha V_{REF} \frac{\text{ppm}}{^\circ\text{C}} = \frac{\pm \left( \frac{V_{MAX} - V_{MIN}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1} = \frac{\pm \left( \frac{V_{DEV}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1}$$

Where:  $T_2 - T_1 =$  full temperature change.  $\alpha V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{DEV} = 6\text{ mV}, V_{REF} = 1240\text{ mV}, T_2 - T_1 = 125^\circ\text{C}.$

$$\alpha V_{REF} = \frac{\left( \frac{6.0\text{ mV}}{1240\text{ mV}} \right) 10^6}{125^\circ\text{C}} = +39\text{ ppm} / ^\circ\text{C}$$

- (2) The dynamic output impedance,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors,  $R_1$  and  $R_2$ , (see [Figure 33](#)), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

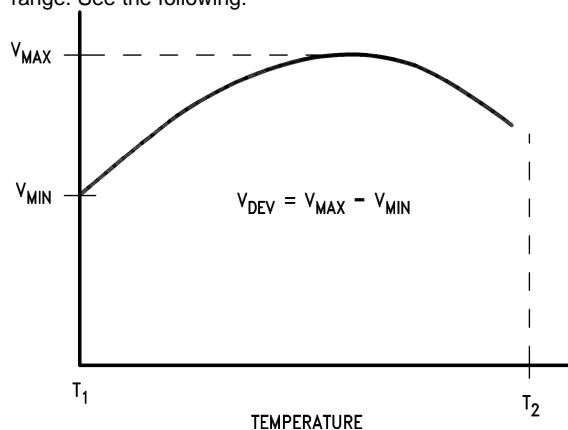
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R_1}{R_2} \right) \right]$$

## 7.7 LMV431AC Electrical Characteristics

 $T_A = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$V_{REF}$	Reference Voltage	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ (See <a href="#">Figure 32</a> )	$T_A = 25^\circ\text{C}$ 1.228	1.24	1.252	V
			$T_A = \text{Full Range}$ 1.221		1.259	
$V_{DEV}$	Deviation of Reference Input Voltage Over Temperature <sup>(1)</sup>	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ , $T_A = \text{Full Range}$ (See <a href="#">Figure 32</a> )		4	12	mV
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{ mA}$ (see <a href="#">Figure 33</a> ) $V_Z$ from $V_{REF}$ to 6 V $R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ and 2.6 k $\Omega$		-1.5	-2.7	mV/V
$I_{REF}$	Reference Input Current	$R_1 = 1\text{ k}\Omega$ , $R_2 = \infty$ $I_I = 10\text{ mA}$ (see <a href="#">Figure 33</a> )		0.15	0.50	$\mu\text{A}$
$\alpha I_{REF}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ , $I_I = 10\text{ mA}$ , $T_A = \text{Full Range}$ (see <a href="#">Figure 33</a> )		0.05	0.3	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see <a href="#">Figure 32</a> )		55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{ V}$ , $V_{REF} = 0\text{ V}$ (see <a href="#">Figure 34</a> )		0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance <sup>(2)</sup>	$V_Z = V_{REF}$ , $I_Z = 0.1\text{ mA}$ to 15mA Frequency = 0 Hz (see <a href="#">Figure 32</a> )		0.25	0.4	$\Omega$

- (1) Deviation of reference input voltage,  $V_{DEV}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See the following:



The average temperature coefficient of the reference input voltage,  $\alpha V_{REF}$ , is defined as:

$$\alpha V_{REF} \frac{\text{ppm}}{^\circ\text{C}} = \frac{\pm \left( \frac{V_{\text{Max}} - V_{\text{Min}}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1} = \frac{\pm \left( \frac{V_{DEV}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1}$$

Where:  $T_2 - T_1$  = full temperature change.  $\alpha V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{DEV} = 6\text{ mV}$ ,  $V_{REF} = 1240\text{ mV}$ ,  $T_2 - T_1 = 125^\circ\text{C}$ .

$$\alpha V_{REF} = \frac{\left( \frac{6.0\text{ mV}}{1240\text{ mV}} \right) 10^6}{125^\circ\text{C}} = +39\text{ ppm} / ^\circ\text{C}$$

- (2) The dynamic output impedance,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors,  $R_1$  and  $R_2$ , (see [Figure 33](#)), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

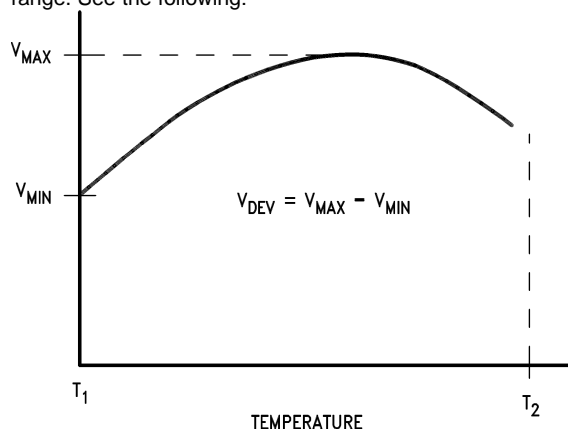
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R_1}{R_2} \right) \right]$$

## 7.8 LMV431AI Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT
$V_{REF}$	Reference Voltage	$V_Z = V_{REF}$ , $I_Z = 10\text{mA}$ (See <a href="#">Figure 32</a> )	$T_A = 25^\circ\text{C}$	1.228	1.24	1.252	V
			$T_A = \text{Full Range}$	1.215		1.265	V
$V_{DEV}$	Deviation of Reference Input Voltage Over Temperature <sup>(1)</sup>	$V_Z = V_{REF}$ , $I_Z = 10\text{mA}$ , $T_A = \text{Full Range}$ (See <a href="#">Figure 32</a> )			6	20	mV
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{mA}$ (see <a href="#">Figure 33</a> ) $V_Z$ from $V_{REF}$ to 6 V $R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ and 2.6 k $\Omega$			-1.5	-2.7	mV/V
$I_{REF}$	Reference Input Current	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ $I_1 = 10\text{ mA}$ (see <a href="#">Figure 33</a> )			0.15	0.5	$\mu\text{A}$
$\alpha I_{REF}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ , $I_1 = 10\text{ mA}$ , $T_A = \text{Full Range}$ (see <a href="#">Figure 33</a> )			0.1	0.4	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see <a href="#">Figure 32</a> )			55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{ V}$ , $V_{REF} = 0\text{ V}$ (see <a href="#">Figure 34</a> )			0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance <sup>(2)</sup>	$V_Z = V_{REF}$ , $I_Z = 0.1\text{ mA}$ to 15 mA Frequency = 0 Hz (see <a href="#">Figure 32</a> )			0.25	0.4	$\Omega$

- (1) Deviation of reference input voltage,  $V_{DEV}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See the following:



The average temperature coefficient of the reference input voltage,  $\alpha V_{REF}$ , is defined as:

$$\alpha V_{REF} \frac{\text{ppm}}{^\circ\text{C}} = \frac{\pm \left( \frac{V_{MAX} - V_{MIN}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1} = \frac{\pm \left( \frac{V_{DEV}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1}$$

Where:  $T_2 - T_1 =$  full temperature change.  $\alpha V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{DEV} = 6\text{ mV}$ ,  $V_{REF} = 1240\text{ mV}$ ,  $T_2 - T_1 = 125^\circ\text{C}$ .

$$\alpha V_{REF} = \frac{\left( \frac{6.0\text{ mV}}{1240\text{ mV}} \right) 10^6}{125^\circ\text{C}} = +39\text{ ppm} / ^\circ\text{C}$$

- (2) The dynamic output impedance,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors,  $R_1$  and  $R_2$ , (see [Figure 33](#)), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R_1}{R_2} \right) \right]$$

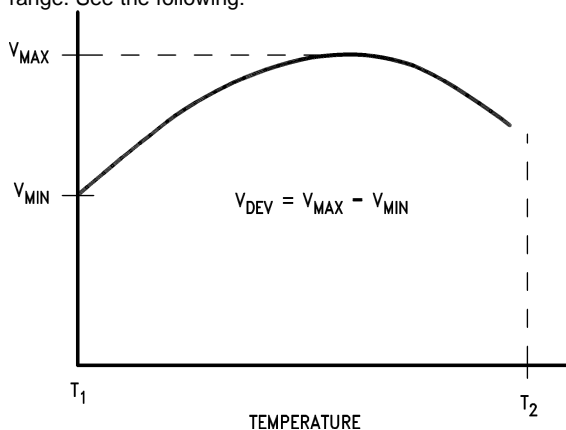


## 7.9 LMV431BC Electrical Characteristics

 $T_A = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$V_{REF}$	Reference Voltage	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ (See <a href="#">Figure 32</a> )	$T_A = 25^\circ\text{C}$ 1.234	1.24	1.246	V
			$T_A = \text{Full Range}$ 1.227		1.253	V
$V_{DEV}$	Deviation of Reference Input Voltage Over Temperature <sup>(1)</sup>	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ , $T_A = \text{Full Range}$ (See <a href="#">Figure 32</a> )		4	12	mV
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{ mA}$ (see <a href="#">Figure 33</a> ) $V_Z$ from $V_{REF}$ to 6 V $R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ and 2.6 k $\Omega$		-1.5	-2.7	mV/V
$I_{REF}$	Reference Input Current	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ $I_1 = 10\text{ mA}$ (see <a href="#">Figure 33</a> )		0.15	0.50	$\mu\text{A}$
$\alpha I_{REF}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ , $I_1 = 10\text{ mA}$ , $T_A = \text{Full Range}$ (see <a href="#">Figure 33</a> )		0.05	0.3	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see <a href="#">Figure 32</a> )		55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{ V}$ , $V_{REF} = 0\text{ V}$ (see <a href="#">Figure 34</a> )		0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance <sup>(2)</sup>	$V_Z = V_{REF}$ , $I_Z = 0.1\text{ mA}$ to 15mA Frequency = 0 Hz (see <a href="#">Figure 32</a> )		0.25	0.4	$\Omega$

- (1) Deviation of reference input voltage,  $V_{DEV}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See the following:



The average temperature coefficient of the reference input voltage,  $\alpha V_{REF}$ , is defined as:

$$\alpha V_{REF} \frac{\text{ppm}}{^\circ\text{C}} = \frac{\pm \left( \frac{V_{MAX} - V_{MIN}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1} = \frac{\pm \left( \frac{V_{DEV}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1}$$

Where:  $T_2 - T_1$  = full temperature change.  $\alpha V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{DEV} = 6\text{ mV}$ ,  $V_{REF} = 1240\text{ mV}$ ,  $T_2 - T_1 = 125^\circ\text{C}$ .

$$\alpha V_{REF} = \frac{\left( \frac{6.0\text{ mV}}{1240\text{ mV}} \right) 10^6}{125^\circ\text{C}} = +39\text{ ppm} / ^\circ\text{C}$$

- (2) The dynamic output impedance,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors,  $R_1$  and  $R_2$ , (see [Figure 33](#)), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

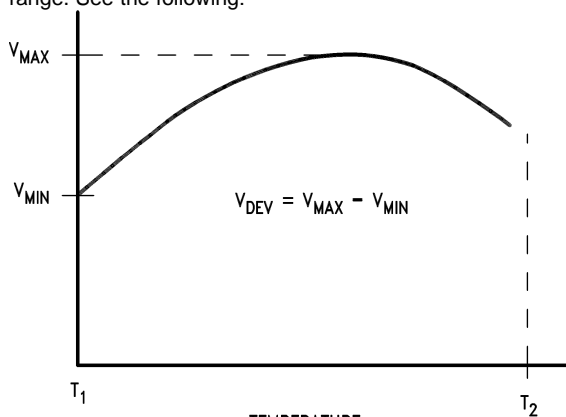
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R_1}{R_2} \right) \right]$$

## 7.10 LMV431BI Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT
$V_{REF}$	Reference Voltage	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ (See <a href="#">Figure 32</a> )	$T_A = 25^\circ\text{C}$	1.234	1.24	1.246	V
			$T_A = \text{Full Range}$	1.224		1.259	V
$V_{DEV}$	Deviation of Reference Input Voltage Over Temperature <sup>(1)</sup>	$V_Z = V_{REF}$ , $I_Z = 10\text{ mA}$ , $T_A = \text{Full Range}$ (See <a href="#">Figure 32</a> )			6	20	mV
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{ mA}$ (see <a href="#">Figure 33</a> ) $V_Z$ from $V_{REF}$ to 6V $R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ and 2.6 k $\Omega$			-1.5	-2.7	mV/V
$I_{REF}$	Reference Input Current	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ $I_1 = 10\text{ mA}$ (see <a href="#">Figure 33</a> )			0.15	0.50	$\mu\text{A}$
$\alpha I_{REF}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{ k}\Omega$ , $R_2 = \infty$ , $I_1 = 10\text{ mA}$ , $T_A = \text{Full Range}$ (see <a href="#">Figure 33</a> )			0.1	0.4	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see <a href="#">Figure 32</a> )			55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{ V}$ , $V_{REF} = 0\text{ V}$ (see <a href="#">Figure 34</a> )			0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance <sup>(2)</sup>	$V_Z = V_{REF}$ , $I_Z = 0.1\text{ mA}$ to 15 mA Frequency = 0 Hz (see <a href="#">Figure 32</a> )			0.25	0.4	$\Omega$

- (1) Deviation of reference input voltage,  $V_{DEV}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See the following:



The average temperature coefficient of the reference input voltage,  $\alpha V_{REF}$ , is defined as:

$$\alpha V_{REF} \frac{\text{ppm}}{^\circ\text{C}} = \frac{\pm \left( \frac{V_{MAX} - V_{MIN}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1} = \frac{\pm \left( \frac{V_{DEV}}{V_{REF}(\text{at } 25^\circ\text{C})} \right) 10^6}{T_2 - T_1}$$

Where:  $T_2 - T_1 =$  full temperature change.  $\alpha V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{DEV} = 6\text{ mV}$ ,  $V_{REF} = 1240\text{ mV}$ ,  $T_2 - T_1 = 125^\circ\text{C}$ .

$$\alpha V_{REF} = \frac{\left( \frac{6.0\text{ mV}}{1240\text{ mV}} \right) 10^6}{125^\circ\text{C}} = +39\text{ ppm} / ^\circ\text{C}$$

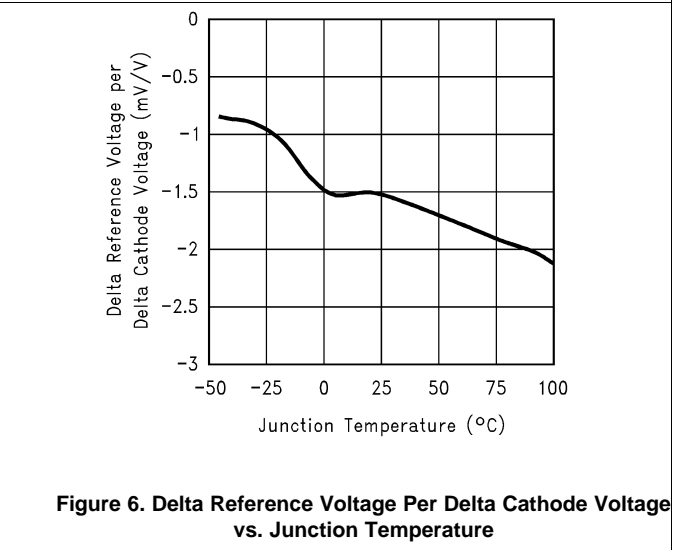
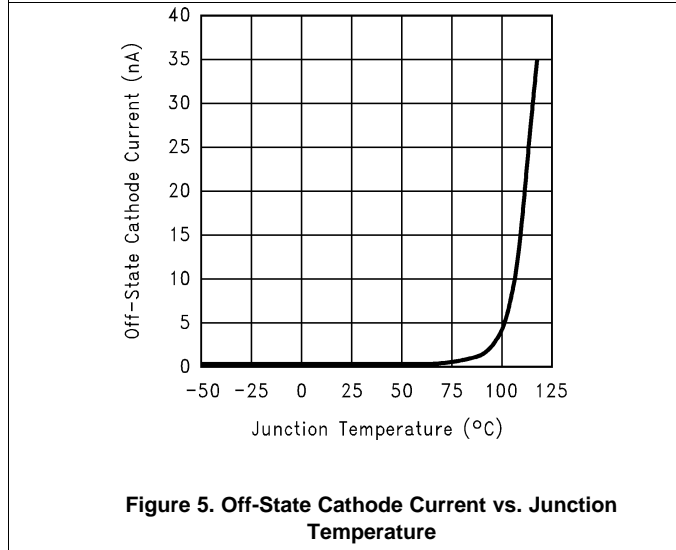
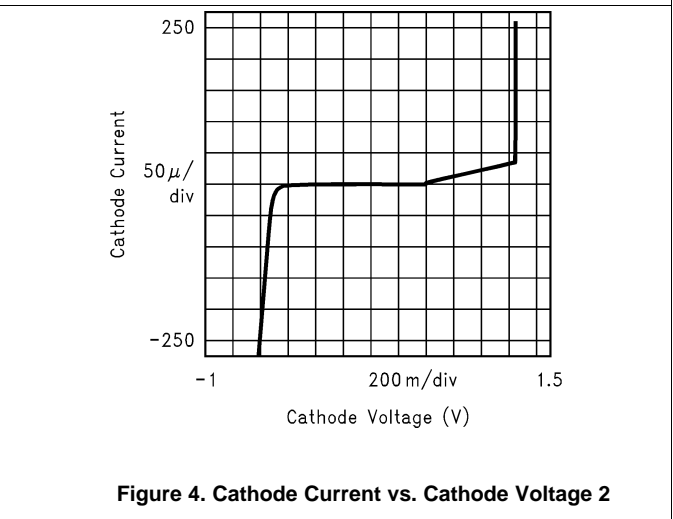
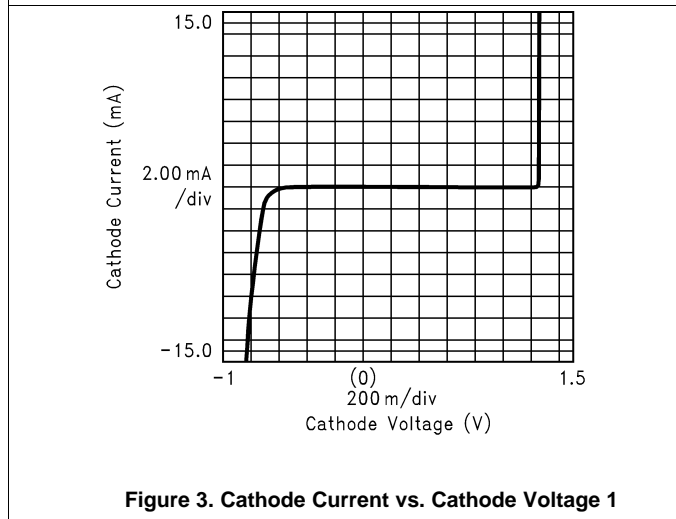
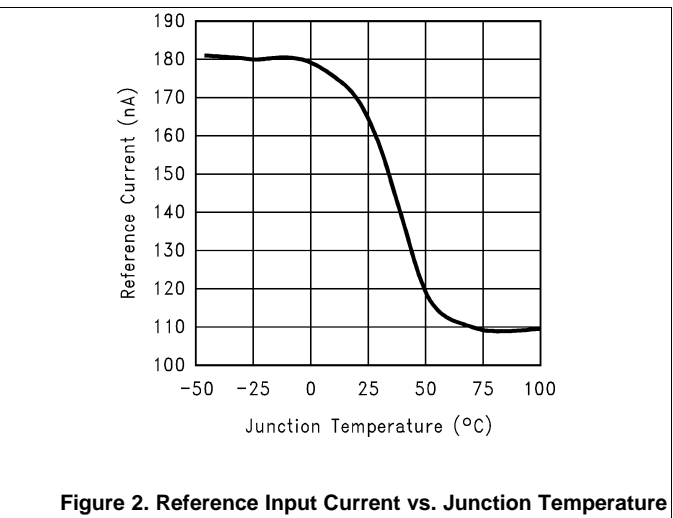
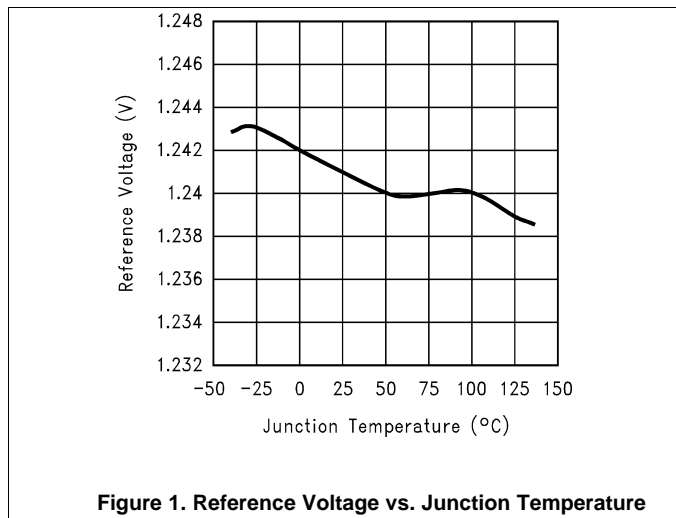
- (2) The dynamic output impedance,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors,  $R_1$  and  $R_2$ , (see [Figure 33](#)), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R_1}{R_2} \right) \right]$$

### 7.11 Typical Performance Characteristics



Typical Performance Characteristics (continued)

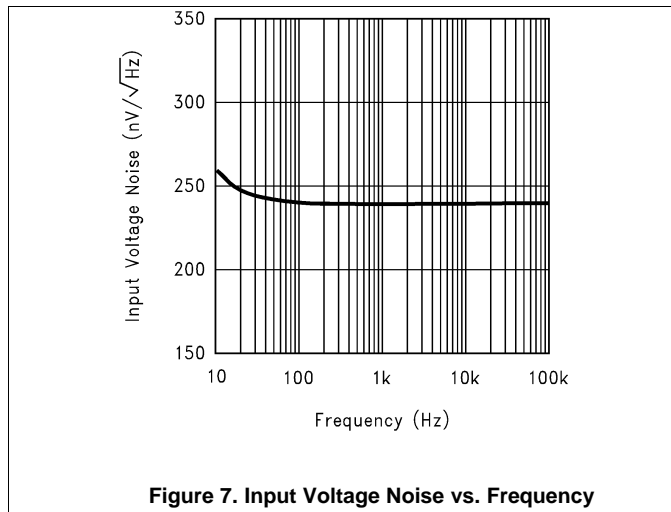


Figure 7. Input Voltage Noise vs. Frequency

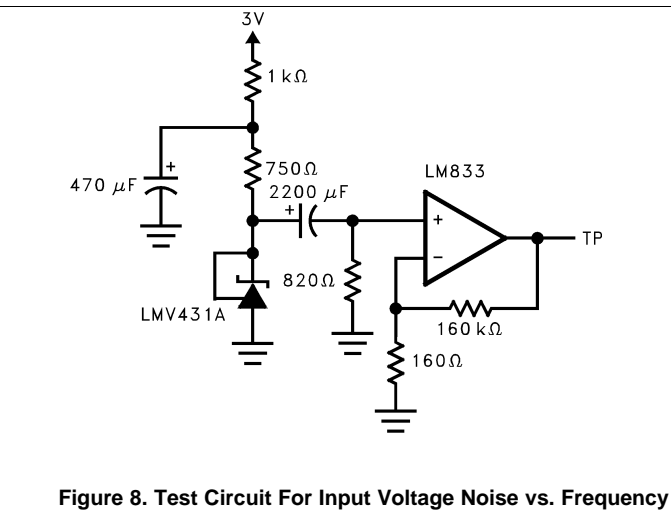


Figure 8. Test Circuit For Input Voltage Noise vs. Frequency

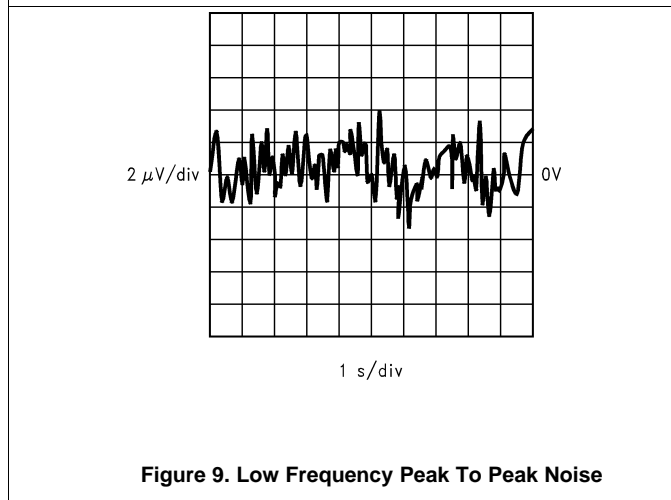


Figure 9. Low Frequency Peak To Peak Noise

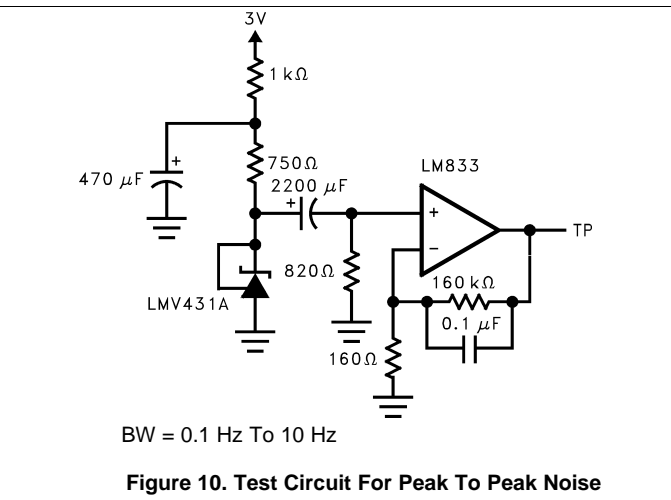


Figure 10. Test Circuit For Peak To Peak Noise

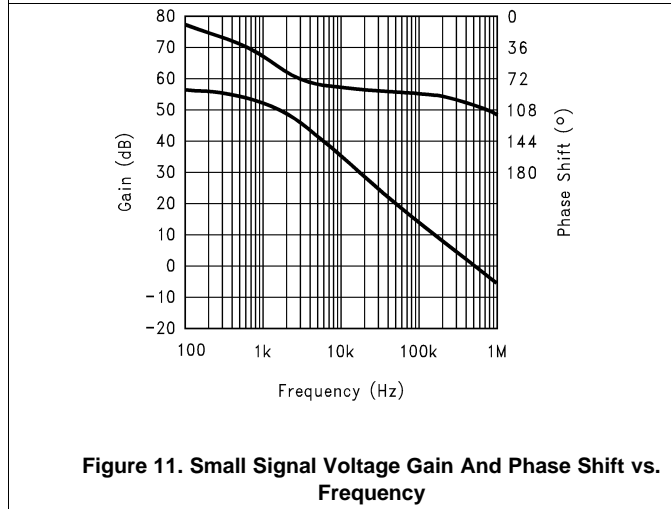


Figure 11. Small Signal Voltage Gain And Phase Shift vs. Frequency

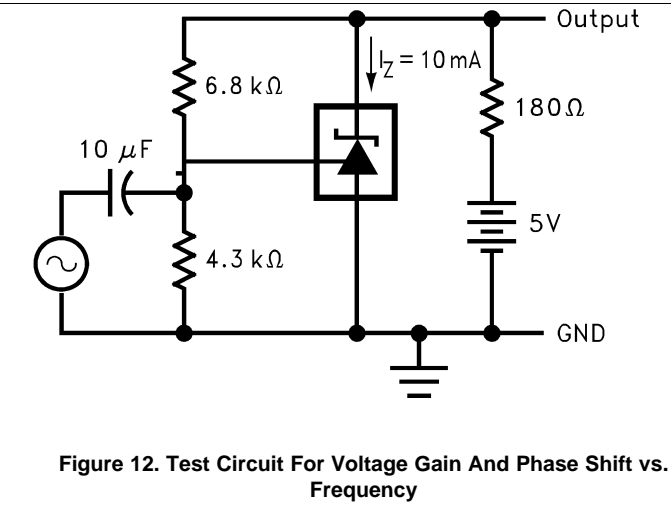


Figure 12. Test Circuit For Voltage Gain And Phase Shift vs. Frequency

Typical Performance Characteristics (continued)

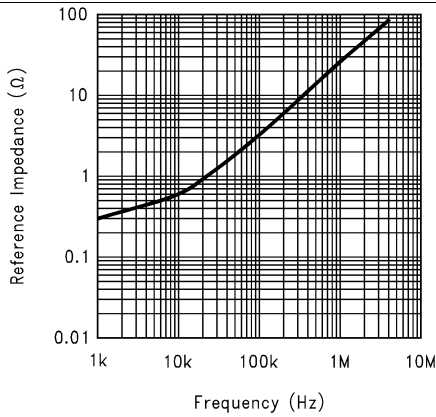


Figure 13. Reference Impedance vs. Frequency

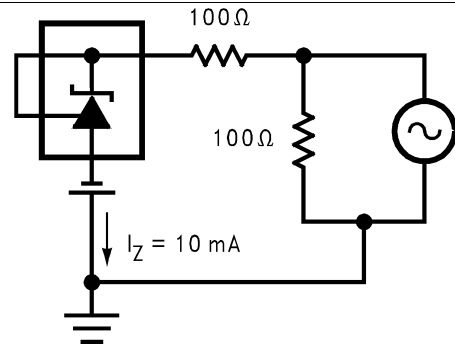


Figure 14. Test Circuit For Reference Impedance vs. Frequency

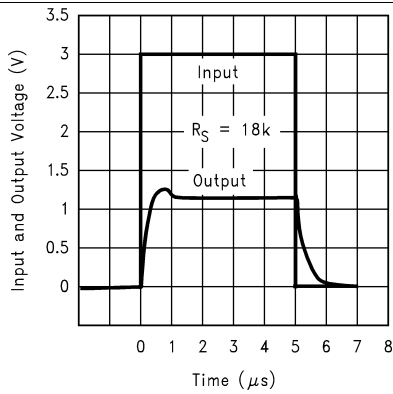


Figure 15. Pulse Response 1

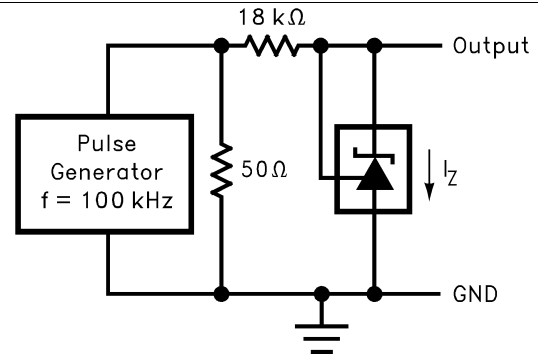


Figure 16. Test Circuit For Pulse Response 1

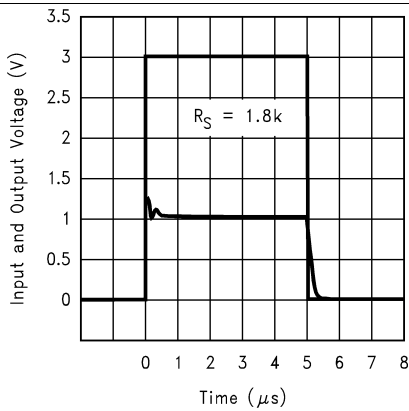


Figure 17. Pulse Response 2

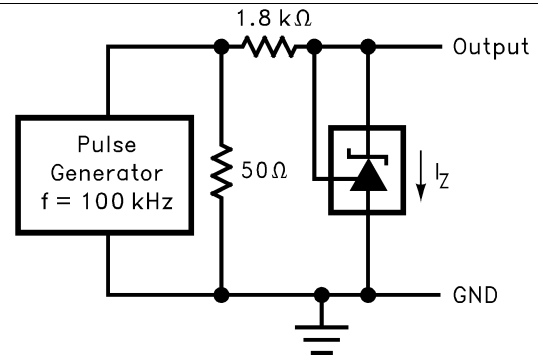


Figure 18. Test Circuit For Pulse Response 2

Typical Performance Characteristics (continued)

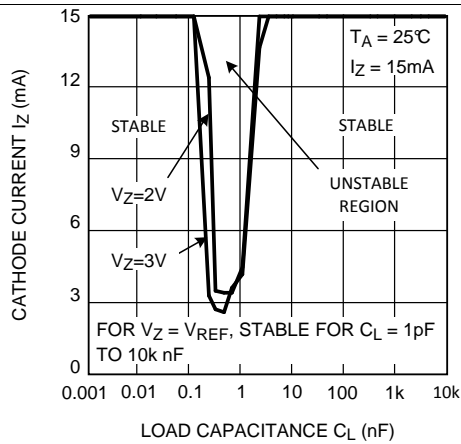


Figure 19. LMV431 Stability Boundary Condition

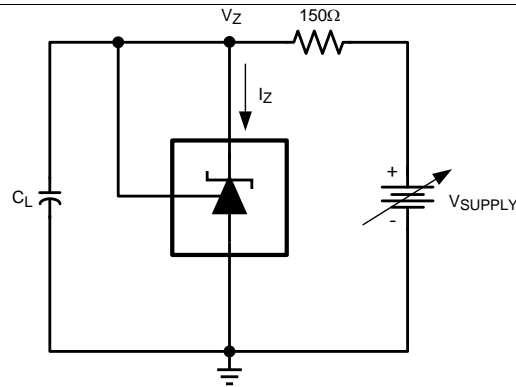


Figure 20. Test Circuit For  $V_Z = V_{REF}$

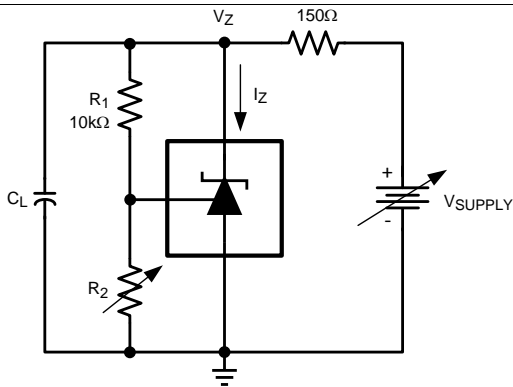
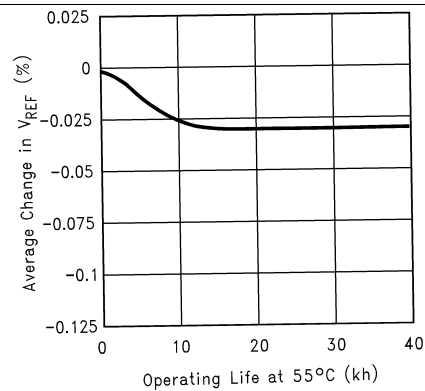


Figure 21. Test Circuit For  $V_Z = 2V, 3V$

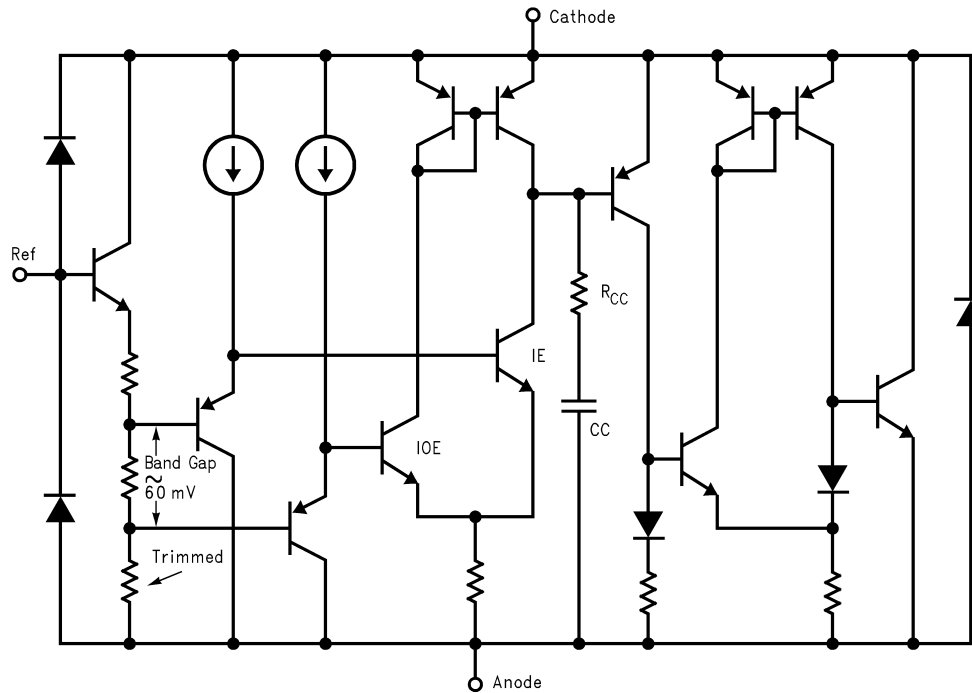


Extrapolated from life-test data taken at  $125^\circ C$ ; the activation energy assumed is 0.7eV.

Figure 22. Percentage Change In  $V_{REF}$  vs. Operating Life At  $55^\circ C$

## 8 Detailed Description

### 8.1 Functional Block Diagram

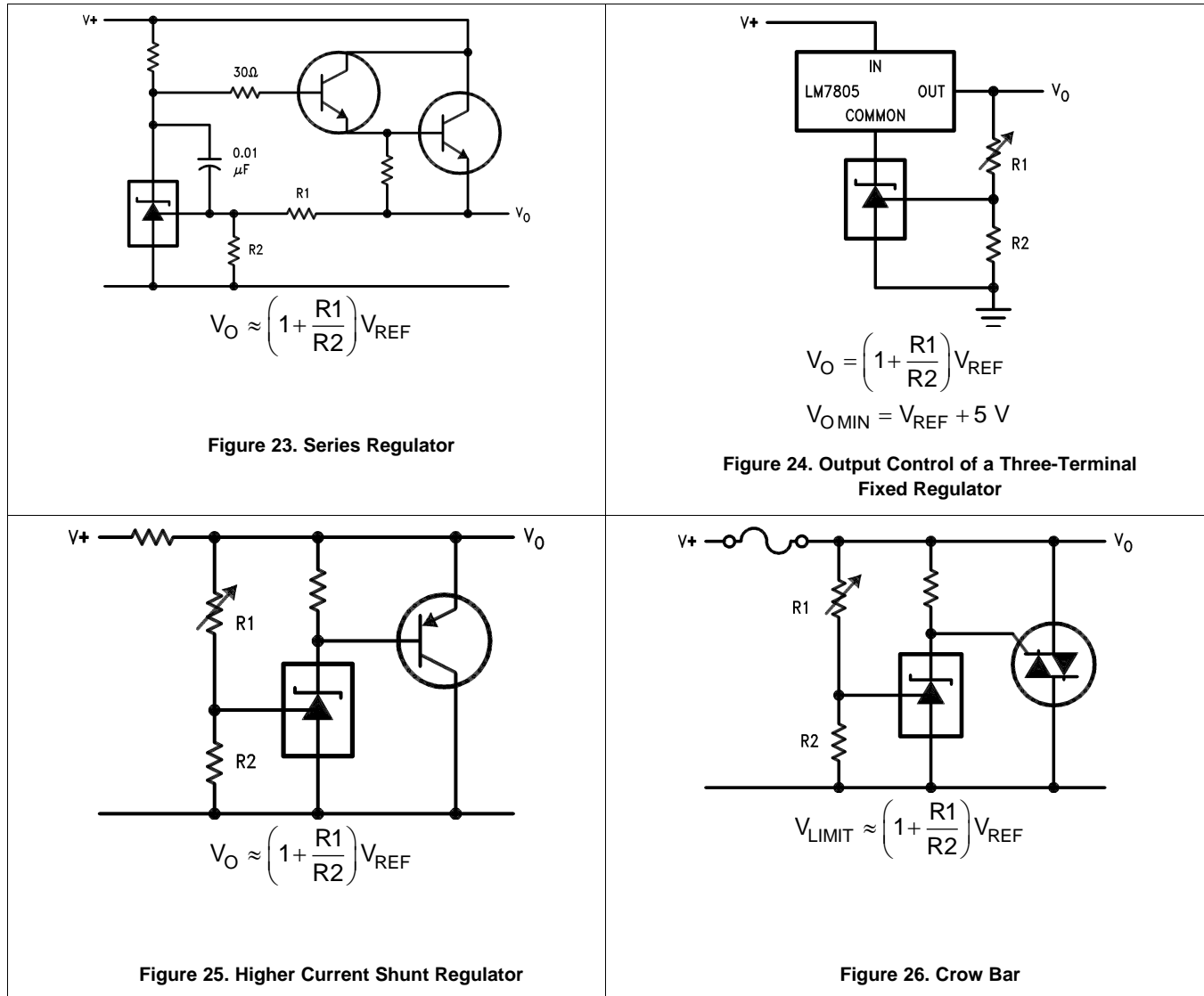


## 9 Application and Implementation

### NOTE

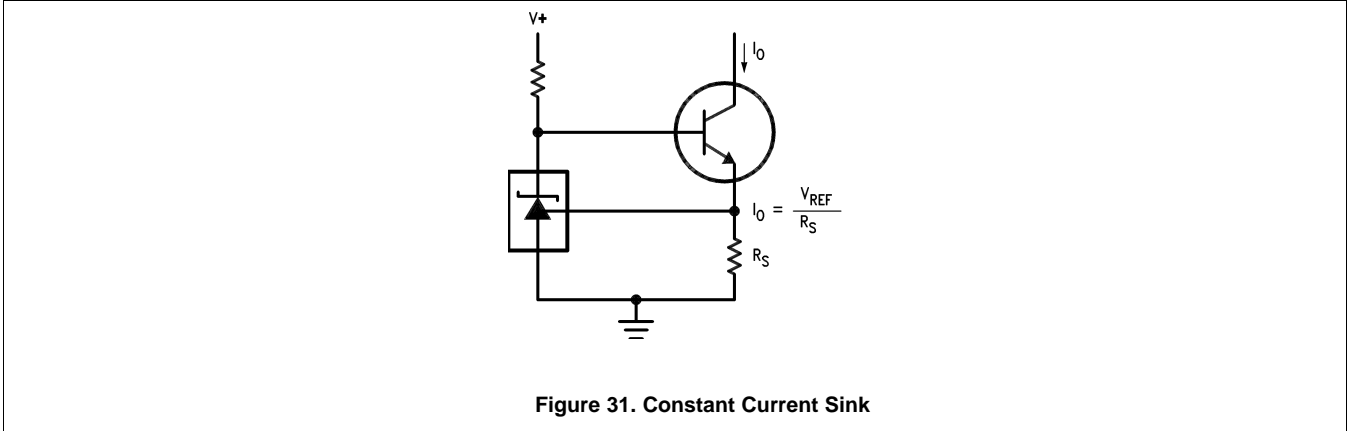
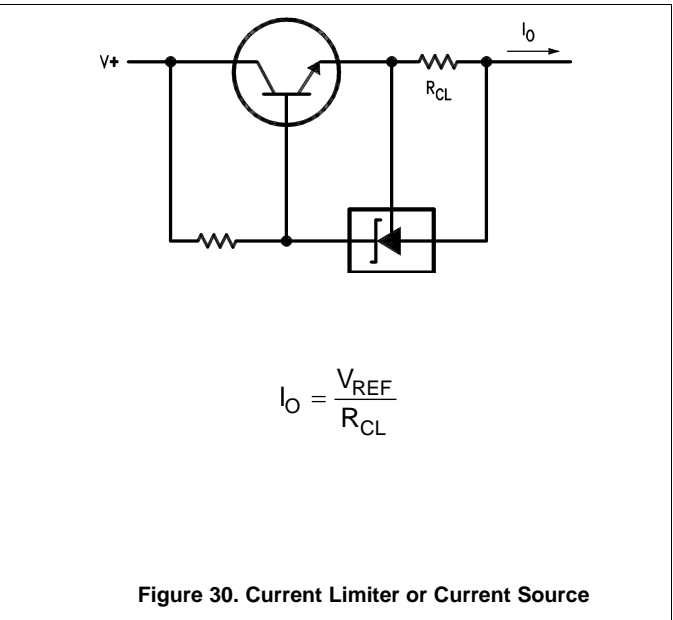
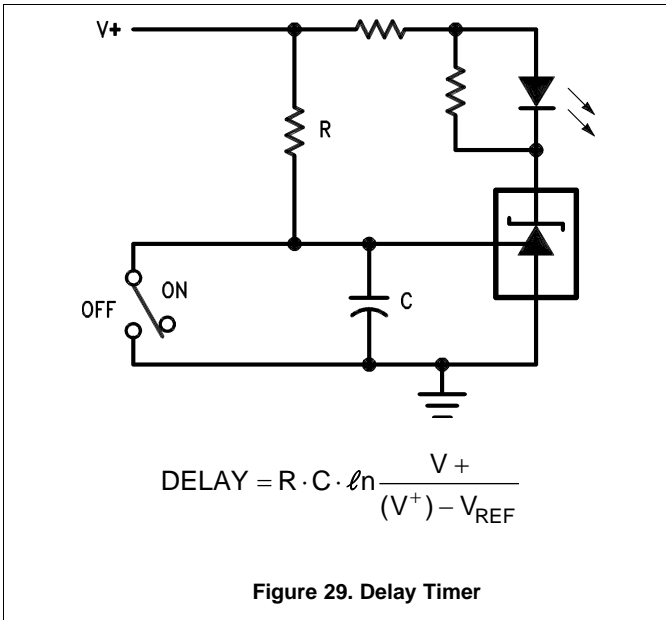
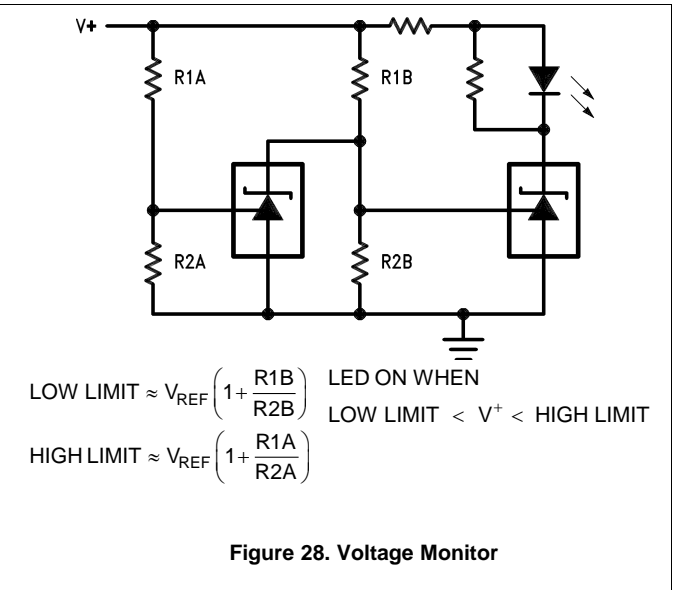
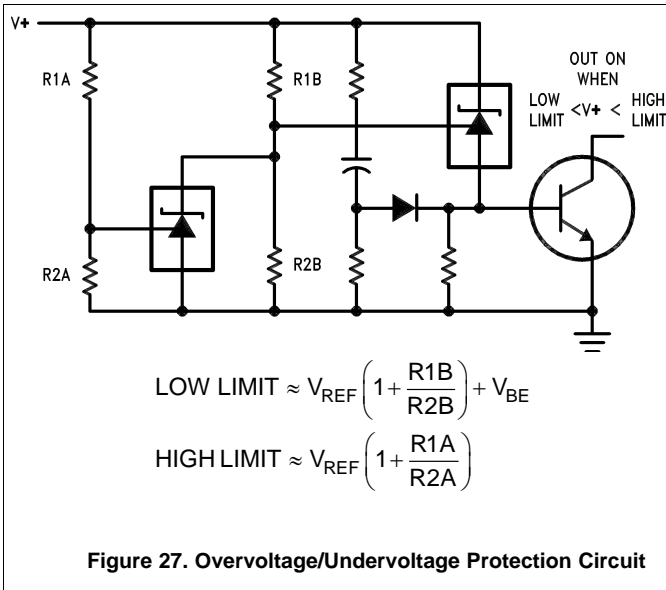
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 9.1 Typical Application





Typical Application (continued)



## 9.2 DC/AC Test Circuit

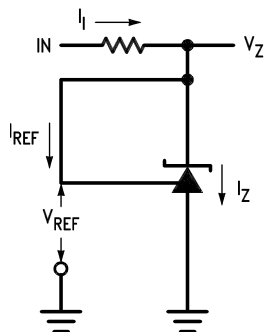
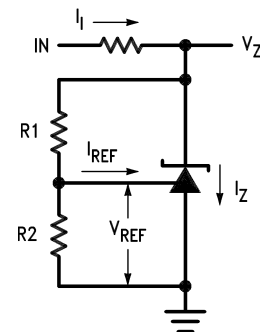
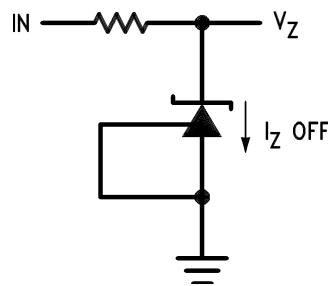

 Figure 32. Test Circuit For  $V_Z = V_{REF}$ 

 Figure 33. Test Circuit For  $V_Z > V_{REF}$ 


Figure 34. Test Circuit For Off-State Current

## 10 Device and Documentation Support

### 10.1 Documentation Support

#### 10.1.1 Related Links

The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

**Table 1. Related Links**

PARTS	PRODUCT FOLDER	SAMPLE & BUY	TECHNICAL DOCUMENTS	TOOLS & SOFTWARE	SUPPORT & COMMUNITY
LMV431	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
LMV431A	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
LMV431B	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>

### 10.2 Trademarks

All trademarks are the property of their respective owners.

### 10.3 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

## 10.4 Glossary

[SLYZ022](#) — *TI Glossary*.





This glossary lists and explains terms, acronyms, and definitions.

## 11 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LMV431ACM5	NRND	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 85	N09A	
LMV431ACM5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	N09A	<a href="#">Samples</a>
LMV431ACM5X/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	N09A	<a href="#">Samples</a>
LMV431AIM5	NRND	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 85	N08A	
LMV431AIM5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	N08A	<a href="#">Samples</a>
LMV431AIM5X	NRND	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 85	N08A	
LMV431AIM5X/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	N08A	<a href="#">Samples</a>
LMV431AIMF	NRND	SOT-23	DBZ	3	1000	TBD	Call TI	Call TI	-40 to 85	RLA	
LMV431AIMF/NOPB	ACTIVE	SOT-23	DBZ	3	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	RLA	<a href="#">Samples</a>
LMV431AIMFX	NRND	SOT-23	DBZ	3	3000	TBD	Call TI	Call TI	-40 to 85	RLA	
LMV431AIMFX/NOPB	ACTIVE	SOT-23	DBZ	3	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	RLA	<a href="#">Samples</a>
LMV431AIZ/LFT3	ACTIVE	TO-92	LP	3	2000	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type		LMV431 AIZ	<a href="#">Samples</a>
LMV431AIZ/NOPB	ACTIVE	TO-92	LP	3	1800	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 85	LMV431 AIZ	<a href="#">Samples</a>
LMV431BCM5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM		N09C	<a href="#">Samples</a>
LMV431BCM5X/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM		N09C	<a href="#">Samples</a>
LMV431BIMF	NRND	SOT-23	DBZ	3	1000	TBD	Call TI	Call TI	-40 to 85	RLB	
LMV431BIMF/NOPB	ACTIVE	SOT-23	DBZ	3	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	RLB	<a href="#">Samples</a>
LMV431BIMFX/NOPB	ACTIVE	SOT-23	DBZ	3	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	RLB	<a href="#">Samples</a>
LMV431CM5	NRND	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	0 to 70	N09B	
LMV431CM5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 70	N09B	<a href="#">Samples</a>

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LMV431CM5X/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 70	N09B	
LMV431CZ/NOPB	ACTIVE	TO-92	LP	3	1800	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	0 to 70	LMV431 CZ	
LMV431IM5	NRND	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 85	N08B	
LMV431IM5/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	N08B	
LMV431IM5X/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 85	N08B	

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**TAPE AND REEL INFORMATION**

**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**


\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LMV431AIMF	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LMV431AIMF/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LMV431AIMFX	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LMV431AIMFX/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LMV431BIMF	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LMV431BIMF/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LMV431BIMFX/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3

**TAPE AND REEL BOX DIMENSIONS**

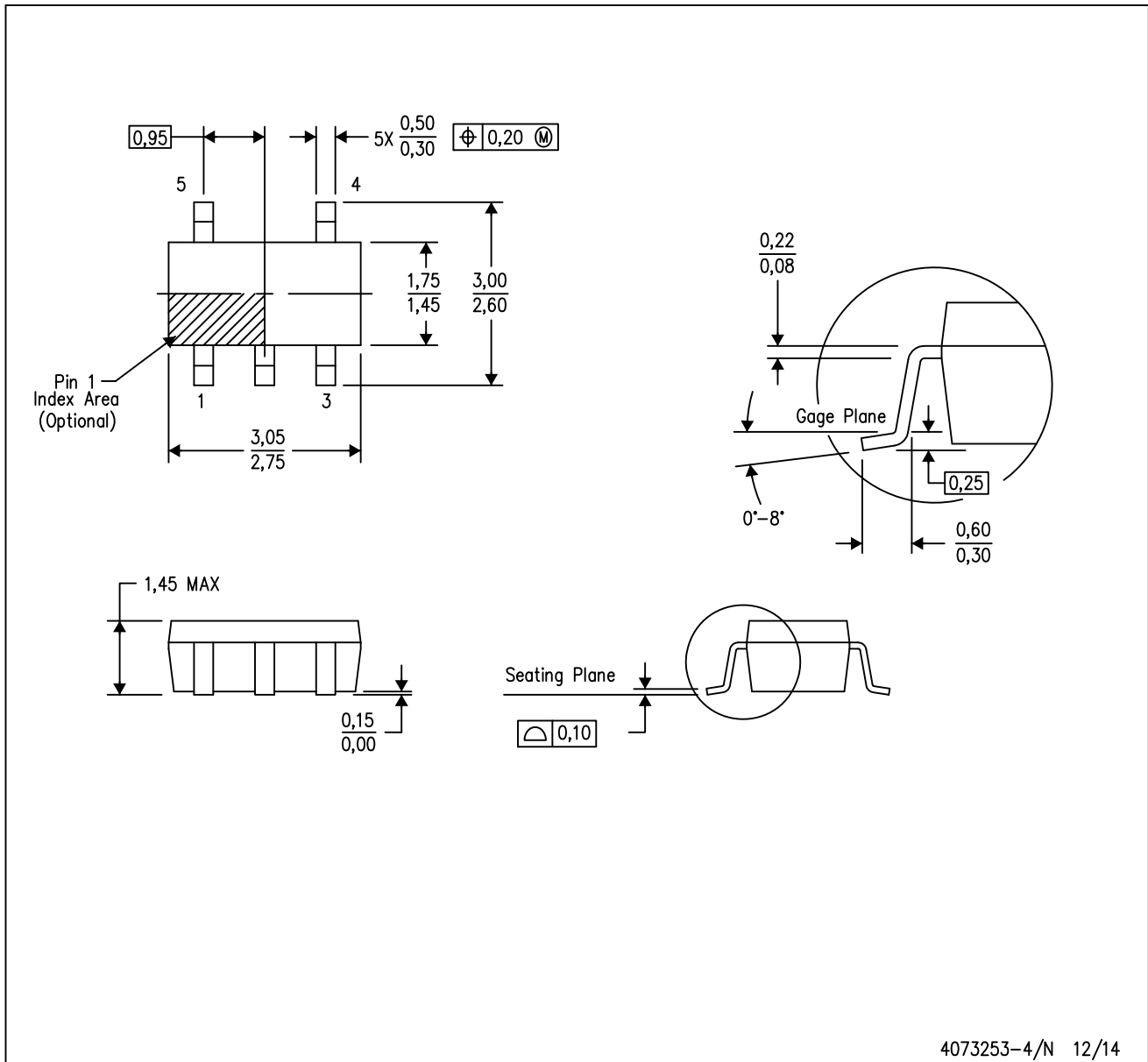

\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LMV431AIMF	SOT-23	DBZ	3	1000	210.0	185.0	35.0
LMV431AIMF/NOPB	SOT-23	DBZ	3	1000	210.0	185.0	35.0
LMV431AIMFX	SOT-23	DBZ	3	3000	210.0	185.0	35.0
LMV431AIMFX/NOPB	SOT-23	DBZ	3	3000	210.0	185.0	35.0
LMV431BIMF	SOT-23	DBZ	3	1000	210.0	185.0	35.0
LMV431BIMF/NOPB	SOT-23	DBZ	3	1000	210.0	185.0	35.0
LMV431BIMFX/NOPB	SOT-23	DBZ	3	3000	210.0	185.0	35.0



DBV (R-PDSO-G5)

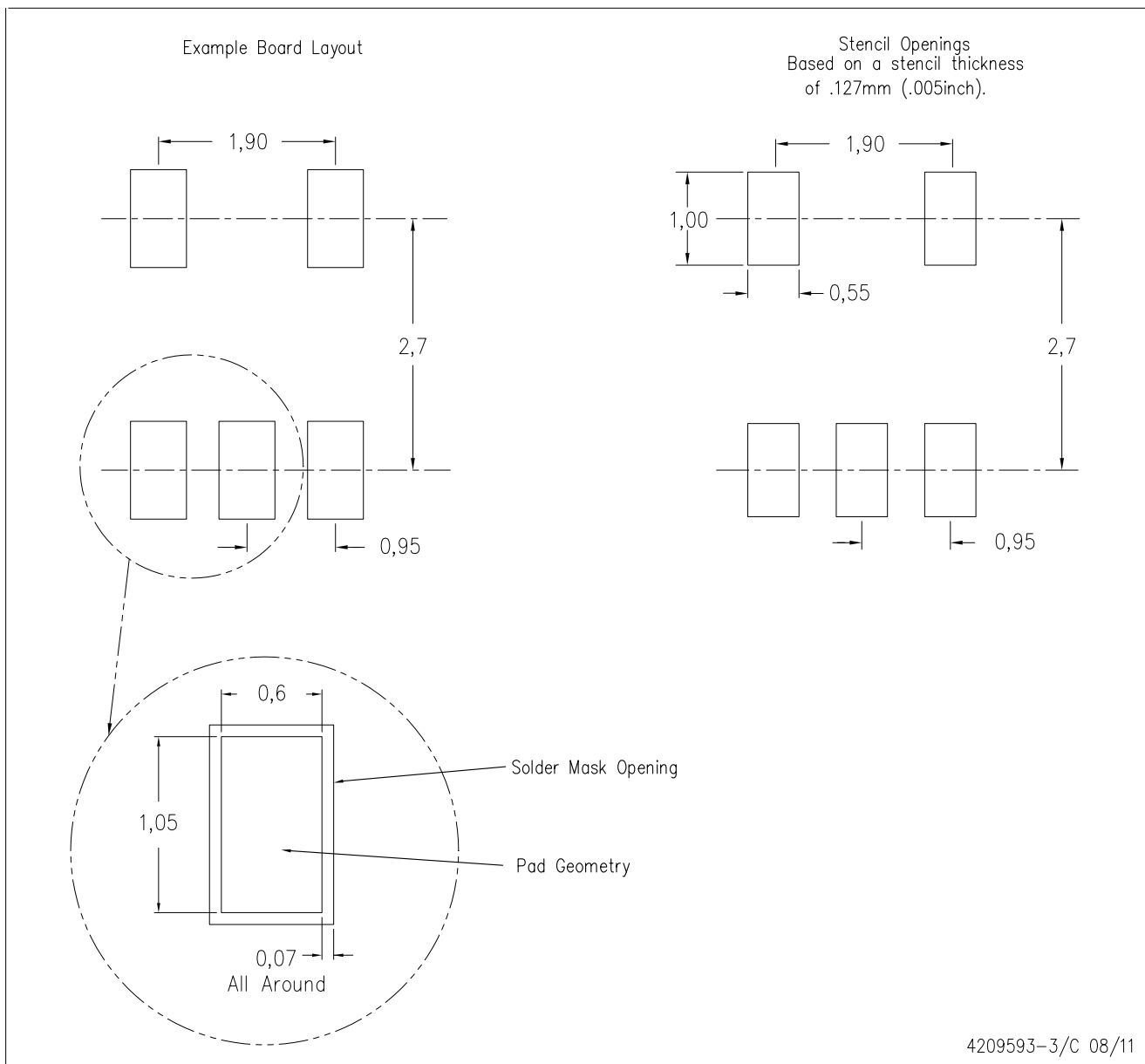
PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
  - Falls within JEDEC MO-178 Variation AA.

DBV (R-PDSO-G5)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
  - D. Publication IPC-7351 is recommended for alternate designs.
  - E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.

DBZ (R-PDSO-G3)

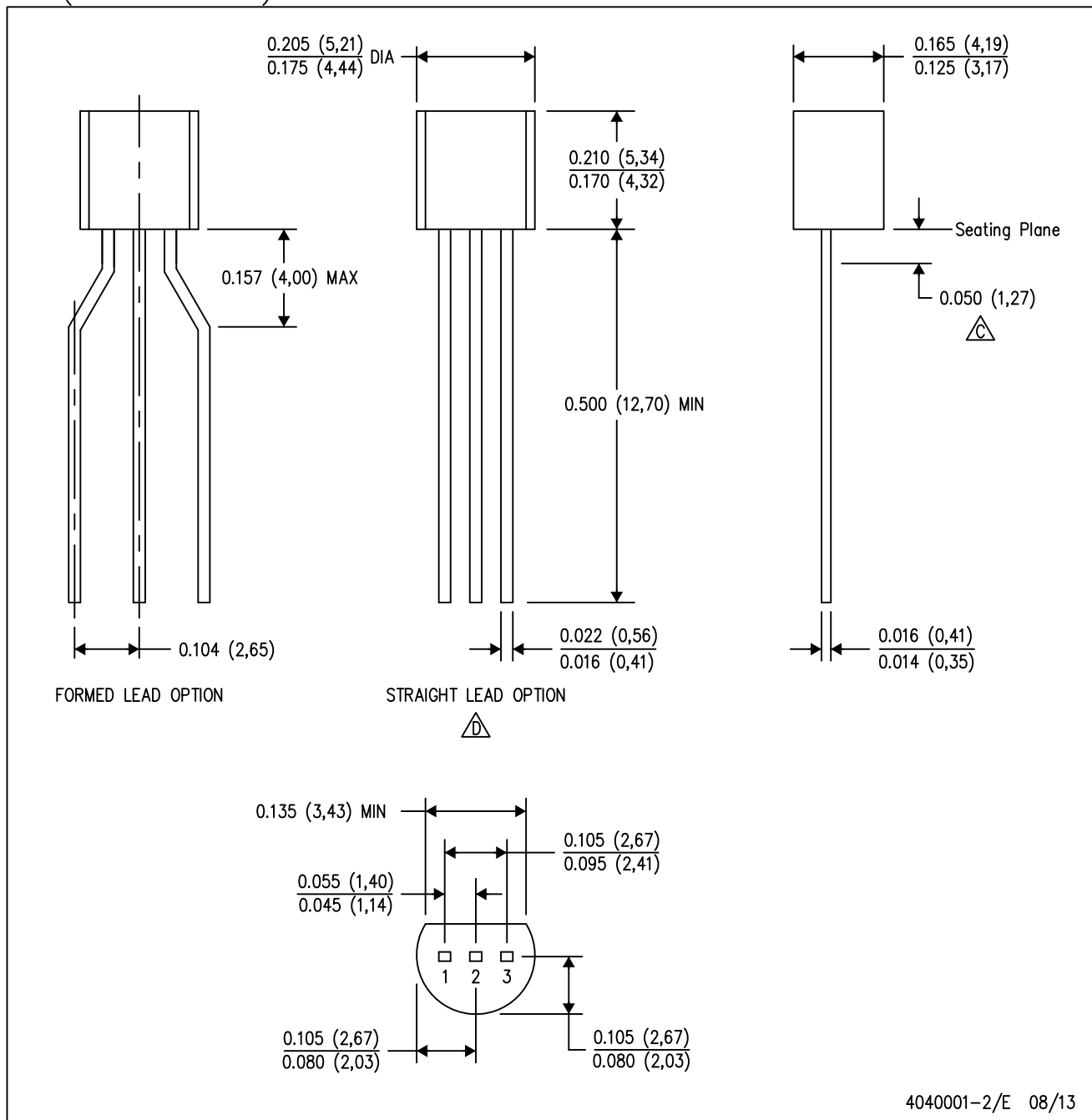
PLASTIC SMALL-OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
  - B. This drawing is subject to change without notice.
  - C. Lead dimensions are inclusive of plating.
  - D. Body dimensions are exclusive of mold flash and protrusion. Mold flash and protrusion not to exceed 0.25 per side.
  - $\triangle$  Falls within JEDEC TO-236 variation AB, except minimum foot length.

LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



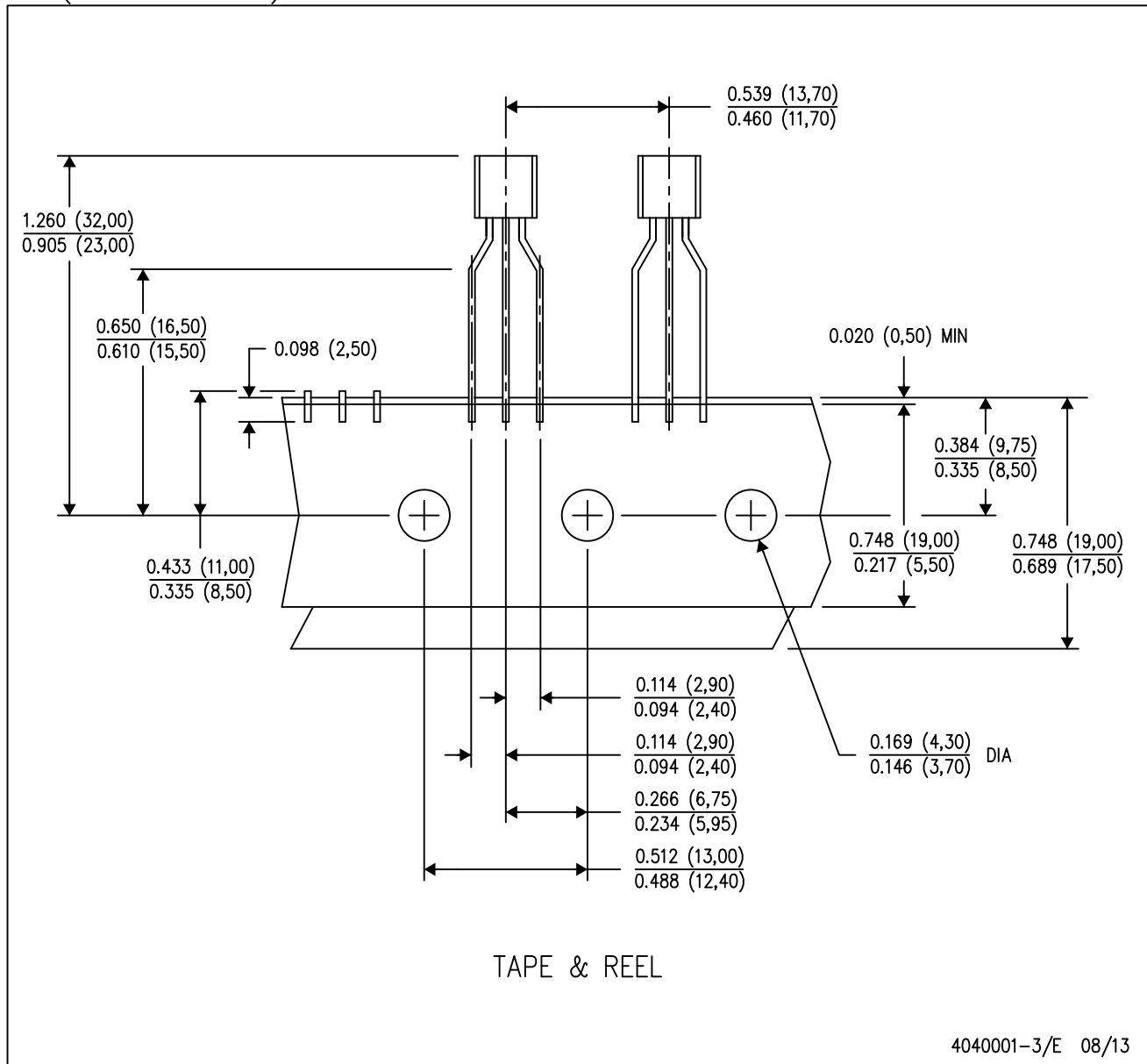
4040001-2/E 08/13

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Lead dimensions are not controlled within this area.
  - Falls within JEDEC TO-226 Variation AA (TO-226 replaces TO-92).
  - E. Shipping Method:  
 Straight lead option available in bulk pack only.  
 Formed lead option available in tape & reel or ammo pack.  
 Specific products can be offered in limited combinations of shipping mediums and lead options.  
 Consult product folder for more information on available options.

# MECHANICAL DATA

LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. Tape and Reel information for the Formed Lead Option package.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

### Products

Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Clocks and Timers	<a href="http://www.ti.com/clocks">www.ti.com/clocks</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
OMAP Applications Processors	<a href="http://www.ti.com/omap">www.ti.com/omap</a>
Wireless Connectivity	<a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a>

### Applications

Automotive and Transportation	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
Communications and Telecom	<a href="http://www.ti.com/communications">www.ti.com/communications</a>
Computers and Peripherals	<a href="http://www.ti.com/computers">www.ti.com/computers</a>
Consumer Electronics	<a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>
Energy and Lighting	<a href="http://www.ti.com/energy">www.ti.com/energy</a>
Industrial	<a href="http://www.ti.com/industrial">www.ti.com/industrial</a>
Medical	<a href="http://www.ti.com/medical">www.ti.com/medical</a>
Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Space, Avionics and Defense	<a href="http://www.ti.com/space-avionics-defense">www.ti.com/space-avionics-defense</a>
Video and Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>

### TI E2E Community

[e2e.ti.com](http://e2e.ti.com)