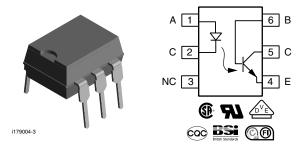




**Vishay Semiconductors** 

## **Optocoupler, Phototransistor Output, with Base Connection**



### DESCRIPTION

The H11Ax family is an industry standard single channel phototransistor coupler. It includes the H11A1, H11A2, H11A3, H11A4, H11A5 couplers.

Each optocoupler consists of gallium arsenide infrared LED and a silicon NPN phototransistor.

The isolation performance is accomplished through Vishay double molding isolation manufacturing process. Compliance to DIN EN 60747-5-5 partial discharge isolation specification is available is by ordering option 1.

These isolation processes and the Vishay ISO9001 quality program results in the highest isolation performance available for a commercial plastic phototransistor optocoupler.

The devices are available in lead formed configuration suitable for surface mounting and are available either on tape and reel, or in standard tube shipping containers.

#### Note

• Designing with data sheet is covered in Application Note 45.

### FEATURES

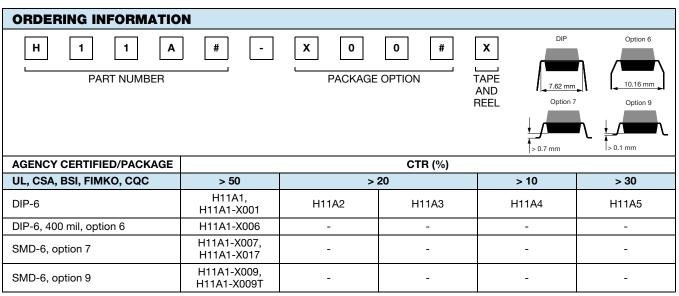
- Interfaces with common logic families
- Input-output coupling capacitance < 0.5 pF</li>
- Industry standard dual-in line 6-pin package
- Isolation test voltage: 5300 V<sub>RMS</sub>
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- AC mains detection
- · Reed relay driving
- Switch mode power supply feedback
- Telephone ring detection
- Logic ground isolation
- · Logic coupling with high frequency noise rejection

#### AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO
- CQC



Note

• Additional options may be possible, please contact sales office.

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT	- <b>·</b>	•	•			
Reverse voltage		V <sub>R</sub>	6	V		
Forward current		١ <sub>F</sub>	60	mA		
Surge current	t ≤ 10 µs	I <sub>FSM</sub>	2.5	A		
Power dissipation		P <sub>diss</sub>	100	mW		
OUTPUT						
Collector emitter breakdown voltage		V <sub>CEO</sub>	70	V		
Emitter base breakdown voltage		V <sub>EBO</sub>	7	V		
Collector current		I <sub>C</sub>	50	mA		
Collector current	t < 1 ms	I <sub>C</sub>	100	mA		
Power dissipation		P <sub>diss</sub>	150	mW		
COUPLER						
Isolation test voltage		V <sub>ISO</sub>	5300	V <sub>RMS</sub>		
Creepage distance			≥ 7	mm		
Clearance distance			≥ 7	mm		
Insulation thickness between emitter and detector			≥ 0.4	mm		
Comparative tracking index	per DIN IEC 112/VDE 0303, part 1		175			
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature range		T <sub>amb</sub>	-55 to +100	°C		
Junction temperature		Тj	100	°C		
Soldering temperature	max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C		

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
		H11A1	V <sub>F</sub>		1.1	1.5	V
		H11A2	V <sub>F</sub>		1.1	1.5	V
Forward voltage	I <sub>F</sub> = 10 mA	H11A3	V <sub>F</sub>		1.1	1.5	V
		H11A4	V <sub>F</sub>		1.1	1.5	V
		H11A5	V <sub>F</sub>		1.1	1.7	V
Reverse current	V <sub>R</sub> = 3 V		I <sub>R</sub>			10	μA
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Co		50		pF
OUTPUT							
Collector emitter breakdown voltage	$I_{C} = 1 \text{ mA}, I_{F} = 0 \text{ mA}$		BV <sub>CEO</sub>	30			V
Emitter collector breakdown voltage	$I_E = 100 \ \mu A, I_F = 0 \ m A$		BV <sub>ECO</sub>	7			V
Collector base breakdown voltage	$I_{C} = 10 \ \mu A, I_{F} = 0 \ mA$		BV <sub>CBO</sub>	70			V
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ mA}$		I <sub>CEO</sub>		5	50	nA
Emitter collector capacitance	$V_{CE} = 0 V$		C <sub>CE</sub>		6		pF
COUPLER							
Collector emitter, saturation voltage	$I_{CE} = 0.5 \text{ mA}, I_F = 10 \text{ mA}$		V <sub>CEsat</sub>			0.4	V
Capacitance (input-output)			CIO		0.5		pF

#### Note

• Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

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CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	V <sub>CE</sub> = 10 V, I <sub>F</sub> = 10 mA	H11A1	CTR <sub>DC</sub>	50			%
		H11A2	CTR <sub>DC</sub>	20			%
		H11A3	CTR <sub>DC</sub>	20			%
		H11A4	CTR <sub>DC</sub>	10			%
		H11A5	CTR <sub>DC</sub>	30			%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Turn-on time	$I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$ , $V_{CE}$ = 10 V	t <sub>on</sub>		3		μs	
Turn-off time		t <sub>off</sub>		3		μs	

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

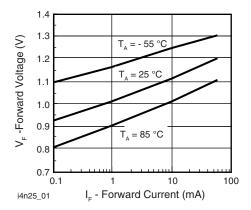


Fig. 1 - Forward Voltage vs. Forward Current

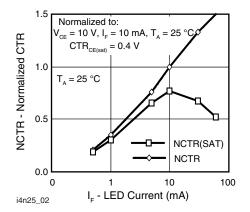


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

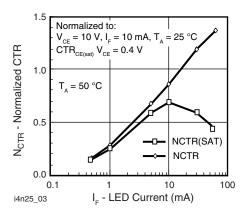


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

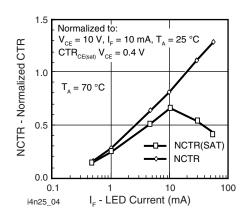


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

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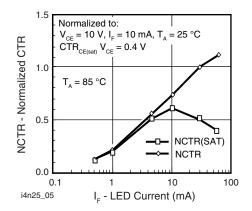


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

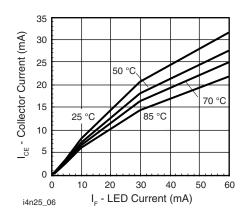


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current

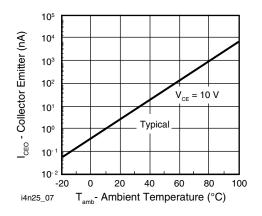


Fig. 7 - Collector Emitter Leakage Current vs. Temperature

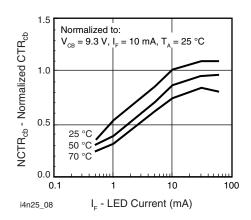


Fig. 8 - Normalized CTR<sub>cb</sub> vs. LED Current and Temperature

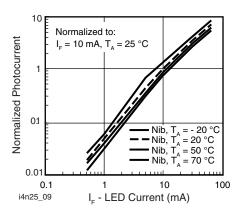


Fig. 9 - Normalized Photocurrent vs. I<sub>F</sub> and Temperature

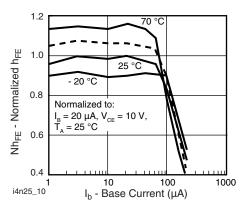


Fig. 10 - Normalized Non-Saturated  $h_{\text{FE}}$  vs. Base Current and Temperature

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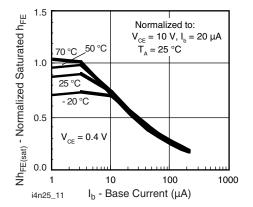
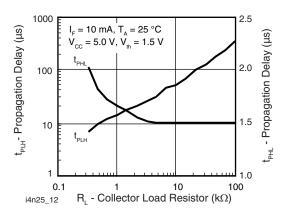
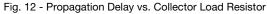


Fig. 11 - Normalized HFE vs. Base Current and Temperature





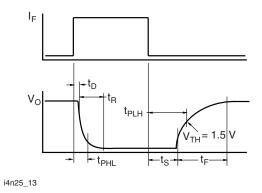
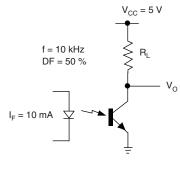


Fig. 13 - Switching Timing



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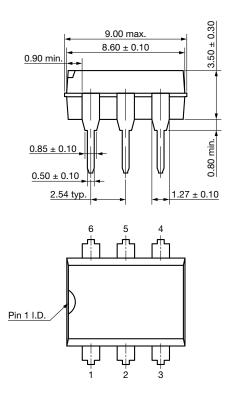
Fig. 14 - Switching Schematic

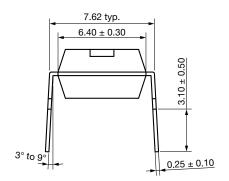


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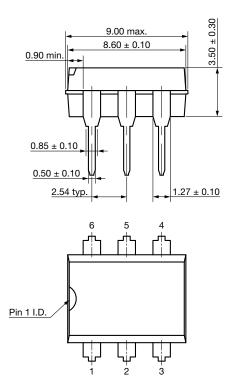
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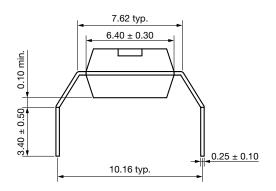
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#### **Option 6**





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10.30 max

7.62 typ.

 $6.40 \pm 0.30$ 

8.00 min

0.70 min.

0.50 min

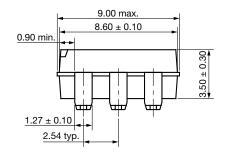
**Vishay Semiconductors** 

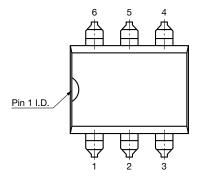
Leads coplanarity

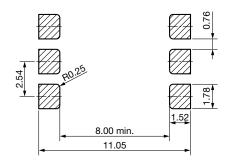
0.1 max.

 $0.25 \pm 0.10$  $4.30 \pm 0.30$ 

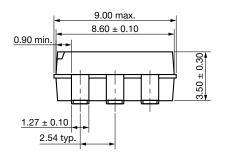
**Option 7** 

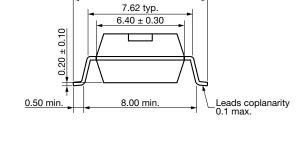




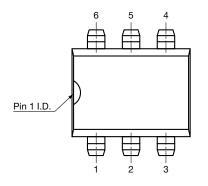


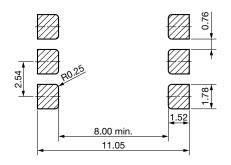
#### **Option 9**





10.05 max.





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