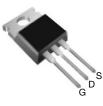


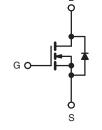


D Series Power MOSFET

PRODUCT SUMMARY					
V_{DS} (V) at T_{J} max.	450				
R _{DS(on)} max. at 25 °C (Ω)	$V_{GS} = 10 V$	0.17			
Q _g max. (nC)	88				
Q _{gs} (nC)	12				
Q _{gd} (nC)	23				
Configuration	Single				

TO-220AB





N-Channel MOSFET

FEATURES

- Optimal Design
 - Low Area Specific On-Resistance
 - Low Input Capacitance (Ciss)
 - Reduced Capacitive Switching Losses
 - High Body Diode Ruggedness
 - Avalanche Energy Rated (UIS)
- Optimal Efficiency and Operation
 - Low Cost
 - Simple Gate Drive Circuitry
 - Low Figure-of-Merit (FOM): Ron x Qg
 - Fast Switching
- Compliant to RoHS Directive 2011/65/EU

Note

* Pb containing terminations are not RoHS compliant, exemptions may apply

APPLICATIONS

- Consumer Electronics
 Displays (LCD or Plasma TV)
 - Displays (LC
- Lighting
- Industrial
 - Welding
 - Induction HeatingMotor Drives
 - Battery Chargers
- SMPS

ORDERING INFORMATION				
Package	TO-220AB			
Lead (Pb)-free	SiHP25N40D-E3			
Lead (Pb)-free and Halogen-free	SiHP25N40D-GE3			

PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	400		
Gate-Source Voltage	N/	± 30	V	
Gate-Source Voltage AC (f > 1 Hz)	V _{GS}	30		
Continuous Drain Current (T _J = 150 °C)	$V_{GS} \text{ at } 10 \text{ V} \qquad \frac{T_{C} = 25 \text{ °C}}{T_{C} = 100 \text{ °C}}$	۱ _D	25	А
	$T_{\rm C} = 100 ^{\circ}{\rm C}$		16	
Pulsed Drain Current ^a	I _{DM}	78		
Linear Derating Factor		2.2	W/°C	
Single Pulse Avalanche Energy ^b	E _{AS}	556	mJ	
Maximum Power Dissipation	PD	278	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 150	°C	
Drain-Source Voltage Slope	T _J = 125 °C	dV/dt	24	1//20
Reverse Diode dV/dt ^d	av/dt	0.6	V/ns	
Soldering Recommendations (Peak Temperature)		300 ^c	°C	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature.

b. V_{DD} = 50 V, starting T_J = 25 °C, L = 2.3 mH, R_g = 25 Ω , I_{AS} = 17 A.

c. 1.6 mm from case.

d. $I_{SD} \leq I_D$, starting $T_J = 25 \ ^\circ C$.

S12-0625-Rev. B, 26-Mar-12



Available

www.vishay.com

Vishay Siliconix

PARAMETER	SYMBOL	TYP.		MAX.		UNIT		
Maximum Junction-to-Ambient	R _{thJA}	-	- 62					
Maximum Junction-to-Case (Drain)	R _{thJC}	- 0.45			°C/W			
		•						
SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$, u	Inless otherwi	ise noted)						
PARAMETER	SYMBOL	TES	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		·						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} :	= 0 V, I _D = 2	250 µA	400	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C, I	_D = 250 μA	-	0.5	-	V/°C
Gate-Source Threshold Voltage (N)	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 2	250 µA	3	-	5	V
Gate-Source Leakage	I _{GSS}		$V_{GS} = \pm 30$	V	-	-	± 100	nA
		V _{DS} =	= 400 V, V _G	_S = 0 V	-	-	1	
Zero Gate Voltage Drain Current	I _{DSS} V _{DS} = 320 V, V _{GS} = 0 V, T _J = 125 °C		′, T _J = 125 °C	-	-	10	μA	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	١	₀ = 13 A	-	0.14	0.17	Ω
Forward Transconductance	9 _{fs}	V _{DS}	V _{DS} = 50 V, I _D = 13 A		-	7.4	-	S
Dynamic					1	I	1	
Input Capacitance	C _{iss}				-	1707	-	pF
Output Capacitance	C _{oss}	$\label{eq:VGS} \begin{array}{l} V_{GS} = 0 \ V, \\ V_{DS} = 100 \ V, \\ f = 1 \ \text{MHz} \end{array}$		-	177	-		
Reverse Transfer Capacitance	C _{rss}			-	19	-		
Total Gate Charge	Qg			-	44	88		
Gate-Source Charge	Q _{gs}	$V_{GS} = 10 V$	$V_{GS} = 10 \text{ V}$ $I_D = 13 \text{ A}, V_{DS} = 320 \text{ V}$		-	12	-	nC
Gate-Drain Charge	Q _{gd}				-	23	-	1
Turn-On Delay Time	t _{d(on)}			-	21	42		
Rise Time	t _r	Vee -	V _{DD} = 320 V, I _D = 13 A,		-	57	86	-
Turn-Off Delay Time	t _{d(off)}	V _{GS} =	= 10 V, R _q =	24.6 Ω	-	40	80	ns
Fall Time	t _f				-	37	74	1
Gate Input Resistance	R _g	f = 1 MHz, open drain		-	1.8	-	Ω	
Drain-Source Body Diode Characteristic	cs							
Continuous Source-Drain Diode Current	١ _S	MOSFET sym showing the	MOSFET symbol showing the		-	-	24	
Pulsed Diode Forward Current	I _{SM}	integral reverse p - n junction diode		-	-	78	A	
Diode Forward Voltage	V _{SD}	T _J = 25 °0	T _J = 25 °C, I _S = 13 A, V _{GS} = 0 V		-	-	1.2	V
Reverse Recovery Time	t _{rr}				-	353	-	ns
Reverse Recovery Charge	Q _{rr}	$T_J = 25 \text{ °C}, I_F = I_S = 13 \text{ A},$ dl/dt = 100 A/µs, V _R = 20 V		-	4.4	-	uС	
Reverse Recovery Current	I _{RRM}				24		A	

For technical questions, contact: <u>hvm@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



Vishay Siliconix

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

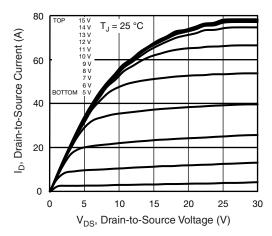


Fig. 1 - Typical Output Characteristics

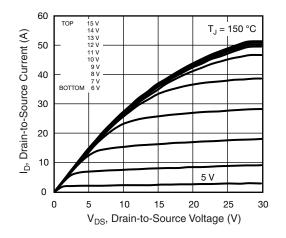


Fig. 2 - Typical Output Characteristics

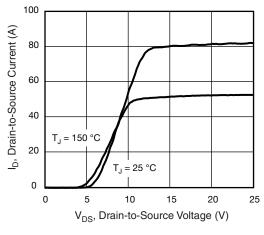


Fig. 3 - Typical Transfer Characteristics

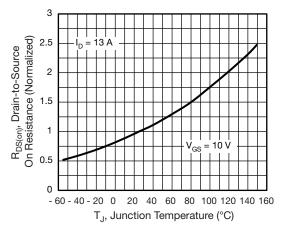


Fig. 4 - Normalized On-Resistance vs. Temperature

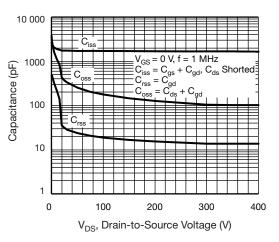


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

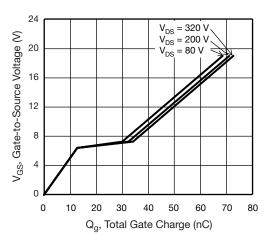


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

Document Number: 91483



Vishay Siliconix

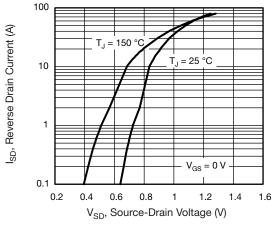
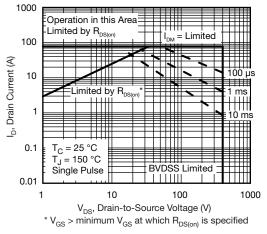
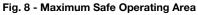


Fig. 7 - Typical Source-Drain Diode Forward Voltage





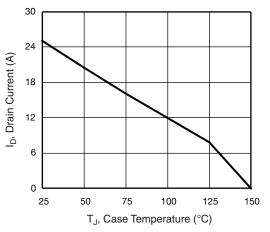


Fig. 9 - Maximum Drain Current vs. Case Temperature

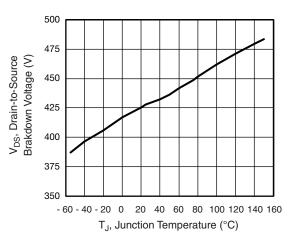
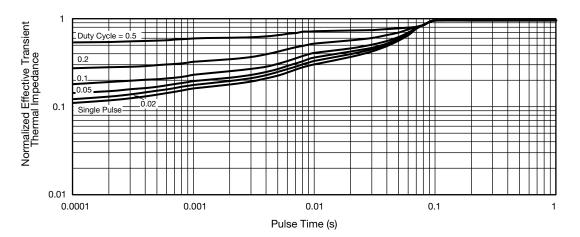


Fig. 10 - Temperature vs. Drain-to-Source Voltage







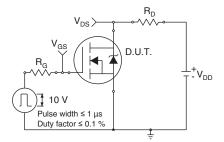


Fig. 12 - Switching Time Test Circuit



Fig. 13 - Switching Time Waveforms

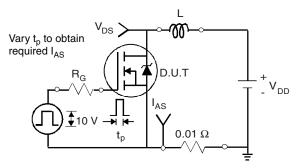


Fig. 14 - Unclamped Inductive Test Circuit

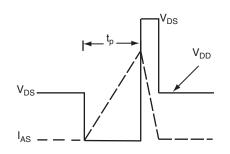


Fig. 15 - Unclamped Inductive Waveforms

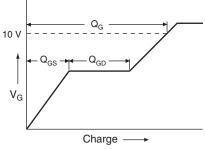


Fig. 16 - Basic Gate Charge Waveform

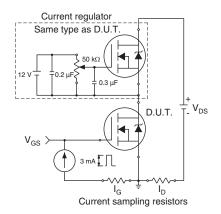


Fig. 17 - Gate Charge Test Circuit

5

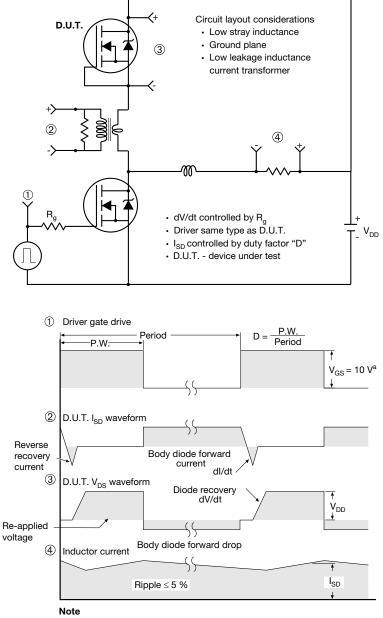
Vishay Siliconix





Vishay Siliconix

Peak Diode Recovery dV/dt Test Circuit



a. $V_{GS} = 5 V$ for logic level devices

Fig. 18 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?91483.



www.vishay.com

TO-220-1



DIM.	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
А	4.24	4.65	0.167	0.183	
b	0.69	1.02	0.027	0.040	
b(1)	1.14	1.78	0.045	0.070	
С	0.36	0.61	0.014	0.024	
D	14.33	15.85	0.564	0.624	
E	9.96	10.52	0.392	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.10	6.71	0.240	0.264	
J(1)	2.41	2.92	0.095	0.115	
L	13.36	14.40	0.526	0.567	
L(1)	3.33	4.04	0.131	0.159	
ØР	3.53	3.94	0.139	0.155	
Q	2.54	3.00	0.100	0.118	
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031					

Note

- M^{\star} = 0.052 inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM

Package Picture					
ASE		Xi'an			
		IRF 9510 744K AB			

Revison: 14-Dec-15

1 For technical questions, contact: <u>hvm@vishay.com</u> Document Number: 66542



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.