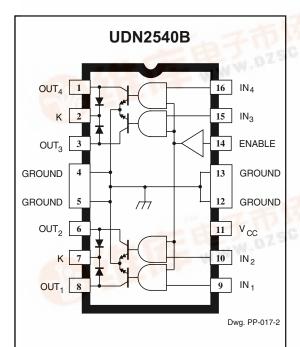
2540

Data Sheet **29317C**

QUAD DARLINGTON POWER DRIVER



Combining AND logic gates and inverting high-current bipolar outputs, the UDN2540B and A2540SLB quad Darlington power drivers provide interface between low-level signal-processing circuits and power loads totaling 360 W. Each of the four independent outputs can sink up to 1.8 A in the on state with peak inrush currents to 2.5 A. The four power outputs are each comprised of an open-collector Darlington driver and an internal flyback/clamp diode for switching inductive leads. They feature a minimum breakdown and sustaining voltage of 50 V. The logic inputs are compatible with TTL and 5 V CMOS logic systems.

Typical applications include print heads, relays, solenoids, and dc stepping motors. These drivers can also be used to drive high-current incandescent lamps, LEDS, and heaters. A similar device, specifically intended for driving a unipolar stepper motor in the two-phase drive format, is the UDN2544B.

The UDN2540B is supplied in a 16-pin batwing power DIP; the A2540SLB is supplied in a 20-lead batwing power SOIC for surface-mount applications. The batwing construction provides for maximum package power dissipation in a standard construction. At 25°C, and with only 1 sq. in. of copper foil at the ground tabs, either package is capable of safely dissipating more than 2 W.

ABSOLUTE MAXIMUM RATINGS

at $T_A = 25^{\circ}C$

Output Voltage, V _{OUT}	. 50 \	/
Output Current, I _{OUT} (peak)	2.5	4
(continuous)	1.8	4
Logic Supply Voltage, V _{CC}	7.0	/
Input Voltage, V _{IN}	7.0 \	ı
Package Power Dissipation,		

P_DSee Graph
Operating Temperature Range,

T_A.....-20°C to +85°C

Storage Temperature Range,

dzsc.com

T_S.....--55°C to +150°C

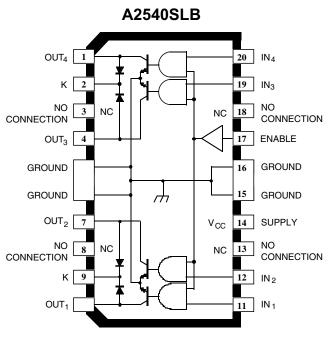
FEATURES

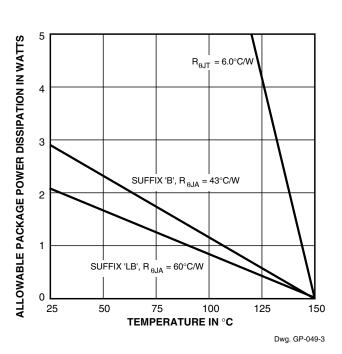
- 1.8 A Continuous Output Current
- Output Voltage to 50 V
- TTL and 5 V CMOS Compatible Inputs
- Efficient Input/Output Pinning
- Integral Transient-Suppression Diodes
- Replaces L6221A and L6221CD

Always order by complete part number:

Part Number	Package
UDN2540B	16-pin batwing DIP
A2540SLB	20-lead batwing SOIC







Dwg. PP-017-3

TRUTH TABLE

ENABLE	IN _N	OUT _N
Н	Н	ON
_	L	OFF
L	Х	OFF

X = Don't care.

ELECTRICAL CHARACTERISTICS at T_A = 25°C, T_J \leq 150°C, V_{CC} = 4.75 V to 5.25 V.

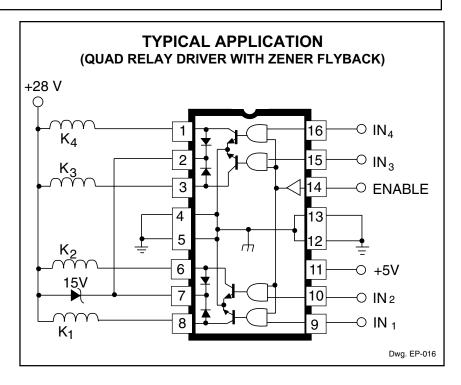
				Lim	its	
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Leakage Current	I _{CEX}	$V_{OUT} = 50 \text{ V}, V_{IN} = 0.8 \text{ V}, V_{EN} = 2.4 \text{ V}$	_	<1.0	100	μΑ
		V _{OUT} = 50 V, V _{IN} = 2.4 V, V _{EN} = 0.8 V	_	<1.0	100	μΑ
Output Sustaining Voltage	V _{CE(sus)}	I _{OUT} = 1.8 A, L = 3.0 mH	50	_	_	V
Output Saturation Voltage	V _{CE(SAT)}	I _{OUT} = 600 mA, V _{IN} = V _{EN} = 2.4 V	_	0.9	1.0	V
		I _{OUT} = 1.0 A, V _{IN} = V _{EN} = 2.4 V	_	1.0	1.2	V
		$I_{OUT} = 1.8 \text{ A}, V_{IN} = V_{EN} = 2.4 \text{ V}$	_	1.3	1.6	V
Input Voltage	Logic 1	$V_{IN(1)}$ or $V_{EN(1)}$	2.4	_	-	V
	Logic 0	V _{IN(0)} or V _{EN(0)}	_	_	0.8	V
Input Current	Logic 1	$V_{IN(1)}$ or $V_{EN(1)} = 2.4 \text{ V}$	_	_	10	μΑ
	Logic 0	$V_{IN(0)}$ or $V_{EN(0)} = 0.8 \text{ V}$	_	_	-100	μΑ
Total Supply Current	I _{CC}	$V_{IN}^* = V_{EN} = 2.4 \text{ V}, V_{CC} = 5.0 \text{ V},$	_	14	20	mA
		Outputs Open				
		$V_{IN}^* = V_{EN} = 0.8 \text{ V}, V_{CC} = 5.0 \text{ V}$	_	0.4	2.0	mA
Clamp Diode Forward Voltage	V _F	I _F = 1.0 A	_	1.3	1.6	V
		I _F = 1.8 A	_	1.6	2.0	٧
Clamp Diode Leakage Current	I _R	V _R = 50 V	_	<1.0	100	μΑ

Typical Data is for design information only.

Negative current is defined as coming out of (sourcing) the specified terminal.

As used here, -100 is defined as greater than +10 (absolute magnitude convention) and the minimum is implicitly zero.

^{*}All inputs simultaneously, all other tests are performed with each input tested separately.

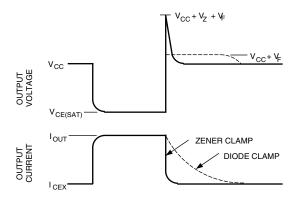


APPLICATIONS INFORMATION

A typical application is shown for driving four high-current relays, solenoids, or print heads. A Zener diode is used to increase the flyback voltage, providing a much faster inductive load turn-off current decay, resulting in faster dropout (reduced relay contact arcing), and improved performance. The maximum Zener voltage, plus the load supply voltage, plus the flyback diode forward voltage must not exceed the device's rated sustaining voltage.

With external control circuitry, the ENABLE input can be used for chopper (PWM) applications. If the ENABLE input is not used, it should be tied high.

All inputs will float high if open circuited.

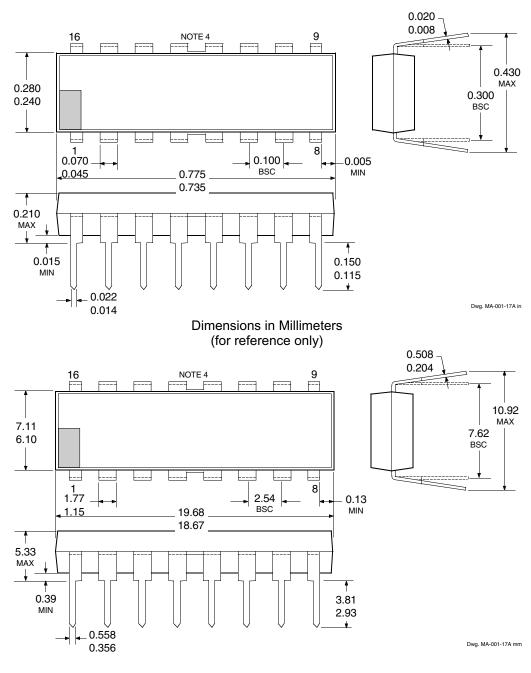


Dwg. WP-001



UDN2540B

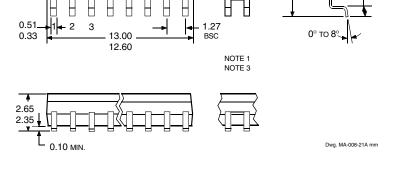
Dimensions in Inches (controlling dimensions)



NOTES: 1. Leads 1, 8, 9, and 16 may be half leads at vendor's option.

- 2. Lead thickness is measured at seating plane or below.
- 3. Lead spacing tolerance is non-cumulative.
- 4. Webbed lead frame. Leads indicated are internally one piece.
- 5. Exact body and lead configuration at vendor's option within limits shown.

A2540SLB Dimensions in Inches (for reference only) 0.0125 0.0091 0.2992 0.419 0.2914 0.394 0.050 0.016 0.020 1 ← 2 0.050 3 0° TO 8° **↓** 0.013 0.4961 NOTE 1 0.0926 0.1043 L_{0.0040} MIN. Dwg. MA-008-21A in **Dimensions in Millimeters** (controlling dimensions) 0.32



0.23

1.27

10.65

10.00

NOTES: 1. Webbed lead frame. Leads 5, 6, 15, and 16 are internally one piece.

7.60

7.40

- 2. Lead spacing tolerance is non-cumulative.
- 3. Exact body and lead configuration at vendor's option within limits shown.

The products described here are manufactured under one or more U.S. patents or U.S. patents pending.

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POWER SINK DRIVERS

IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

0	utnut Batir	ngo *	Features					
Output Ratings *		Serial Latched Diode Internal			Internal	_		
mA	V	#	Input	Drivers	Clamp	Outputs	Protection	Part Number [†]
75	17	8	X	X		constant current	_	6275
400	17	16	X	Х		constant current		6276
100	20 30	8 32	X		-	saturated	_	2595 5833
	40	32 32	â	X X	_	_ saturated	_	5832
	50	8		essable decod	ler/driver	DMOS	_	6B259
	50	8	_	Χ	_	DMOS	_	6B273
	50	8	X	X	_	DMOS		6B595
250	50	8	addre	essable decod	ler/driver	DMOS	_	6259
	50	8	X	X X	_	DMOS	_	6273
	50 135	8 7	_	_	X	DMOS -		6595 7003
300	45	1		all sensor/driv			X	5140
300	50	7	— па —	- CIISOI/UIIV	X	<u>-</u>	^	2003
	50	8	_	_	Χ	_	_	2803
	50	8	_	_	X X	saturated	_	2596
	60	<u>4</u>	_	_	X	saturated	X	2557
	95	7	_	_	X	_	_	2023
050	95	8			X	_		2823
350	50 50	4 7	_	Х	X	_	_	5800 2004
	50 50	8	_	_	X X X	_	_	2804
	50	8	_	X	â	_	_	5801
	50	8	X	X	_	_	_	5821
	50	8	Χ	X	. , , X	_	_	5841
	50	8		essable decod	ler/driver	DMOS	_	6A259
	50 80	8 8	X	X	_	DMOS	_	6A595 5822
	80	8	X X	X X X	X	_	_	5842
	95	7	_	_	X	_	_	2024
	95	8	_	_	X	_	_	2824
450	30	28	dual 4	4- to 14-line d	ecoder/driv	er –	-	6817
600	60	4	_	_	_	saturated	Х	2547
	60	4	_	_	Х	saturated	X	2549
700	60	4	_	_	Χ	saturated	Х	2543 and 2559
750	50	8	_	_	Х	saturated	_	2597
900	14	2		all sensor/driv	er X	saturated	X	3625
	26	2		all sensor/driv		saturated	X	3626
1000	46	4		er motor cont			_	7024 and 7029
1200	46	4		stepping cont			_	7042
1250	50	4	stepp	er motor trans		r –	Χ	5804
	50	4		_	X	_	_	2064 and 2068
1500	80	4		_	Χ	_	_	2065 and 2069
1800	50	4	_	_	X	_	_	2544
	50	4	_	-	X	_	_	2540
3000	46	4		er motor cont			-	7026
4000	46	4	micro	stepping conf		r MOS		7044
4000	50	4	_	_	X	_	_	2878
	80	4	_	_	Χ	_	-	2879

^{*} Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

[†] Complete part number includes additional characters to indicate operating temperature range and package style.

