TOSHIBA 2SJ507

TOSHIBA FIELD EFFECT TRANSISTOR SILICON P CHANNEL MOS TYPE (L²-π-MOS V)

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE **APPLICATIONS**

4V Gate Drive

Low Drain-Source On Resistance : RDS (ON) = 0.5Ω (Typ.)

High Forward Transfer Admittance : $|Y_{fs}| = 1.0S$ (Typ.)

Low Leakage Current : $I_{DSS} = -100 \mu A \text{ (Max.)} \text{ (V}_{DS} = -60 \text{V)}$

Enhancement-Mode : $V_{th} = -0.8 \sim -2.0 V$

 $(V_{DS} = -10V, I_D = -1mA)$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTER	SYMBOL	RATING	UNIT	
Drain-Source Voltage	$v_{ m DSS}$	-60	V	
Drain-Gate Voltage (R	$v_{ m DGR}$	-60	V	
Gate-Source Voltage	VGSS	±20	V	
Drain Current	DC	$I_{\mathbf{D}}$	_1	A
	Pulse	$I_{ m DP}$	-3	A
Drain Power Dissipation	$P_{\mathbf{D}}$	0.9	W	
Single Pulse Avalanch	E_{AS}	249.6	mJ	
Avalanche Current	I_{AR}	-1	A	
Repetitive Avalanche l	E_{AR}	0.09	mJ	
Channel Temperature	$\mathrm{T_{ch}}$	150	°C	
Storage Temperature F	$\mathrm{T_{stg}}$	-55~150	°C	

EIAJ

JEDEC

TOSHIBA 2-5J1C

INDUSTRIAL APPLICATIONS

2.54

SOURCE DRAIN **GATE**

TO-92MOD

0.75MAX 1.0MAX

XAM8.0

0.6MAX

Unit in mm

Weight: 0.36g (Typ.)

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Ambient	R _{th (ch-a)}	138	°C/W

Note;

- Repetitive rating; Pulse Width Limited by Max. junction temperature.
- $V_{DD} = -25V$, Starting $T_{ch} = 25^{\circ}C$, L=339mH, $R_{G} = 25\Omega$, $I_{AR} = -1A$

This transistor is an electrostatic sensitive device. Please handle with caution.

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

ELECTRICAL CHARACTERISTICS (Ta = 25 C)								
CHARA	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakag	e Current	I_{GSS}	$V_{GS} = \pm 16V, V_{DS} = 0V$	_	_	±10	μ A	
Drain Cut-of	f Current	$I_{ m DSS}$	$V_{DS} = -60V, V_{GS} = 0V$	_	_	-100	μ A	
Drain-Source Voltage	Breakdown	V (BR) DSS	$I_{D} = -10 \text{mA}, V_{GS} = 0 \text{V}$	-60	_	_	V	
Gate Thresho	old Voltage	$V_{ m th}$	$V_{DS} = -10V, I_{D} = -1mA$	-0.8	_	-2.0	V	
Drain-Source	ON Resistance	R _{DS} (ON)	$V_{GS} = -4V, I_D = -0.5A$ $V_{GS} = -10V, I_D = -0.5A$	_	0.72	1.0	Ω	
Forward Train Admittance	nsfer	Y _{fs}	$V_{DS} = -10V, I_{D} = -0.5A$	0.5	1.0	_	s	
Input Capaci	tance	C_{iss}		_	170	_		
Reverse Transfer Capacitance		C_{rss}	$egin{array}{l} V_{ m DS} = -10 { m V}, \ V_{ m GS} = 0 { m V}, \ { m f} = 1 { m MHz} \end{array}$	_	25	_	pF	
Output Capacitance		Coss		_	72	_	İ	
Switching Time	Rise Time	$t_{\mathbf{r}}$	V_{GS} $I_{D} = -0.5A$ V_{OUT}	_	20	_		
	Turn-on Time	t _{on}	V_{GS} V	_	35	_	ns	
	Fall Time	tf		_	30	_	115	
	Turn-off Time	t _{off}	$egin{aligned} V_{ ext{IN}} : t_{ ext{r}}, \ t_{ ext{f}} < 5 ext{ns}, \ Duty \leq 1\%, \ t_{ ext{w}} = 10 \mu ext{s} \end{aligned}$		135	_		
Total Gate Charge (Gate-Source Plus Gate-Drain)		$\mathbf{Q}_{\mathbf{g}}$	$V_{DD} = -48V, V_{GS} = -10V,$	_	5.6	_	C	
Gate-Source Charge		$Q_{ m gs}$	$I_{D} = -1A$		3.9	_	nC	
Gate-Drain ("Miller") Charge		$ m Q_{gd}$		_	1.7	_	1	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{ m DR}$	_	_	_	-1	A
Pulse Drain Reverse Current	$I_{ m DRP}$	_	_	_	-3	A
Diode Forward Voltage	$v_{ m DSF}$	$I_{DR} = -1A$, $V_{GS} = 0V$		_	1.5	V
Reverse Recovery Time	t_{rr}	$I_{DR} = -1A$, $V_{GS} = 0V$	_	58	_	ns
Reverse Recovery Charge	Q_{rr}	$ m dI_{DR}$ / $ m dt$ = 50A / $ m \mu s$	_	72.5	_	nC

MARKING

