TOSHIBA

Discrete Semiconductors

2SK1358

Field Effect Transistor
Silicon N Channel MOS Type (π -MOS II.5)
High Speed, High Current DC-DC Converter,
Relay Drive and Motor Drive Applications

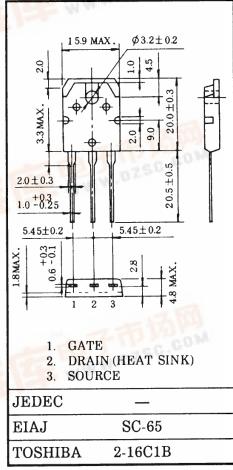
Features

- Low Drain-Source ON Resistance
 - $R_{DS(ON)} = 1.1\Omega$ (Typ.)
- High Forward Transfer Admittance
 - $|Y_{fs}| = 4.0S$ (Typ.)
- Low Leakage Current
 - $I_{DSS} = 300 \mu A \text{ (Max.)} @ V_{DS} = 720 V$
- Enhancement-Mode
 - $V_{th} = 1.5 \sim 3.5 \text{V} @ V_{DS} = 10 \text{V}, I_{D} = 1 \text{mA}$

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Drain-Source Voltage		V _{DSS}	900	V	
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V _{DGR}	900	٧	
Gate-Source Voltage		V _{GSS}	±30	V	
Drain Current	DC	I _D	9	А	
	Pulse	I _{DP}	27		
Drain Power Dissipation (Tc = 25°C)		P_{D}	150	W	
Channel Temperature	13	T _{ch}	150	°C	
Storage Temperature Range		T _{stg}	-55 ~ 150	°C	

Industrial Applications Unit in mm



Weight: 4.6g

Thermal Characteristics

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	R _{th(ch-c)}	0.833	°C/W
Thermal Resistance, Channel to Ambient	R _{th(ch-a)}	50	°C/W

This transistor is an electrostatic sensitive device. Please handle with care.

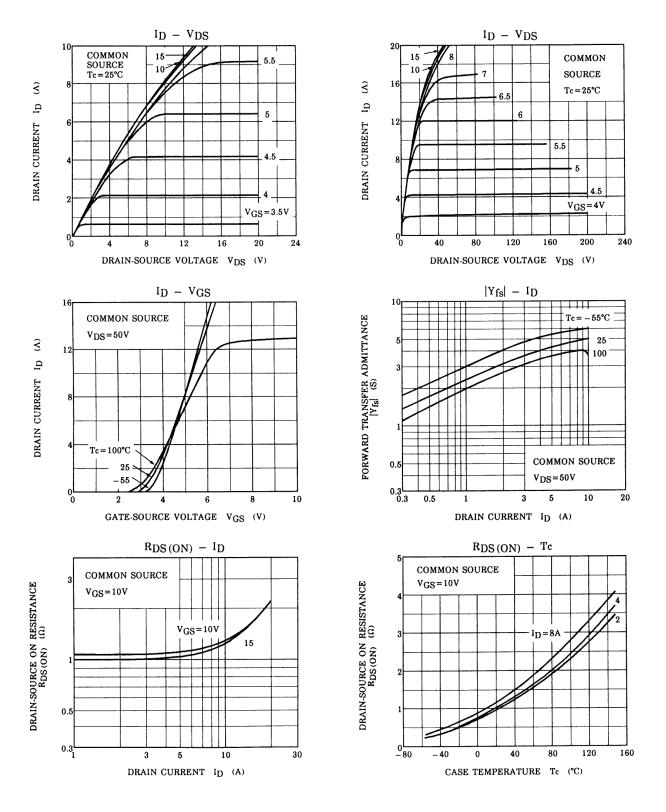


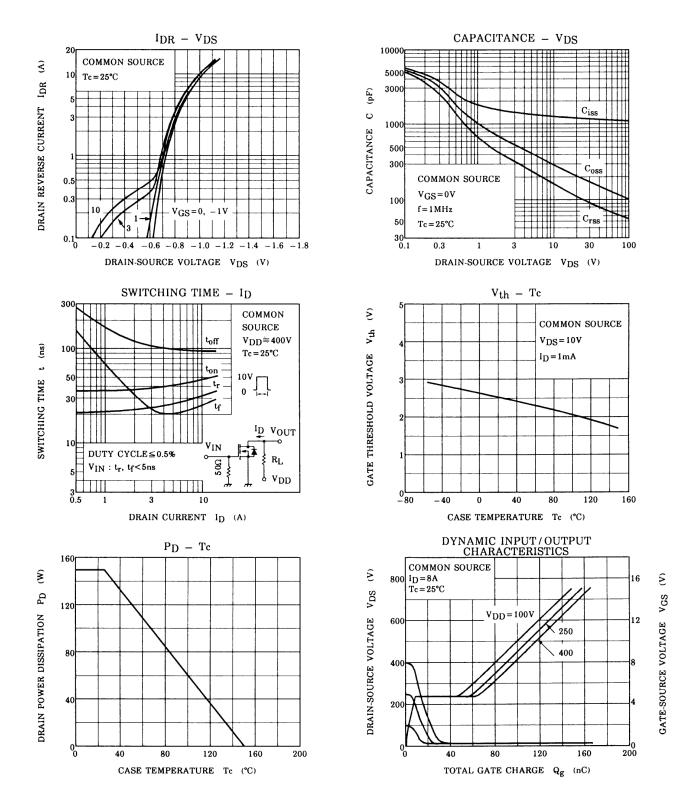
Electrical Characteristics (Ta = 25°C)

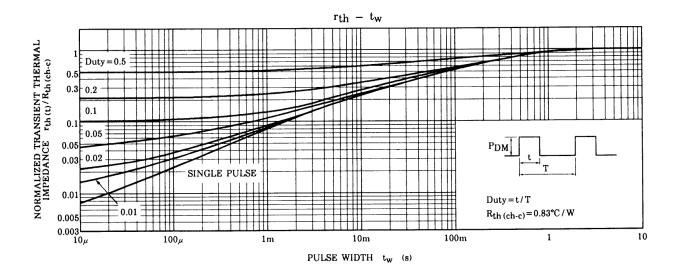
CHAR	ACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage C	Current	I _{GSS}	$V_{GS} = \pm 25V, V_{DS} = 0V$	_	_	±100	nA
Drain Cut-off Current		I _{DSS}	V _{DS} = 720V, V _{GS} = 0V		-	300	μA
Drain-Source E	Breakdown Voltage	down Voltage $V_{(BR) DSS}$ $I_D = 10 \text{mA}, V_{GS} = 0 \text{V}$		900	-	-	V
Gate Threshold	Voltage	V _{th}	$V_{DS} = 10V$, $I_D = 1mA$	1.5	-	3.5	V
Drain-Source C	N Resistance	R _{DS (ON)}	I _D = 4A, V _{GS} = 10V	-	1.1	1.4	Ω
Forward Transf	er Admittance	Y _{fs} I	$V_{DS} = 20V, I_{D} = 4A$	2.0	4.0	-	S
Input Capacitar	nce	C _{iss}		-	1300	1800	_
Reverse Transfer Capacitance Output Capacitance		C _{rss}	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	-	100	150	pF
		C _{oss}		-	180	260	
	Rise Time	t _r		-	25	50	
Switching	Turn-on Time	t _{on}	V_{GS}^{10V} $I_{D=4A}^{I_{D=4A}}$ VOUT	-	40	80	
	Fall Time	t _f	$V_{GS} = V_{OUT}$ $V_{GS} = V_{OUT}$ $R_{L} = 100\Omega$	-	20	40	ns
	Turn-off Time	t _{off}		-	100	200	
			$V_{\text{IN}}: t_{\text{r}}, t_{\text{f}} < 5 \text{ns}, V_{\text{DD}} = 400 \text{V}$ $Duty \leq 1\%, t_{\text{W}} = 10 \mu \text{s}$				
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	V _{DD} = 400V, V _{GS} = 10V,	-	120	240	0
Gate-Source Charge		Q_{gs}	$I_D = 9A$	-	70	_	nC
Gate-Drain ("Miller") Charge		Q_{gd}		-	50	-	

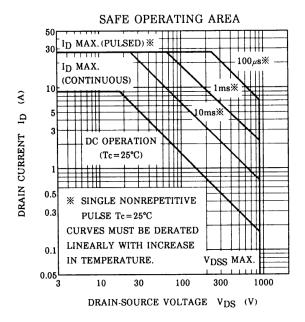
Source-Drain Diode Ratings and Characteristics (Ta = 25° C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I _{DR}	-	-	-	9	Α
Pulse Drain Reverse Current	I _{DRP}	_	-	-	27	Α
Diode Forward Voltage	V _{DSF}	$I_{DR} = 9A$, $V_{GS} = 0V$	_	-	-2.0	V









Notes

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