Ordering number : ENN6970

N-Channel Silicon MOSFET

2SK3495



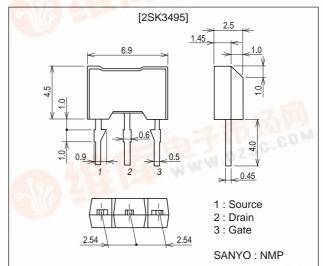
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.
- · Meets radial taping.

Package Dimensions

unit : mm 2087A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		1.2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	4.8	Α
Allowable Power Dissipation	PD	and Later	WWW TO	W
Channel Temperature	Tch	900 7 4 2	150	°C
Storage Temperature	Tstg	- 57 67 6	-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _G S=0	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0			10	μА
Gate-to-Sourse Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =0.6A	1.0	1.5	0720	S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =0.6A, V _G S=10V	1100	380	500	mΩ
	R _{DS} (on)2	I _D =0.6A, V _{GS} =4V		500	680	mΩ

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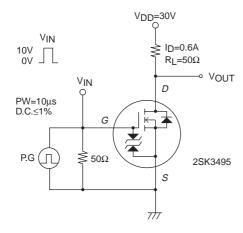
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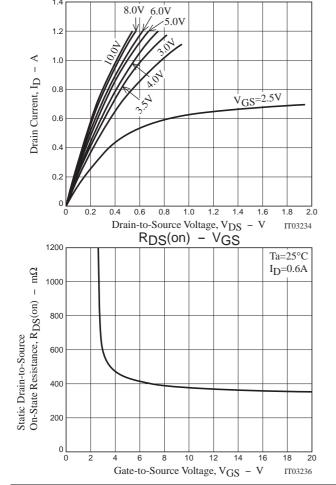
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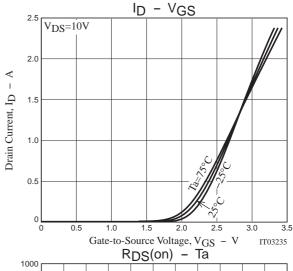
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		70		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		20		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		5		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		4		ns
Rise Time	t _r	See specified Test Circuit		3		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		17		ns
Fall Time	tf	See specified Test Circuit		4		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =1.2A		3.6		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =1.2A		0.6		nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=10V, VGS=10V, ID=1.2A		0.5		nC
Diode Forward Voltage	V _{SD}	I _S =1.2A, V _G S=0		0.86	1.2	V

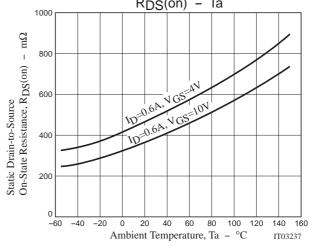
Switching Time Test Circuit

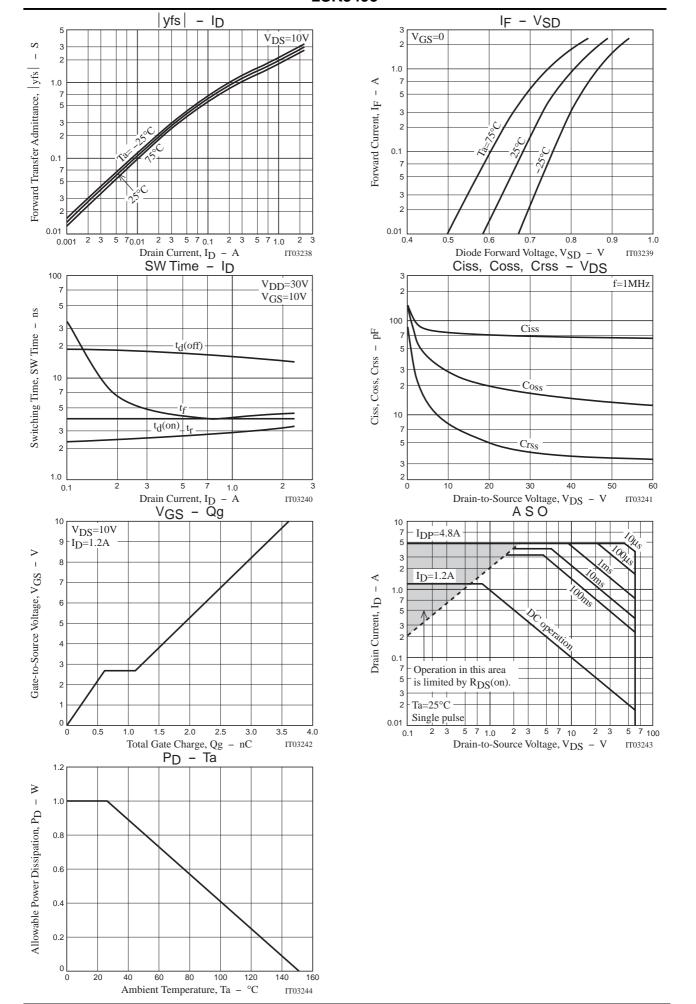


ID - VDS









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