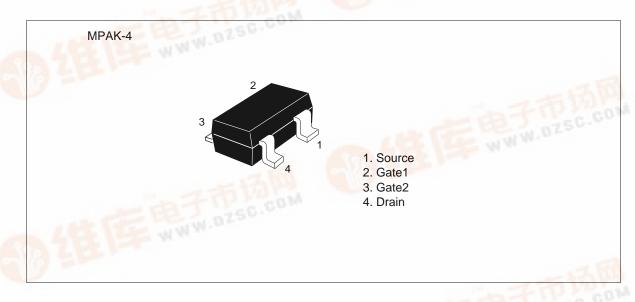
Silicon N-Channel Dual Gate MOS FET

HITACHI

Application

UHF TV tuner RF amplifier

Outline





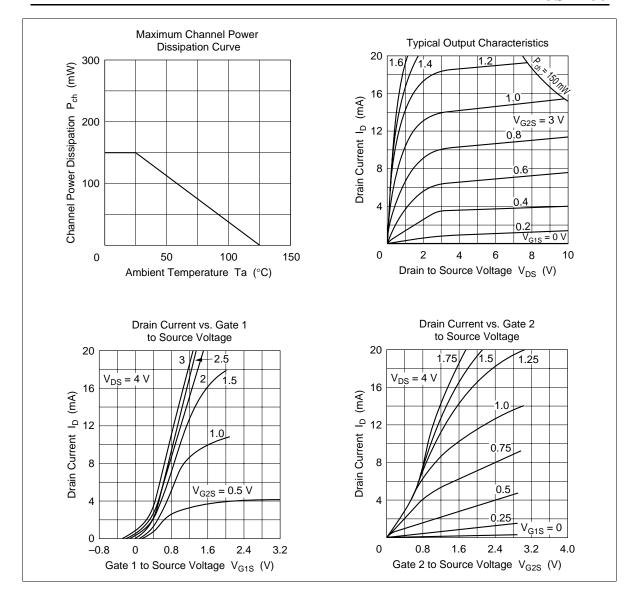
Absolute Maximum Ratings (Ta = 25°C)

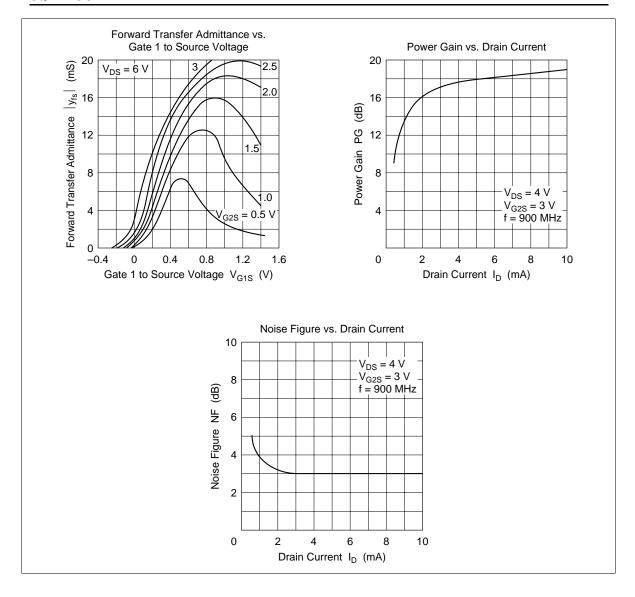
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DS}	12	V
Gate 1 to source voltage	$V_{\sf G1S}$	±10	V
Gate 2 to source voltage	$V_{\rm G2S}$	±10	V
Drain current	I _D	35	mA
Channel power dissipation	Pch	150	mW
Channel temperature	Tch	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics ($Ta = 25^{\circ}C$)

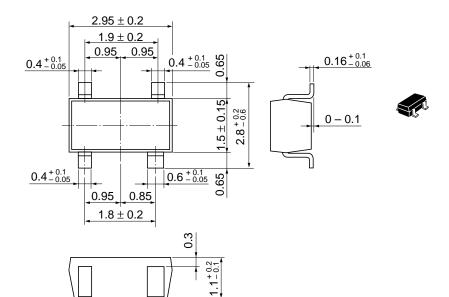
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSX}$	12	_	_	V	$V_{G1S} = V_{G2S} = -5 \text{ V},$ $I_D = 200 \mu A$
Gate 1 to source breakdown voltage	$V_{(BR)G1SS}$	±10	_	_	V	$I_{G1} = \pm 10 \mu A, V_{G2S} = V_{DS} = 0$
Gate 2 to source breakdown voltage	$V_{(BR) G2SS}$	±10	_	_	V	$I_{G2} = \pm 10 \mu A, V_{G1S} = V_{DS} = 0$
Gate 1 cutoff current	I _{G1SS}	_	_	±100	nA	$V_{G1S} = \pm 8 \text{ V}, V_{G2S} = V_{DS} = 0$
Gate 2 cutoff current	$I_{\rm G2SS}$	_	_	±100	nA	$V_{G2S} = \pm 8 \text{ V}, V_{G1S} = V_{DS} = 0$
Gate 1 to source cutoff voltage	$V_{\text{G1S(off)}}$	+0.5	_	-0.8	V	$V_{DS} = 6 \text{ V}, V_{G2S} = 3 \text{V},$ $I_D = 100 \mu\text{A}$
Gate 2 to source cutoff voltage	$V_{\text{G2S(off)}}$	+0.5	_	-0.8	V	$V_{DS} = 6 \text{ V}, V_{G1S} = 3 \text{V}, I_D = 100 \mu\text{A}$
Drain current	I _{DSS}	0	_	4	mA	$V_{DS} = 6 \text{ V}, V_{G2S} = 3 \text{V}, V_{G1S} = 0$
Forward transfer admittance	$ y_{fs} $	15	_	_	mS	$V_{DS} = 6 \text{ V}, V_{G2S} = 3 \text{V},$ $I_D = 10 \text{ mA}, f = 1 \text{ kHz}$
Input capacitance	Ciss	_	1.7	2.2	pF	$V_{DS} = 6 \text{ V}, V_{G2S} = 3 \text{V},$ $I_D = 10 \text{ mA}, f = 1 \text{ MHz}$
Output capacitance	Coss	_	1.0	1.4	pF	_
Reverse transfer capacitance	Crss	_	0.017	0.03	pF	
Power gain	PG	16	19	_	dB	$V_{DS} = 4 \text{ V}, V_{G2S} = 3 \text{V},$ $I_D = 10 \text{ mA}, f = 900 \text{ MHz}$
Noise figure	NF	_	3.0	4.5	dB	

Note: Marking is "FI-".









Cautions

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