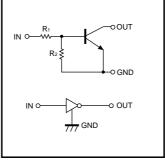
Digital transistors (built-in resistors)

DTC143ZM / DTC143ZE / DTC143ZUA / DTC143ZKA / DTC143ZSA

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making device design easy.

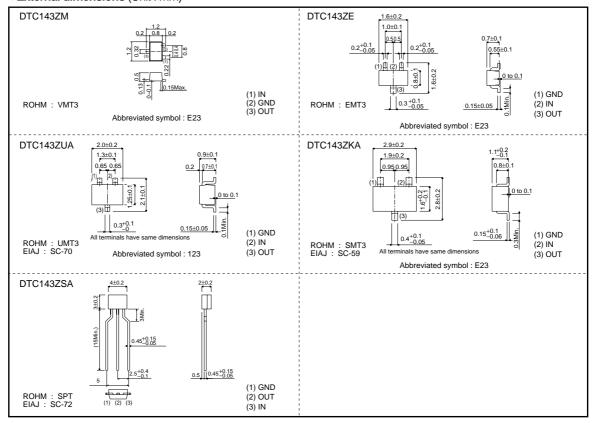


●Equivalent circuit

Structure

NPN digital transistor (Built-in resistor type)

●External dimensions (Unit: mm)



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTC143Z□)						
		М	Е	UA	KA	SA	Unit	
Supply voltage	Vcc	50						
Input voltage	Vin	−5 to +30						
Output current	lo	100						
	IC(Max.)	100						
Power dissipation	Pd	150		200		300	mW	
Junction temperature	Tj	150						
Storage temperature	Tstg	−55 to +150						

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI(off)	_	-	0.5	V	Vcc=5V, Io=100μA	
	V _{I(on)}	1.3	_	_	\ \	Vo=0.3V, Io=5mA	
Output voltage	Vo(on)	-	0.1	0.3	V	Io/I=5mA/0.25mA	
Input current	- Iı	-	_	1.8	mA	Vi=5V	
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	80	-	_	_	Vo=5V, Io=10mA	
Input resistance	R ₁	3.29	4.7	6.11	kΩ	_	
Resistance ratio	R2/R1	8	10	12	_	-	
Transition frequency	f⊤	-	250	_	MHz	VcE=10V, IE= -5mA, f=100MHz *	

^{*}Transition frequency of the device

Packaging specifications

Туре	Package	VMT3	EMT3	UMT3	SMT3	SPT		
	Packaging type	Taping	Taping	Taping	Taping	Taping		
	Code	T2L	TL	T106	T146	TP		
	Basic ordering unit (pieces)	8000	3000	3000	3000	5000		
DTC143ZM		0	_	-	_	_		
DTC143ZE		-	0	-	_	_		
DTC143ZUA		-	_	0	_	_		
DTC143ZKA		-	_	_	0	_		
DTC143ZSA		_	_	_	_	0		

•Electrical characteristic curves

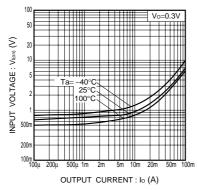


Fig.1 Input voltage vs. output current (ON characteristics)

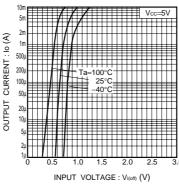


Fig.2 Output current vs. input voltage (OFF characteristics)

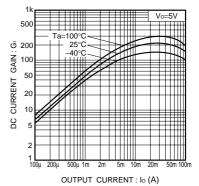


Fig.3 DC current gain vs. output current

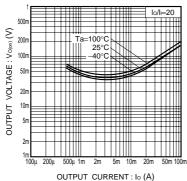


Fig.4 Output voltage vs. output current

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