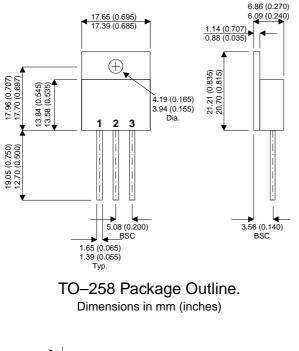
45CKQ100



MECHANICAL DATA Dimensions in mm



 $1 = A_1 \text{ Anode } 1$ 2 = K Cathode $3 = A_2 \text{ Anode } 2$

DUAL SCHOTTKY BARRIER DIODE IN TO-258 HERMETIC METAL PACKAGE FOR HI-REL APPLICATIONS

FEATURES

- SCREENING OPTIONS AVAILABLE
- OUTPUT CURRENT 45A
- LOW V_F
- LOW LEAKAGE

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unle	ss otherwise stated)
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V _{RRM}	DC Reverse Voltage	100V	
V _{RSM}	Peak Non-Repetitive Reverse Voltage	100V	
V _R	Continuous Reverse Voltage	100V	
I _{F(AV)}	Maximum Average Forward Current	45A*	
I _{FSM}	Peak Non-Repetitive Surge Current at 50Hz (per leg)	400A	
T _{STG}	Storage Temperature Range	-55°C to 150°C	
TJ	Maximum Operating Junction Temperature	-55°C to 150°C	

* I_{F(AV)} current is limited by pin diameter

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ELECTRICAL CHARACTERISTICS (T_{CASE} = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit
V _R	Max. DC Reverse Voltage					100	V
V _{RWM}	Max. Working Peak Reverse Voltage					100	-
I _{F(AV)}	Average Forward Current	50% Duty Cycle	T _C =100°C			45	
IFSM	Peak Non-Repetitive Surge Current	T _P =8.3ms Half Si			400	- A	
V _{FM}	Forward Voltage Drop (Per Leg)	I _F =25A	T _J = 25°C			0.89	- V
		I _F =45A	TJ= 25°C			1.13	
		I _F =25A	T _J = 125°C			0.74	
		I _F =45A	T _J = 125°C			0.97	1
I _{RM}	Reverse Leakage Current	V_R = Rated V_R	T _J = 25°C			0.8	mA
			T _J = 125°C			45	
C _T	Junction Capacitance	$V_R = 5V_{DC}$	(1MHz, 25°C)			1400	pF
L _s	Typical Series Inductance	(Anode Lead to Cathode Lead)				8.7	nH
R_{thJC}	Thermal Resistance Junction to Case (Per Leg)	DC Operation				0.83	-°C/W
R_{thJC}	Thermal Resistance Junction to Case (Per Package)	DC Operation			0.42		

*Pulse test tp=300 μ s $\delta \leq 2\%$

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