

MMBT3904LT1

Preferred Device

General Purpose Transistor

NPN Silicon

Features

- Pb-Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	40	Vdc
Collector–Base Voltage	V_{CBO}	60	Vdc
Emitter–Base Voltage	V_{EBO}	6.0	Vdc
Collector Current – Continuous	I_C	200	mAdc

THERMAL CHARACTERISTICS

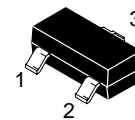
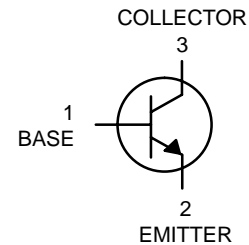
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance Junction–to–Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	–55 to +150	$^\circ\text{C}$

- FR–5 = $1.0 \times 0.75 \times 0.062$ in.
- Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.



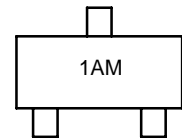
ON Semiconductor®

<http://onsemi.com>



SOT–23 (TO–236)
CASE 318
Style 6

MARKING DIAGRAM



1AM = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping†
MMBT3904LT1	SOT–23	3000 / Tape & Reel
MMBT3904LT1G	SOT–23	3000 / Tape & Reel
MMBT3904LT3	SOT–23	10000 / Tape & Reel
MMBT3904LT3G	SOT–23	10000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage (I _C = 1.0 mAdc, I _B = 0)	V _{(BR)CEO}	40	-	Vdc
Collector-Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)	V _{(BR)CBO}	60	-	Vdc
Emitter-Base Breakdown Voltage (I _E = 10 μAdc, I _C = 0)	V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)	I _{BL}	-	50	nAdc
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)	I _{CEx}	-	50	nAdc

ON CHARACTERISTICS (Note 3)

DC Current Gain (I _C = 0.1 mAdc, V _{CE} = 1.0 Vdc) (I _C = 1.0 mAdc, V _{CE} = 1.0 Vdc) (I _C = 10 mAdc, V _{CE} = 1.0 Vdc) (I _C = 50 mAdc, V _{CE} = 1.0 Vdc) (I _C = 100 mAdc, V _{CE} = 1.0 Vdc)	H _{FE}	40 70 100 60 30	- - 300 - -	-
Collector-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{CE(sat)}	- -	0.2 0.3	Vdc
Base-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{BE(sat)}	0.65 -	0.85 0.95	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current-Gain - Bandwidth Product (I _C = 10 mAdc, V _{CE} = 20 Vdc, f = 100 MHz)	f _T	300	-	MHz
Output Capacitance (V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	4.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	-	8.0	pF
Input Impedance (V _{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{ie}	1.0	10	k ohms
Voltage Feedback Ratio (V _{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{re}	0.5	8.0	X 10 ⁻⁴
Small-Signal Current Gain (V _{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{fe}	100	400	-
Output Admittance (V _{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{oe}	1.0	40	μmhos
Noise Figure (V _{CE} = 5.0 Vdc, I _C = 100 μAdc, R _S = 1.0 k ohms, f = 1.0 kHz)	NF	-	5.0	dB

SWITCHING CHARACTERISTICS

Delay Time	(V _{CC} = 3.0 Vdc, V _{BE} = -0.5 Vdc, I _C = 10 mAdc, I _{B1} = 1.0 mAdc)	t _d	-	35	ns
Rise Time		t _r	-	35	
Storage Time	(V _{CC} = 3.0 Vdc, I _C = 10 mAdc, I _{B1} = I _{B2} = 1.0 mAdc)	t _s	-	200	ns
Fall Time		t _f	-	50	

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

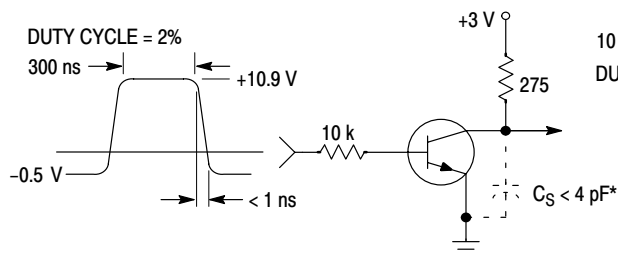


Figure 1. Delay and Rise Time Equivalent Test Circuit

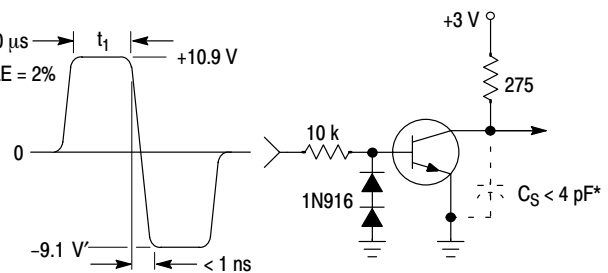


Figure 2. Storage and Fall Time Equivalent Test Circuit

* Total shunt capacitance of test jig and connectors

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TYPICAL TRANSIENT CHARACTERISTICS

— $T_J = 25^\circ\text{C}$
 - - - $T_J = 125^\circ\text{C}$

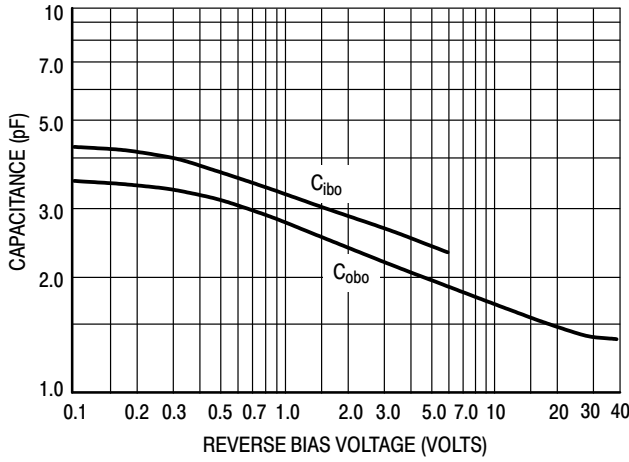


Figure 3. Capacitance

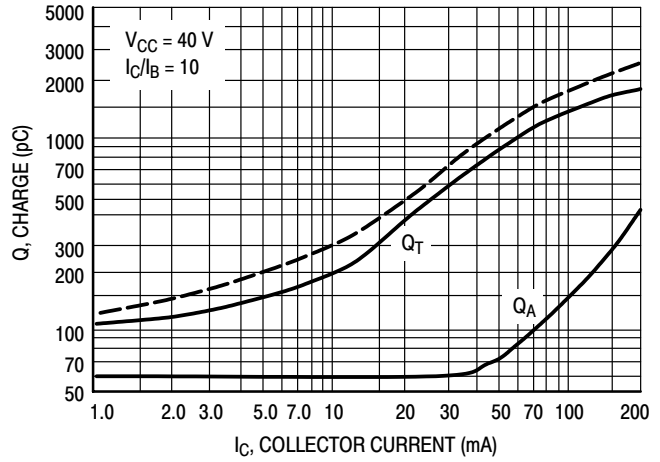


Figure 4. Charge Data

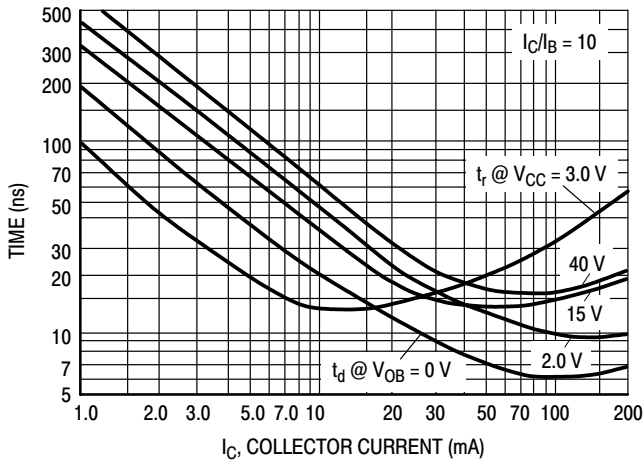


Figure 5. Turn-On Time

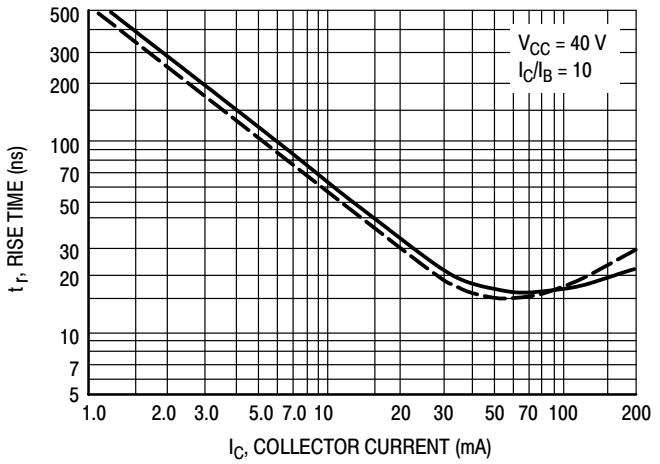


Figure 6. Rise Time

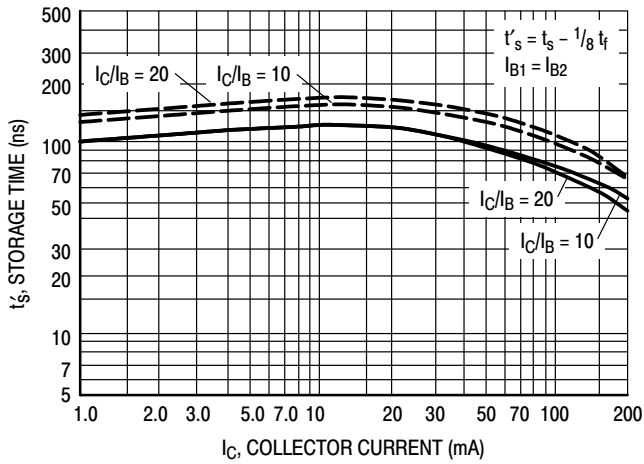


Figure 7. Storage Time

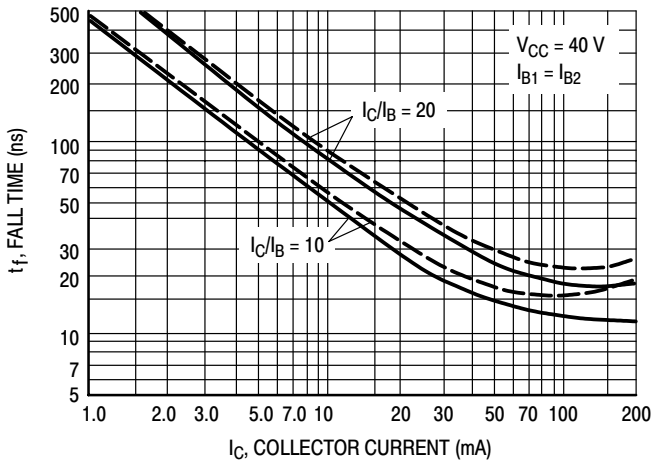


Figure 8. Fall Time

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TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

($V_{CE} = 5.0$ Vdc, $T_A = 25^\circ\text{C}$, Bandwidth = 1.0 Hz)

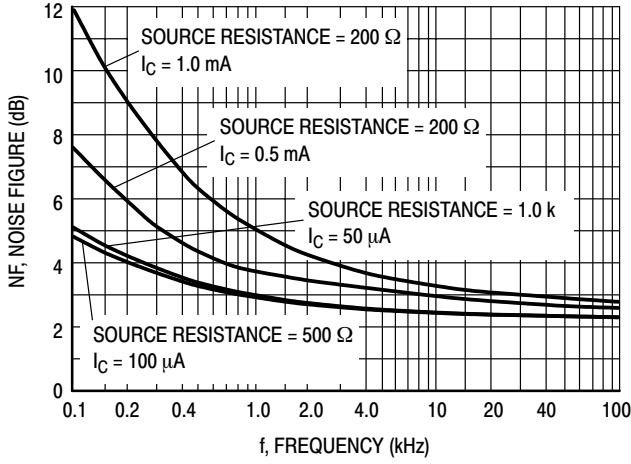


Figure 9.

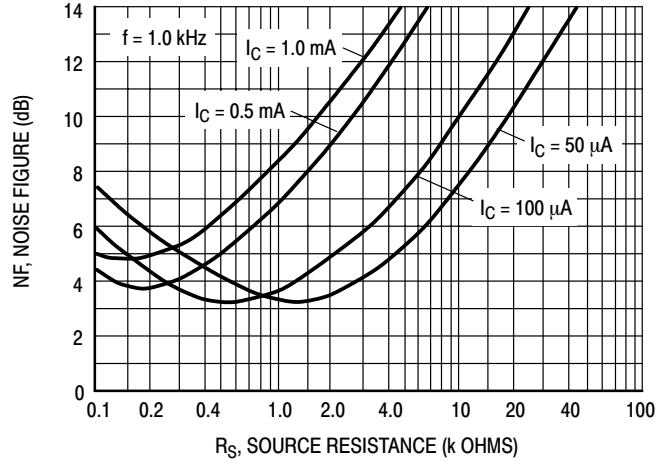


Figure 10.

h PARAMETERS

($V_{CE} = 10$ Vdc, $f = 1.0$ kHz, $T_A = 25^\circ\text{C}$)

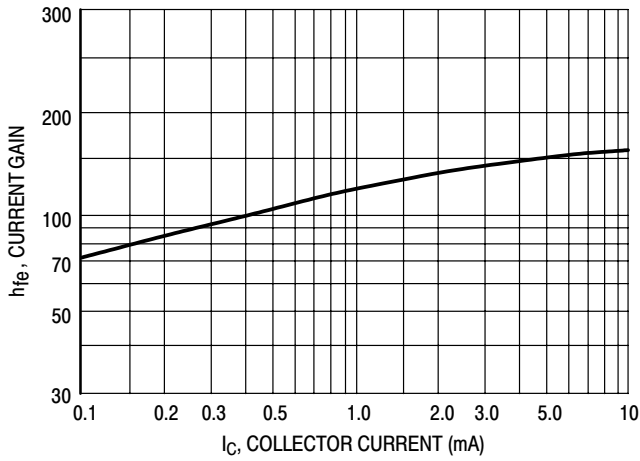


Figure 11. Current Gain

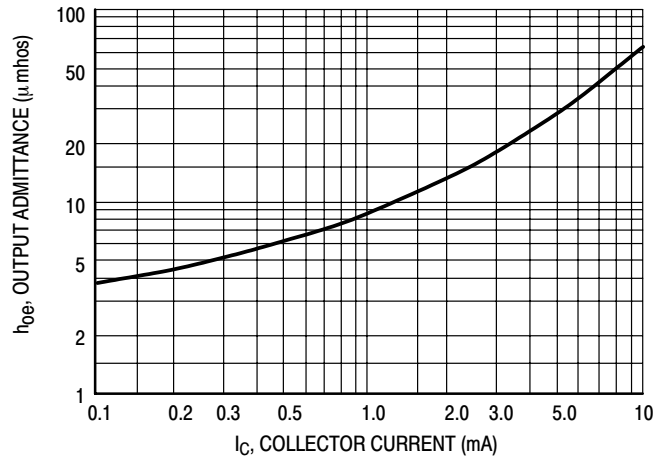


Figure 12. Output Admittance

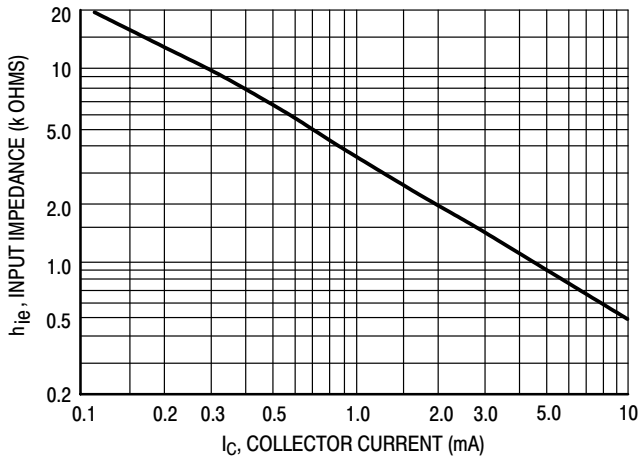


Figure 13. Input Impedance

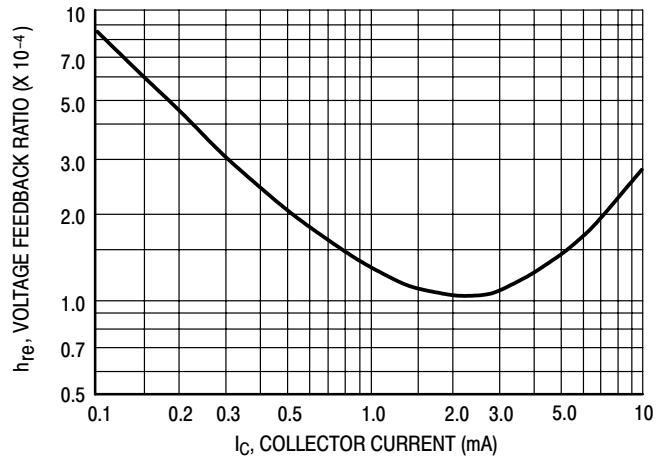


Figure 14. Voltage Feedback Ratio

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TYPICAL STATIC CHARACTERISTICS

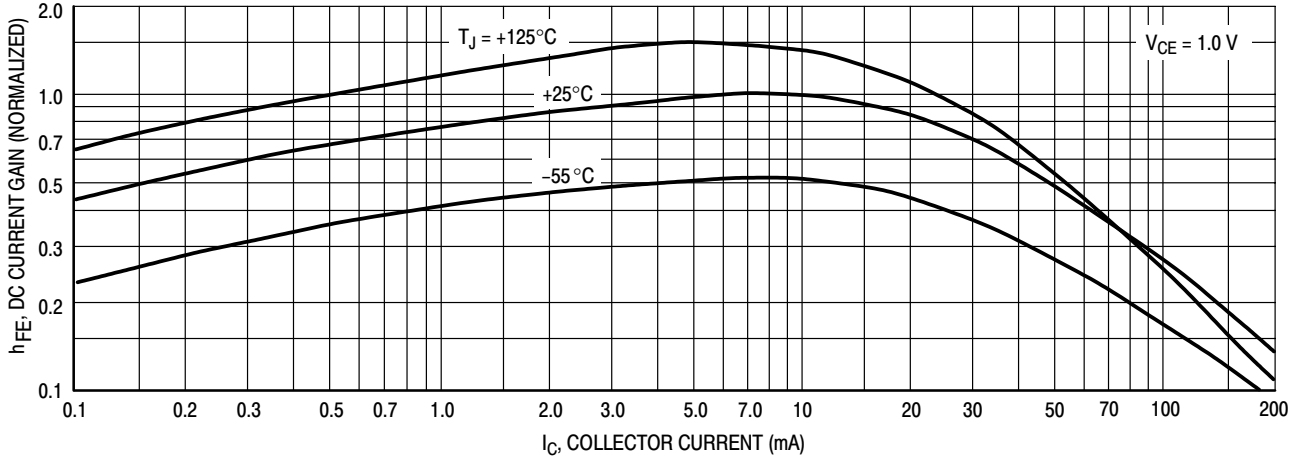


Figure 15. DC Current Gain

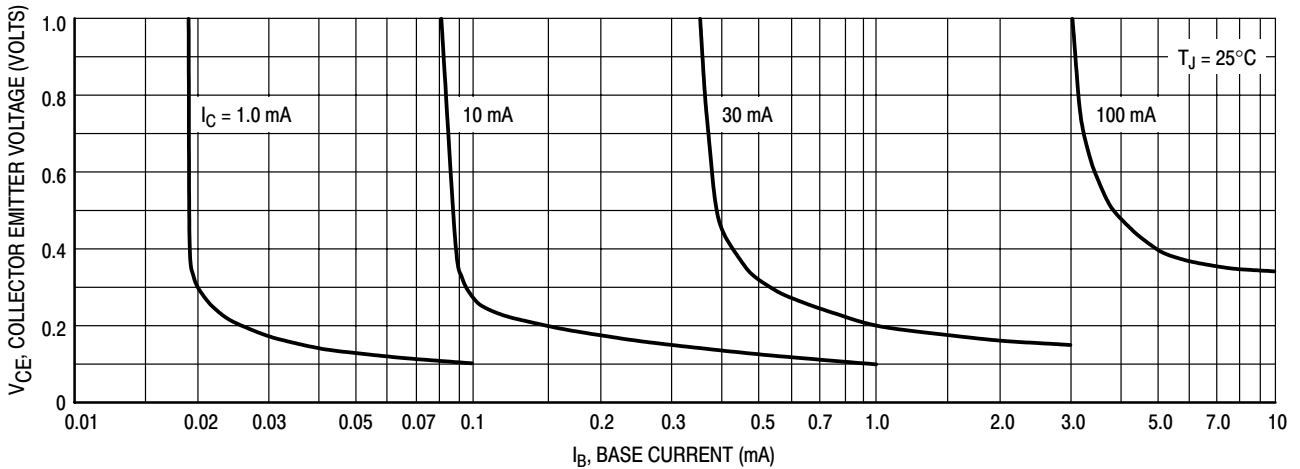


Figure 16. Collector Saturation Region

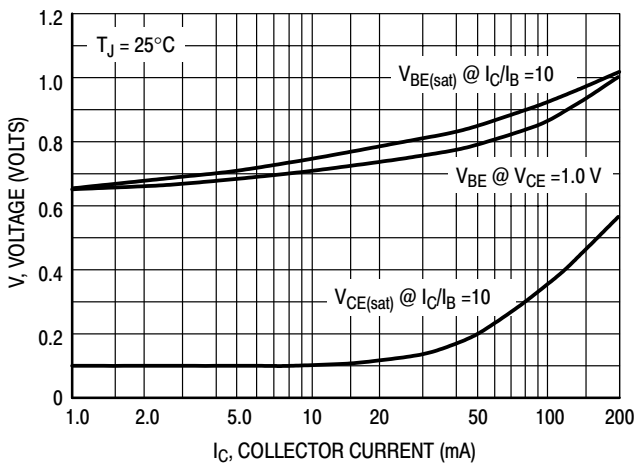


Figure 17. "ON" Voltages

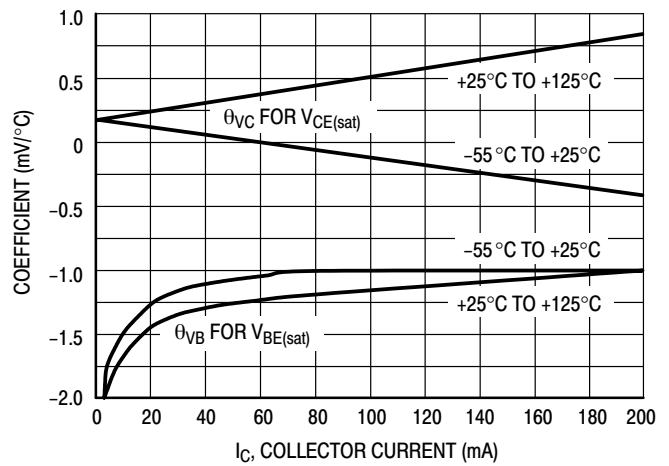


Figure 18. Temperature Coefficients

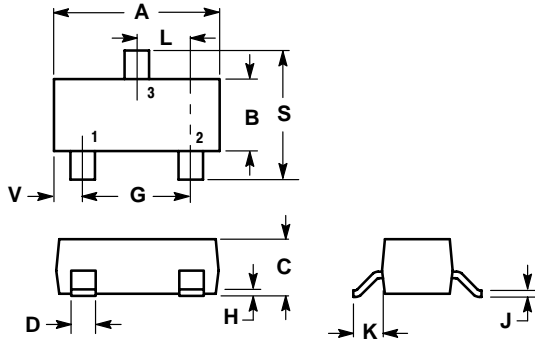
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PACKAGE DIMENSIONS

SOT-23 (TO-236)
CASE 318-08
ISSUE AH

NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318-03 AND -07 OBSOLETE, NEW STANDARD 318-08.



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

STYLE 6:

- PIN 1. BASE
- EMITTER
- COLLECTOR

SOLDERING FOOTPRINT*

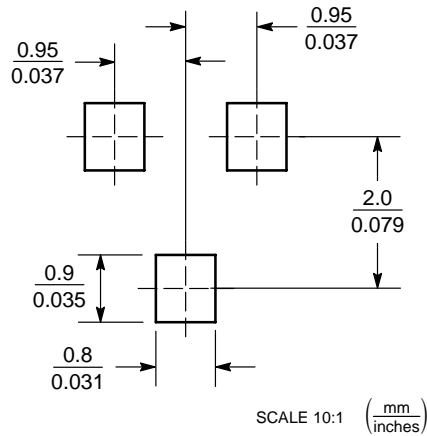


Figure 19. SOT-23

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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