National Semiconductor

June 1989

54125/DM54125/DM74125 Quad TRI-STATE Buffers

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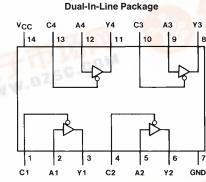
General Description

This device contains four independent gates each of which performs a non-inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard TTL output with additional drive capability at the high Logic level to permit the driving of bus lines without external pull-up resistors. When disabled, both the output transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Features

 Alternate Military/Aerospace device (54125) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.

Connection Diagram



Order Number 54125DMQB, 54125FMQB, DM54125J, DM54125W or DM74125N See NS Package Number J14A, N14A or W14B

Function Table

	$\mathbf{Y} = \mathbf{Z}$	1		
Inp	outs	Output		
А	с	Y		
L	L	L		
н	L	Н		
Х	Н	Hi-Z		

H = High Logic Level

- L = Low Logic Level
- X = Either Low or High Logic Level
- Hi-Z = TRI-STATE (Outputs are disabled)

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Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM54 and 54	-55°C to +125°C
DM74	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54125		DM74125			Units	
oyinibol		Min	Nom	Max	Min	Nom	Max	onno
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
VIL	Low Level Input Voltage			0.8			0.8	V
IOH	High Level Output Current			-2			-5.2	mA
IOL	Low Level Output Current			16			16	mA
Τ _Α	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics

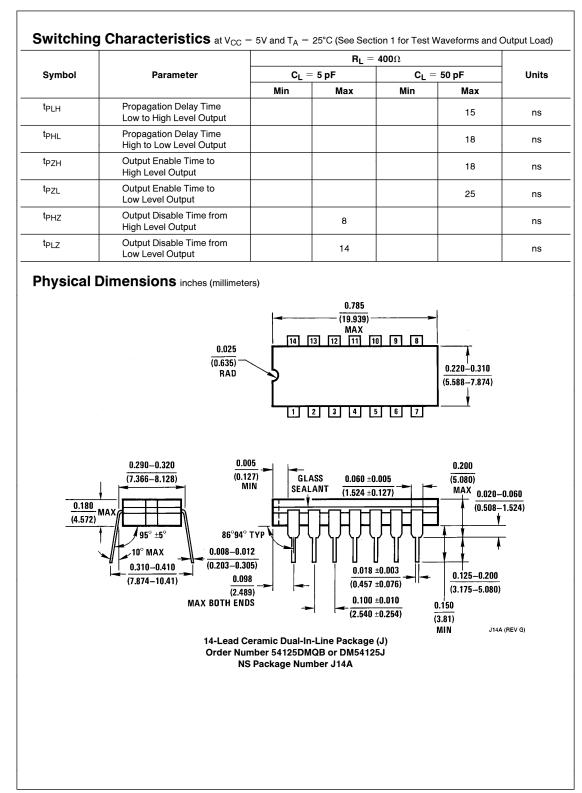
over recommended operating free air temperature range (unless otherwise noted)

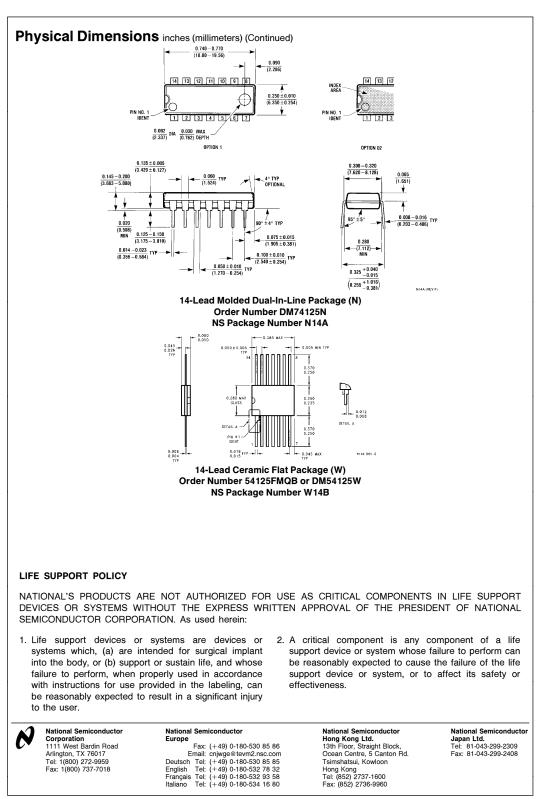
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Мах	Units
VI	Input Clamp Voltage	$V_{CC} = Min$, $I_{I} = -12 \text{ mA}$				-1.5	V
V _{OH}	High Level Output Voltage	$\label{eq:V_CC} \begin{array}{l} V_{CC} = \text{Min}, \text{I}_{OH} = \text{Max} \\ V_{IL} = \text{Max}, \text{V}_{IH} = \text{Min} \end{array}$		2.4	3.3		V
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.2	0.4	V
lı	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.6	mA
I _{IZL}	Off-State Input Current with Low Level Input Voltage Applied	$V_{CC} = Max, V_I = 0.4V$				-40	μΑ
I _{OZH}	Off-State Output Current with High Level Output Voltage Applied	$\label{eq:V_CC} \begin{array}{l} V_{CC} = Max, V_O = 2.4V \\ V_{IH} = Min, V_{IL} = Max \end{array}$				40	μΑ
I _{OZL}	Off-State Output Current with Low Level Output Voltage Applied	$\label{eq:VCC} \begin{array}{l} V_{CC} = Max, V_O = 0.4V \\ V_{IH} = Min, V_{IL} = Max \end{array}$				-40	μΑ
I _{OS}	Short Circuit V _{CC} = Max Output Current (Note 2)	V _{CC} = Max	DM54	-30		-70	mA
		(Note 2)	DM74	-28		-70	
ICC	Supply Current	V _{CC} = Max (Note 3)			36	54	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with the output control (C) inputs at 4.5V, the data inputs grounded, and the outputs open.





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