National Semiconductor

## June 1989

with

Preset, Clear and Complementary Outputs

/DM5474/DM7474 Dual Positive-Edge-Triggered D Flip-Flops

# 5474/DM5474/DM7474 Dual Positive-Edge-Triggered D Flip-Flops with Preset, Clear and Complementary Outputs

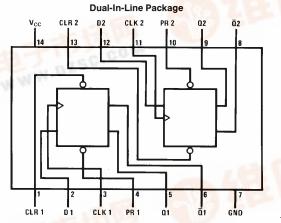
## **General Description**

This device contains two independent positive-edge-triggered D flip-flops with complementary outputs. The information on the D input is accepted by the flip-flops on the positive going edge of the clock pulse. The triggering occurs at a voltage level and is not directly related to the transition time of the rising edge of the clock. The data on the D input may be changed while the clock is low or high without affecting the outputs as long as the data setup and hold times are not violated. A low logic level on the preset or clear inputs will set or reset the outputs regardless of the logic levels of the other inputs.

#### **Features**

 Alternate Military/Aerospace device (5474) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.





TL/F/6526-1 Order Number 5474DMQB, 5474FMQB, DM5474J, DM5474W, DM7474M or DM7474N See NS Package Number J14A, M14A, N14A or W14B

## Function Table

	Inpu	Outputs			
PR	CLR	CLK	D	Q	Q
L	Н	х	X	н	L
н	L	Х	X	L	Н
L	L	Х	X	H*	H*
н	Н	Î Î	н	Н	L
н	н	1	L	L	н
Н	Н	L	X	Q <sub>0</sub>	Q <sub>0</sub>

H = High Logic Level X = Either Low or High Logic Level

Low Logic Level

 $\uparrow$  = Positive-going transition of the clock.

\* = This configuration is nonstable; that is, it will not persist when either the preset and/or clear

inputs return to their inactive (high) level.

 $Q_0$  = The output logic level of Q before the indicated input conditions were established.

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WWW.DT



#### Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM54 and 54	-55°C to +125°C
DM74	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Recommended Operating Conditions**

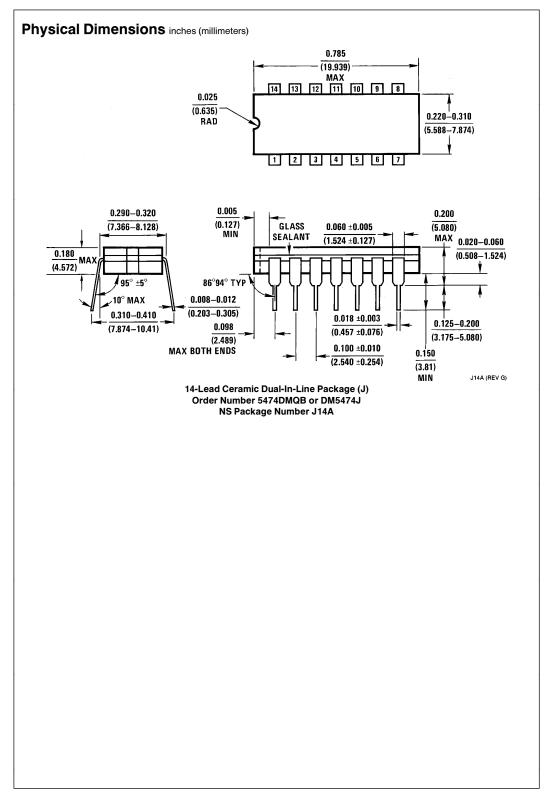
Symbol	Parameter			DM5474			DM7474			Uni
Symbol	Parameter		Ν	lin	Nom	Max	Min	Nom	Max	
Vcc	Supply Voltage	Supply Voltage		1.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High Level Input Voltage			2			2			V
V <sub>IL</sub>	Low Level Input Voltage					0.8			0.8	V
ОН	High Level Output Current					-0.4			-0.4	m/
OL	Low Level Output	t Current				16			16	m/
CLK	Clock Frequency	(Note 2)		0		15	0		15	MH
t <sub>W</sub>	Pulse Width	Clock Hi	igh 3	30			30			
	(Note 2)	Clock Lo	ow 3	37			37			ns
		Clear Lo	w 3	30			30			
		Preset L	.ow 3	30			30			
su	Input Setup Time	(Notes 1 &	2) 20	¢ר			20 ↑			ns
H	Input Hold Time (	(Notes 1 & 2	2) 5	1			5↑			ns
Г <sub>А</sub>	Free Air Operatin	ng Temperat	ture –	-55		125	0		70	°C
Electri	cal Characte	eristics	over recomm	nende	d operating	free air tem			ess otherwis	e noted)
Symbol	Paramete			nende nditio		free air tem Min	Т	ур	ess otherwis	e noted) Unit
		er		nditio	ns		Т			
Symbol	Paramete	er oltage	$Cor$ $V_{CC} = Min$ $V_{CC} = Min$	nditio 1, I <sub>I</sub> = 1, I <sub>OH</sub>	ns 12 mA = Max		T (No	ур	Max	Unit
<b>Symbol</b> V <sub>I</sub>	Paramete Input Clamp Vo High Level Out	er oltage tput	Cor V <sub>CC</sub> = Min	nditio n, I <sub>I</sub> = n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub>	ns - 12 mA = Max = Min = Max	Min	(No	yp te 3)	Max	Unit
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub>	Paramete Input Clamp Vo High Level Out Voltage Low Level Out	er oltage tput	$\label{eq:VCC} \begin{array}{c} \text{Cor} \\ \text{V}_{\text{CC}} = \text{Min} \\ \text{V}_{\text{CC}} = \text{Min} \\ \text{V}_{\text{IL}} = \text{Max}, \\ \text{V}_{\text{CC}} = \text{Min} \end{array}$	nditio n, I <sub>I</sub> = n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> =	ns = Max = Min = Max = Max = Max	Min	(No	yp te 3)	<b>Max</b> 1.5	Units V V
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub>	Paramete Input Clamp Ve High Level Out Voltage Low Level Out Voltage Input Current @ Input Voltage High Level Inp	er oltage tput @ Max	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{V}_{CC} = \mbox{Max} \end{array}$	nditio n, I <sub>1</sub> == n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> =	ns = Max = Min = Max = Max = Max	Min	(No	yp te 3)	<b>Max</b> -1.5 0.4	Unit       V       V       V       V
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub>	Paramete Input Clamp Vo High Level Out Voltage Low Level Out Voltage Input Current ( Input Voltage	er oltage tput @ Max	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max}, \end{array}$	nditio n, I <sub>1</sub> == n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> =	ns = 12 mA = Max = Min = Max = Max = 5.5V	Min	(No	yp te 3)	Max 1.5 0.4 1	Unit: V V MA
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub>	Paramete Input Clamp Ve High Level Out Voltage Low Level Out Voltage Input Current @ Input Voltage High Level Inp	er oltage tput @ Max	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{V}_{CC} = \mbox{Max} \end{array}$	nditio n, I <sub>1</sub> == n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> =	ns 12 mA =- Max =- Max =- Max =- Max =- 5.5V D	Min	(No	yp te 3)	Max 1.5 0.4 1 40	Unit       V       V       V       V
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub>	Paramete Input Clamp Ve High Level Out Voltage Low Level Out Voltage Input Current @ Input Voltage High Level Inp	er oltage tput @ Max	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{V}_{CC} = \mbox{Max} \end{array}$	nditio n, I <sub>1</sub> == n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> =	ns 12 mA =- Max =- Max Max Max Max S.5V D Clock	Min	(No	yp te 3)	Max -1.5 0.4 1 40 80	Unit: V V MA
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub> I <sub>I</sub> I <sub>IH</sub>	Paramete           Input Clamp Voltage           High Level Out           Voltage           Low Level Out           Voltage           Input Current @           Input Voltage           High Level Inp           Current           Low Level Input	er oltage tput @ Max ut	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \end{array}$	nditio n, I <sub>I</sub> = n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> = x	ns 12 mA =- Max =- Max Max Max Max S.5V D Clock Clear	Min	(No	yp te 3)	Max -1.5 0.4 1 40 80 120	Unit: V V MA
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub> I <sub>I</sub> I <sub>IH</sub>	Paramete Input Clamp Vo High Level Out Voltage Low Level Out Voltage Input Current ( Input Voltage High Level Inp Current	er oltage tput @ Max ut	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ 2.4$	nditio n, I <sub>I</sub> = n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> = x	ns - 12 mA = Max = Min = Max = Max = 5.5V D Clock Clear Preset	Min	(No	yp te 3)	Max -1.5 0.4 1 40 80 120 40	Unit:
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub> I <sub>I</sub> I <sub>IH</sub>	Paramete           Input Clamp Voltage           High Level Out           Voltage           Low Level Out           Voltage           Input Current @           Input Voltage           High Level Inp           Current           Low Level Input	er oltage tput @ Max ut	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \end{array}$	nditio n, I <sub>I</sub> = n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> = x	ns - 12 mA = Max = Min = Max = Max = 5.5V D Clock Clear Preset D	Min	(No	yp te 3)	Max -1.5 0.4 1 40 80 120 40 -1.6	Unit: V V MA
Symbol V <sub>I</sub> V <sub>OH</sub> V <sub>OL</sub> I <sub>I</sub> I <sub>IH</sub>	Paramete           Input Clamp Voltage           High Level Out           Voltage           Low Level Out           Voltage           Input Current @           Input Voltage           High Level Inp           Current           Low Level Input	er oltage tput @ Max ut	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ 2.4$	nditio n, I <sub>I</sub> = n, I <sub>OH</sub> , V <sub>IH</sub> = n, I <sub>OL</sub> V <sub>IL</sub> = x, V <sub>I</sub> = x	ns - 12 mA = Max = Min = Max = Max = 5.5V D Clock Clear Preset D Clock	Min	(No	yp te 3)	Max -1.5 0.4 1 40 80 120 40 -1.6 -3.2	Unit:
Symbol V <sub>1</sub> V <sub>OH</sub> V <sub>OL</sub>	Paramete           Input Clamp Voltage           High Level Out           Voltage           Low Level Out           Voltage           Input Current @           Input Voltage           High Level Input Current           Low Level Input Current           Low Level Input Current           Short Circuit	er oltage tput @ Max ut ut	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{IC} = \mbox{Max} \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{(Note 6)} \\ \mbox{V}_{CC} = \mbox{Max} \\ \mbox{Max} \m$	nditio i, l <sub>1</sub> = i, l <sub>0</sub> H i, l <sub>0</sub> H i, l <sub>0</sub> H v <sub>II</sub> = x, V <sub>I</sub> = x	ns - 12 mA = Max = Min = Max = Max = 5.5V D Clock Clear Preset D Clock Clear	Min	(No	yp te 3)	Max -1.5 0.4 1 40 80 120 40 -1.6 -3.2 -3.2	Unit:
Symbol V <sub>1</sub> V <sub>OH</sub> V <sub>OL</sub> I <sub>1</sub> I <sub>1</sub> I <sub>1</sub> H	Parameter           Input Clamp Voltage           High Level Out Voltage           Low Level Out Voltage           Input Current @ Input Voltage           High Level Input Current           Low Level Input Current	er oltage tput @ Max ut ut	$\label{eq:constraint} \begin{array}{c} \mbox{Cor} \\ \mbox{V}_{CC} = \mbox{Min} \\ \mbox{V}_{IL} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Min}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{CC} = \mbox{Max}, \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{I} = \mbox{2.4V} \\ \mbox{V}_{I} = \mbox{0.4V}, \\ \mbox{(Note 6)} \end{array}$	nditio i, l <sub>1</sub> = i, l <sub>0</sub> H i, l <sub>0</sub> H i, l <sub>0</sub> H v <sub>II</sub> = x, V <sub>I</sub> = x	ns - 12 mA = Max = Min = Max = 5.5V D Clock Clear Preset D Clock Clear Preset	Min 2.4	(No	yp te 3)	Max -1.5 0.4 1 40 80 120 40 -1.6 -3.2 -3.2 -1.6	Unit:

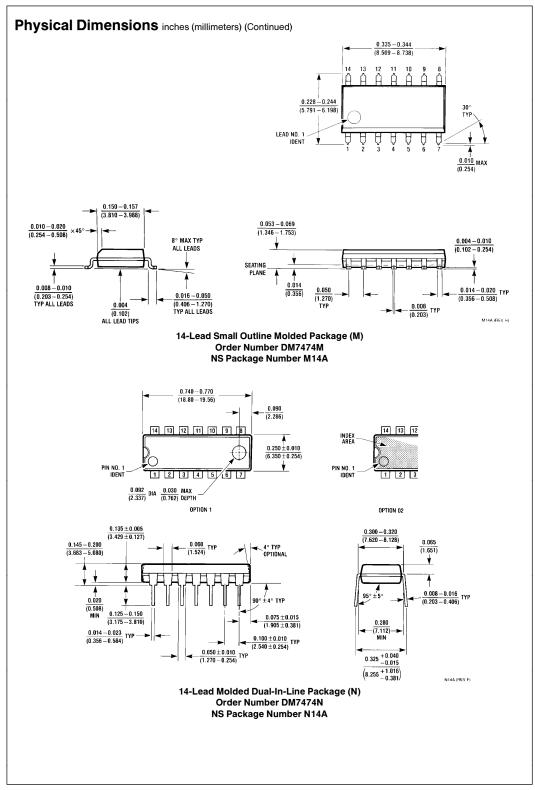
**Note 4:** Not more than one output should be shorted at a time.

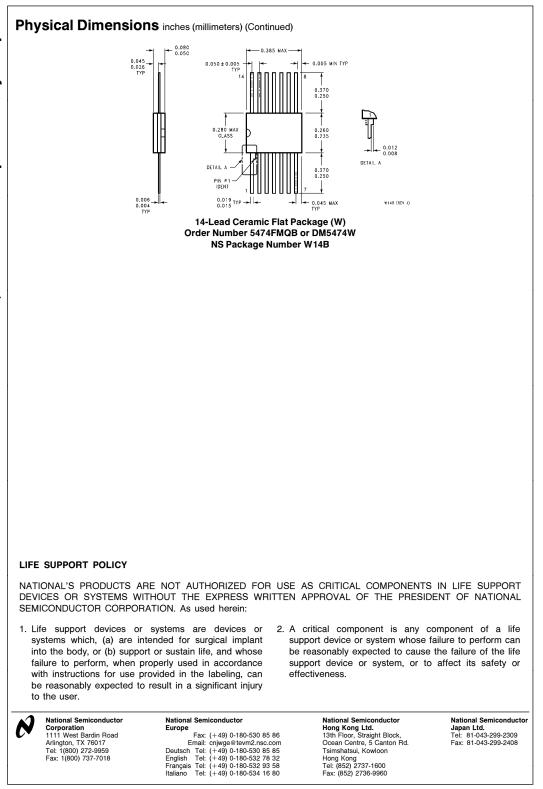
Note 5: With all outputs open, I<sub>CC</sub> is measured with the Q and Q outputs high in turn. At the time of measurement the clock is grounded.

Note 6: Clear is tested with preset high and preset is tested with clear high.

Symbol	Parameter	From (Input) To (Output)	R <sub>L</sub> = C <sub>L</sub> =	Units	
		i o (output)	Min	Max	
f <sub>MAX</sub>	Maximum Clock Frequency		15		MHz
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Preset to Q		40	ns
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Preset to Q		25	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Clear to Q		40	ns
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Clear to Q		25	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Clock to $Q$ or $\overline{Q}$		40	ns
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Clock to Q or Q		25	ns







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